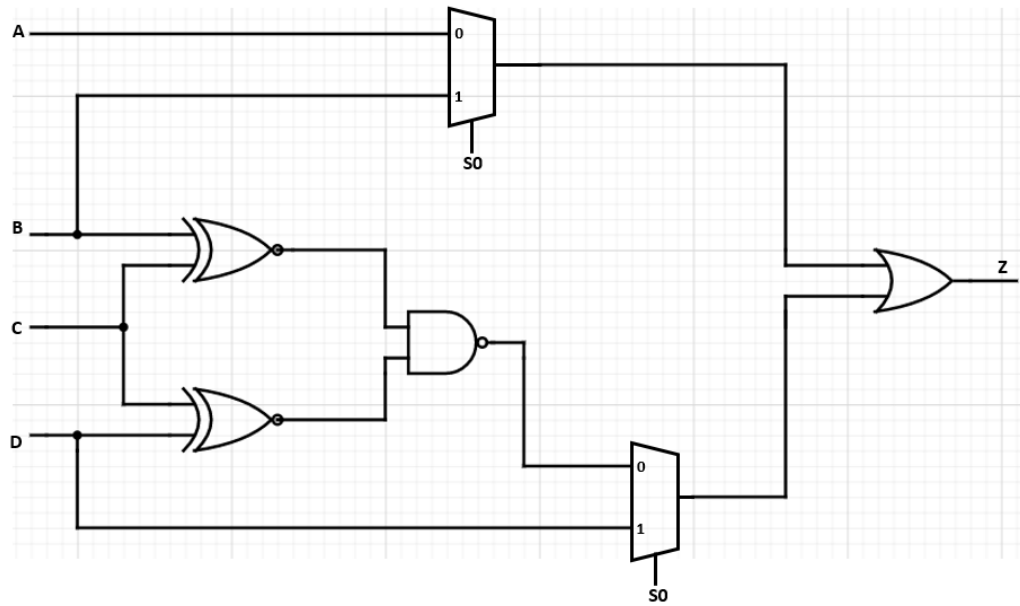


2. For the circuit diagram below, fill the truth table.

(10)



| A | B | C | D | S0 | Z |
|----------|----------|----------|----------|-----------|----------|
| 0 | 0 | 1 | 1 | 0 | |
| 1 | 0 | 0 | 0 | 1 | |
| 0 | 1 | 0 | 1 | 0 | |
| 1 | 0 | 1 | 0 | 0 | |
| 0 | 1 | 1 | 0 | 1 | |
| 0 | 0 | 0 | 0 | 1 | |
| 1 | 1 | 1 | 1 | 0 | |
| 0 | 0 | 0 | 1 | 0 | |
| 1 | 0 | 0 | 1 | 1 | |
| 1 | 1 | 1 | 0 | 1 | |