Q4. A 4-LUT is implemented using 3-LUTs and a set of 2x1 MUXs as shown in figure. If a bit file comprising of bit pattern 0xFFFE (A$_{15}$ to A$_0$ addresses in hex) is transferred, what logic function is implemented by the circuit at Y output. A single event upset (SEU) error caused to flip A0 and A15 bits. How does it affect the output?   [6]