

[Aim](#)

[Theory](#)

[Pre Test](#)

[Procedure](#)

[Simulation](#)

[Post Test](#)

[References](#)

[Feedback](#)

Verification and interpretation of truth table for  
AND, OR, NOT, NAND, NOR, Ex-OR, Ex-NOR  
gates.

### Pre Test

Electronic circuits that operate on one or more input signals to produce  
standard output \_\_\_\_\_

- ☐ a : Series circuits
- ☐ b : Parallel circuits
- ☐ c : Logic signals
- ☒ d : Logic gates

A \_\_\_\_\_ gate gives the output as 1 only if all the inputs signals are  
1.

- ☒ a : AND
- ☐ b : OR
- ☐ c : NOR
- ☐ d : Ex-OR

The boolean expression of an OR gate is \_\_\_\_\_

- ☐ a : A.B
- ☐ b :  $A'B+AB'$
- ☒ c : A+B
- ☐ d :  $AB'$

The gate which is used to reverse the output obtained is \_\_\_\_\_

- ☐ a : NOR
- ☐ b : NAND
- ☐ c : EX-OR
- ☒ d : NOT

Which of the following gate will returns a 1 only if both the inputs are  
0?

- ☐ a : AND
- ☐ b : OR
- ☒ c : NAND
- ☐ d : EX-OR

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