## UG 1 – Lab Schedule – Spring 2012

	Data Structures 2:00-5:00 PM	Basic Electronic Circuits 2:00-5:00 PM	Computer System Organization 2:00-5:00 PM	IT Workshop 2 2:00-5:00 PM	Electronic Workshop-1
Mon	T5, T6 – TL1 T7, T8 – TL2	T3, T4 – BEC Lab			
Tue		T1, T2 – BEC Lab		T4, T5 – TL1 T8 – TL2	T6, T7 – 131 2:00-4:00 PM
Wed				T1, T2 – TL1 T3 – TL2	
Thu			T4, T5, UG2(EHD)-TL1 T6, T7, UG2(CND)-TL2 For all above sections 2:00-2:30PM class will be held in SH1		T6, T7 – 131 11:00-1:00 PM
Fri			T1,T2 – TL1 T3,UG2(CLD) - TL2 For all above sections 2:00-2:30PM class will be held in SH1		
Sat	T1, T2 – TL1 T3, T4 – TL2	T5, T6, T7 – BEC Lab			

DS – T1, T2, T3, T4, T5, T6, T7, T8 – All UG1 BEC – T1, T2, T3, T5, T6, T7 – UG1 – CSE/D, ECE/D CSO – T1, T2, T3, T5, T6, T7 – UG1 – CSE/D, ECE/D and UG2 – CND, CLD, EHD EW1 – T6, T7 – UG1 ECE/D