Bank	Address	Name	Bit	Attribute	Value (HEX)	Value (DEC)	Comment
0	0	PID[7:0]	[7:0]	R	0x13	19	Product ID[7:0] Only metal option
0	1	VID[7:0]	[7:0]	R	0x00	0	Version ID[7:0] Only metal option
0	2	slave_id[6:0]	[6:0]	R	0x20	32	I2C slave ID
0	3	Reg_regreset	[0]	R/W	0x00	0	
0	9	wp_code[7:0]	[7:0]	R/W	0x00	0	Register protection , 當wp_code[7:0] = 0x5A時,其它暫存器才可寫入
0	18	Reg_DigNp[7:0]	[7:0]	R/W	0x92	146	PCLK = Sys_ck /Reg_DigNp = 17.5Mhz / 1170 = 14.95Khz
0	19	Reg_DigNp[11:8]	[3:0]	R/W	0x04	4	PCLK = Sys_ck /Reg_DigNp = 17.5Mhz / 1170 = 14.95Khz
0	20	Reg_SysNp[7:0]	[7:0]	R/W	0x04	4	Sys_ck = 70Mhz/Reg_SysNp = 70Mhz / 4 = 17.5Mhz
0	21	Reg_SPI_MsNp[7:0]	[7:0]	R/W	0x41	65	SPI_SCK = Sys_ck / SPI_MsNp = 17.5Mhz / (Reg_DigNp*18) = 17.5Mhz / 65 = 269Khz
0	25	Reg_PCLKMask_enh	[2]	R/W	0x00	0	Mask PCLK clock in Vsync or Hsync period
0	25	Reg_SPICSMode	[3]	R/W	0x00	0	0: Toggle the CS Pin on each pixel data;1: Toggle the CS Pin when changing frame
0	28	Reg_Hstart_PreSync[4:0]	[4:0]	R/W	0x00	0	w:Reg_Hstart_PreSync; r:Reg_Hstart_PreSync_sync
0	30	Reg_Vstart_PreSync[4:0]	[4:0]	R/W	0x00	0	w:Reg_Vstart_PreSync; r:Reg_Vstart_PreSync_sync
0	32	Reg_Hend_PreSync[4:0]	[4:0]	R/W	0x15	21	w:Reg_Hend_PreSync; r:Reg_Hend_PreSync_sync
0	34	Reg_Vend_PreSync[4:0]	[4:0]	R/W	0x15	21	w:Reg_Vend_PreSync; r:Reg_Vend_PreSync_sync
0	35	Reg_CRstart_PreSync[4:0]	[4:0]	R/W	0x00	0	start position of Calibreation row
0	36	Reg_CRend_PreSync[4:0]	[4:0]	R/W	0x11	17	end position of Calibreation row
0	38	Reg_SYNC_length[4:0]	[4:0]	R/W	0x00	0	
0	40	Reg_PD	[6]	R/W	0x00	0	Software Power down contol by Register : High Active
0	90	Reg_T_ANA_EN	[6]	R/W	0x00	0	Analog output enable,"0": disable,"1": enable
0	91	Reg_T_EXT_VREF_sel[1:0]	[1:0]	R/W	0x00	0	External ADC VREF ratio selection ("0":1x,"1":0.75x,"2":0.5x,"3":0.25x)
0	93	Reg_T_ipix_s[5:0]	[5:0]	R/W	0x09	9	pixel current selection,"0": 1uA,"1":2uA,.....,"63":64uA,default is 10uA
0	94	Reg_T_ADC_VREF_S	[4]	R/W	0x00	0	ADC reference selection,"0": internal,"1": external
0	96	Reg_T_PGA_GAIN[2:0]	[2:0]	R/W	0x00	0	CADC PGA gain,"0": 1x,"1":2x, ..., "7":8x