

## FPGA test report (09041300)

I2C **default** setting:

Addr=20, data=4 (Sys\_ck =17.5MHz)

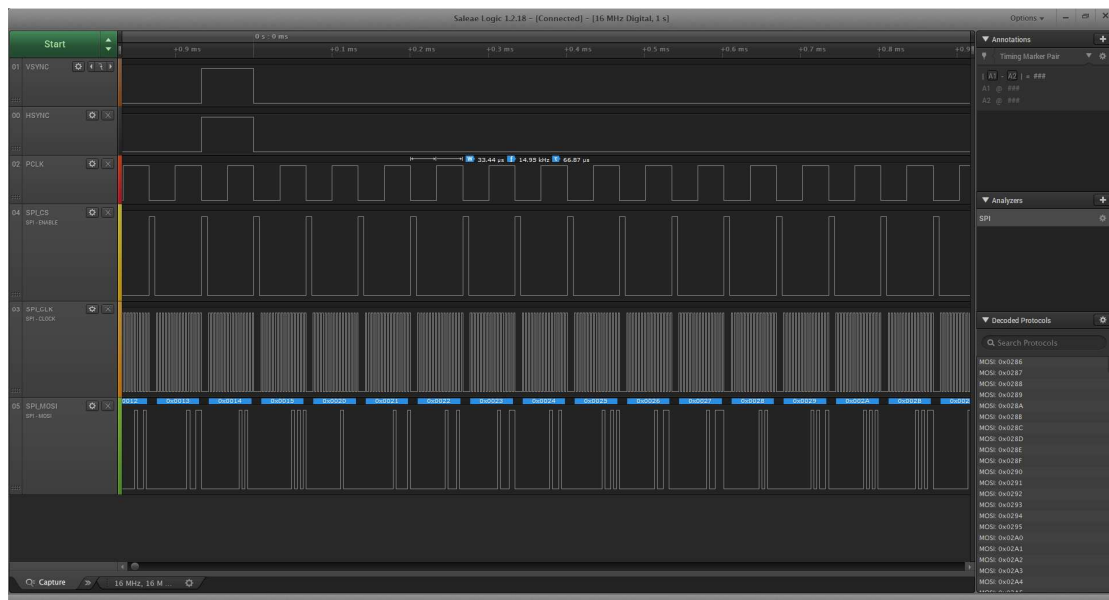
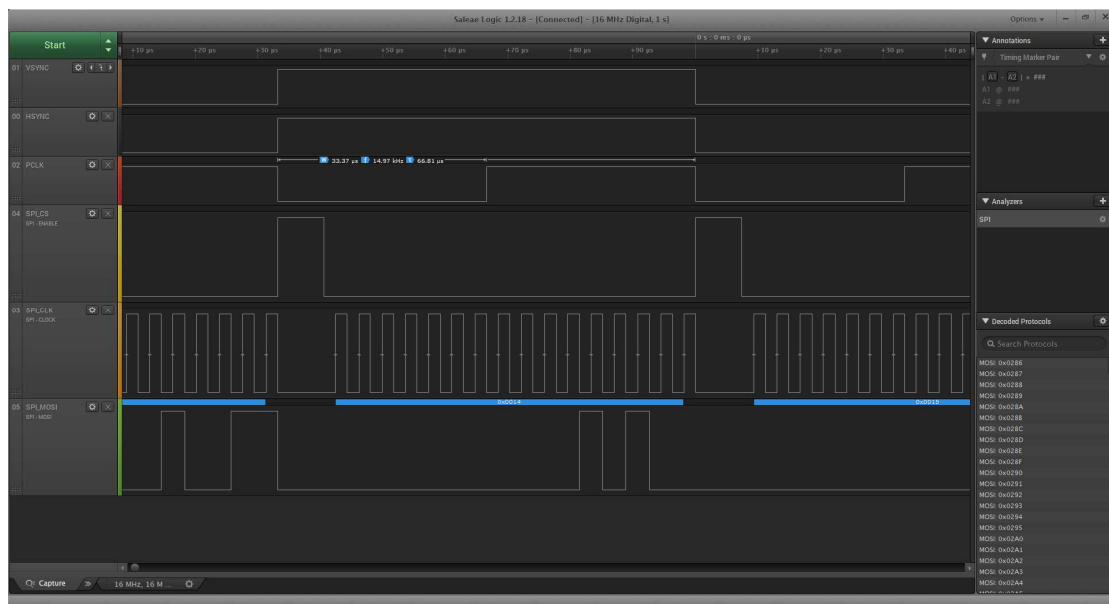
Addr=18, data=146 (Reg\_DigNp =1170)

Addr=19, data=4

Addr=21, data=65 (SPI\_SCK =270KHz)

PCLKMask=0

SPICSMODE=0



I2C setting:

Addr=20, data=4 (Sys\_ck =17.5MHz)

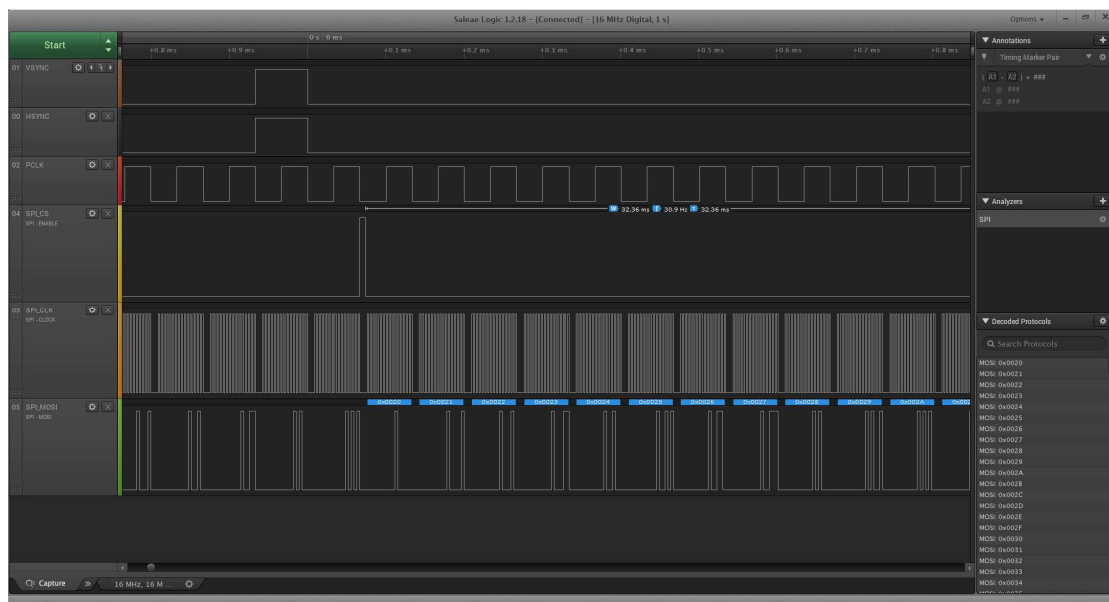
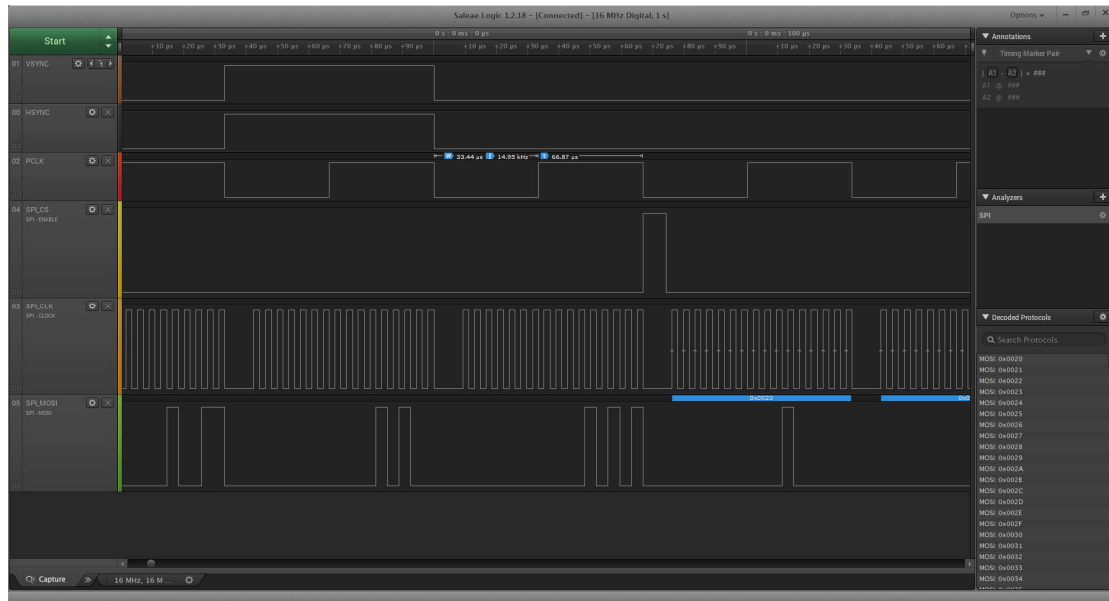
Addr=18, data=146 (Reg\_DigNp =1170)

Addr=19, data=4

Addr=21, data=65 (SPI\_SCK =270KHz)

PCLKMask=0

SPICSMODE=1



I2C setting:

Addr=20, data=4 (Sys\_ck =17.5MHz)

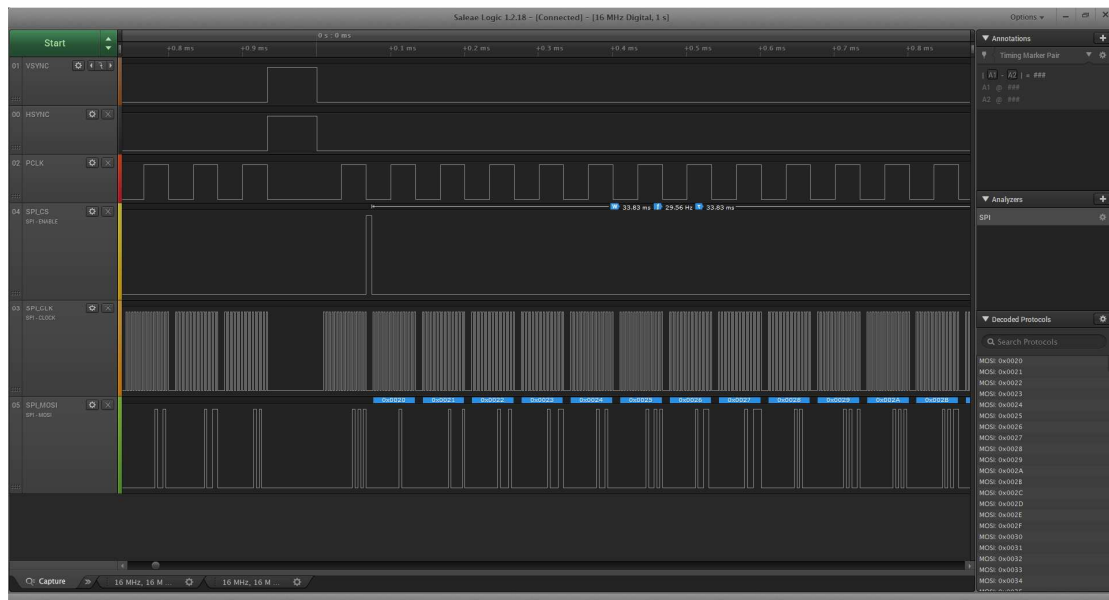
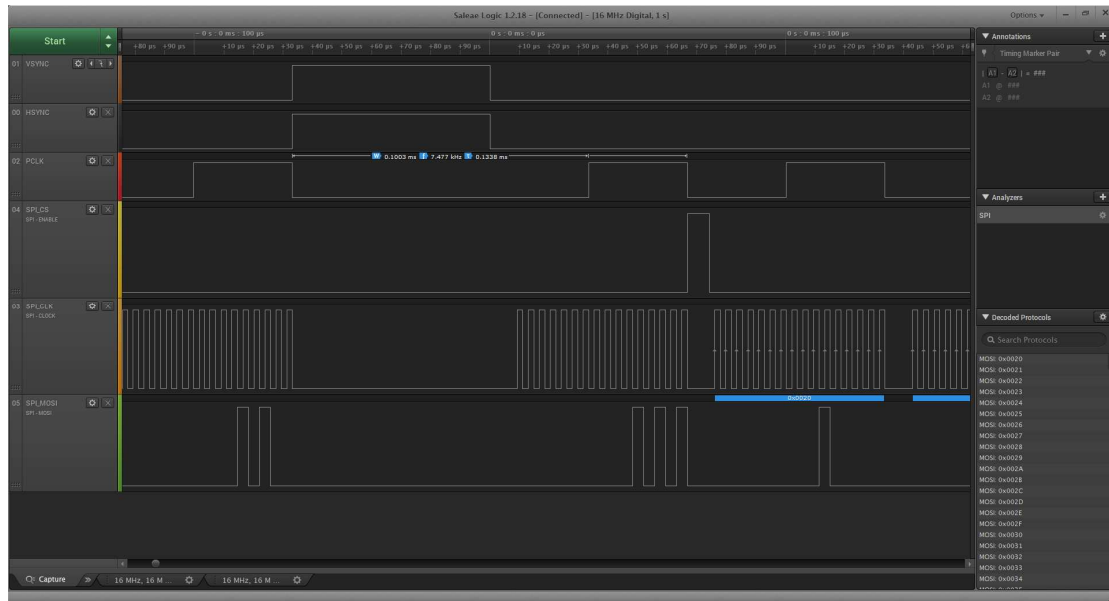
Addr=18, data=146 (Reg\_DigNp =1170)

Addr=19, data=4

Addr=21, data=65 (SPI\_SCK =270KHz)

PCLKMask=1

SPICSMODE=1



09041450

Another I2C setting:

Addr=20, data=4 (Sys\_ck =17.5MHz)

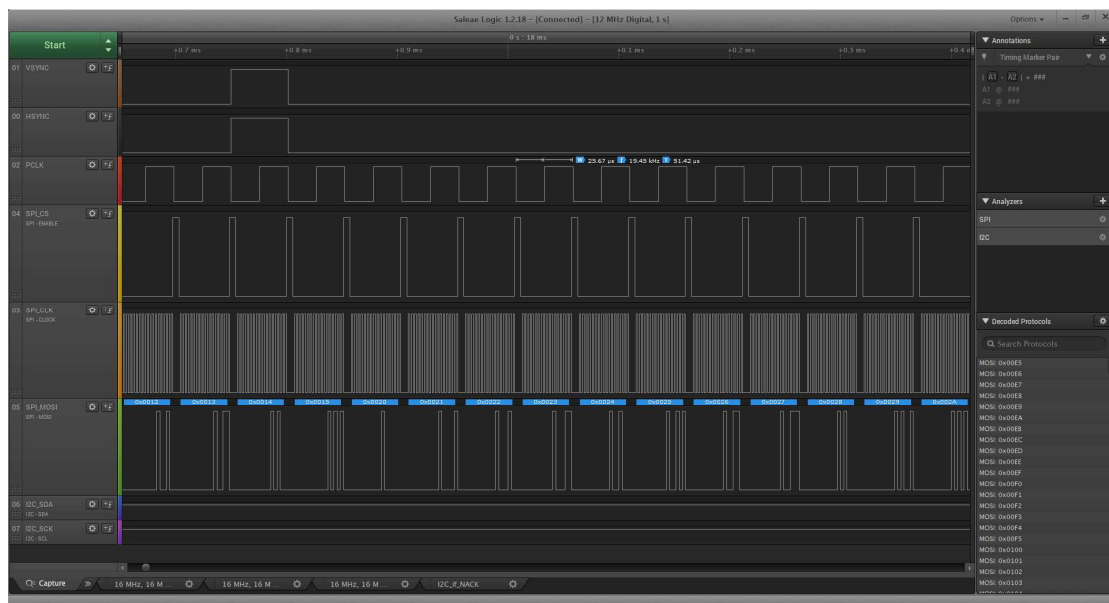
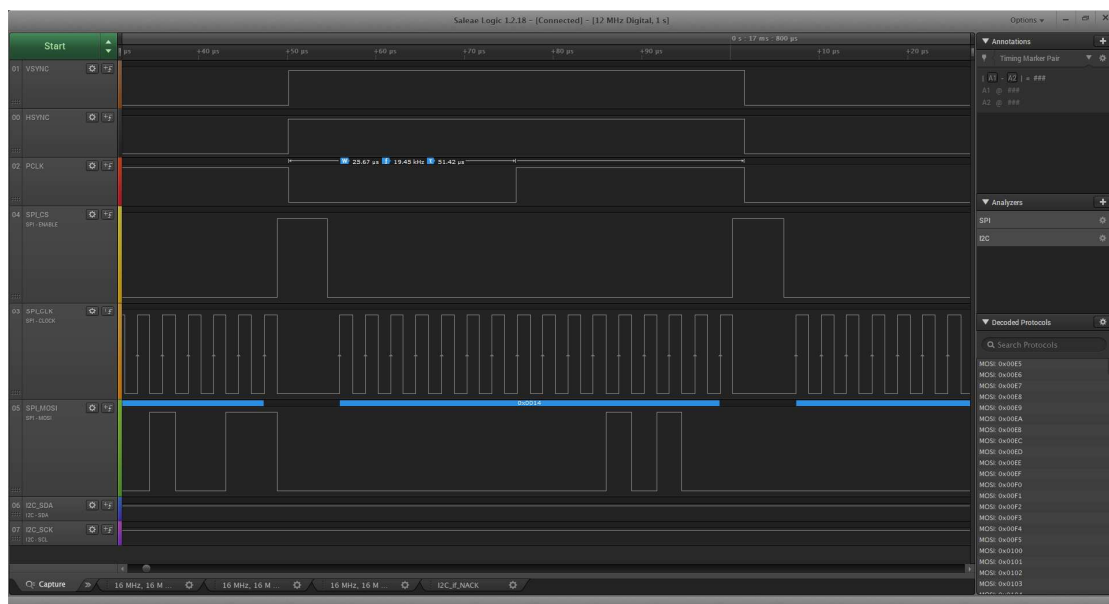
Addr=18, data=146 (Reg\_DigNp =900)

Addr=19, data=4

Addr=21, data=50 (SPI\_SCK =19.4KHz)

PCLKMask=0

SPICSMODE=0



Another I2C setting:

Addr=20, data=4 (Sys\_ck =17.5MHz)

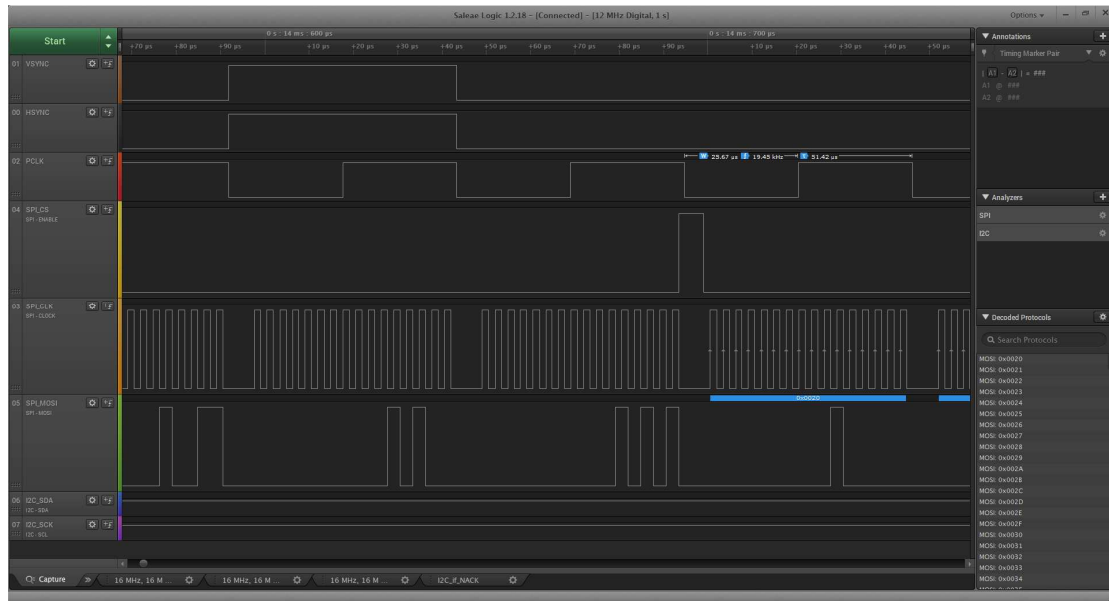
Addr=18, data=146 (Reg\_DigNp =900)

Addr=19, data=4

Addr=21, data=50 (SPI\_SCK =19.4KHz)

PCLKMask=0

SPICSMODE=1



Another I2C setting:

Addr=20, data=4 (Sys\_ck =17.5MHz)

Addr=18, data=146 (Reg\_DigNp =900)

Addr=19, data=4

Addr=21, data=50 (SPI\_SCK =19.4KHz)

PCLKMask=1

SPICSMODE=1

