

# RIYA SHARMA

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## Education

**JSS Academy Of Technical Education, Noida**

*Btech in ECE (GPA: 8.38/10.0)*

**Dec 2020 – Aug 2024**

*Noida, UP*

**Sanskar Public School, Mathura**

*Intermediate (percentage: 91.2/100)*

**April 2020**

*Mathura, UP*

## Relevant Coursework

- |                                |                                    |   |             |
|--------------------------------|------------------------------------|---|-------------|
| • Data Structures & Algorithms | • SQL<br>• DBMS<br>• Communication | Networks<br>• Operating System<br>• Object Oriented | Programming |
|--------------------------------|------------------------------------|---|-------------|

## EXPERIENCE

**Kellton Tech.**

**July 2023 – Sep 2023**

*Frontend Developer Intern*

*Gurgaon*

- Collaborated with the team to create responsive and visually appealing user interfaces, Demonstrated adaptability and a willingness to learn in a dynamic work environment.
- Gained proficiency in HTML, CSS, JavaScript, and front-end frameworks.

## Projects

**CRYPTOFORGE** | *Java*

**March 2023**

- A Data Security project using three key security; Color Key, Key Values and Armstrong Number
- Implemented Armstrong and Color Cryptography methods in Java to convert plain text into cipher text
- Achieved enhanced data security through asymmetric key cryptography techniques
- Demonstrated problem-solving skills and algorithmic thinking in data security with cryptography.
- GithubRepo: <https://github.com/Riya8532/CryptoForge>

**FITRAHO** | *HTML5, CSS, REACT*

**December 2022**

- This project aims to help individuals maintain an active lifestyle by offering a wide range of exercises, workout routines, and fitness tips.
- It is a front-end project built using HTML, CSS, JAVASCRIPT to provide a comprehensive fitness resource for users.
- GithubRepo: [https://github.com/Riya8532/Fit\\_Raho](https://github.com/Riya8532/Fit_Raho)

**RSA Implementation Using FPGA** | *Verilog(Software), FPGA(Hardware)*

**October 2023-Ongoing**

- This is my final year ECE project that focusses on data security.
- RSA Technique is used here for data security.
- Efficient RSA Operations: The primary objective is to design, implement, and optimize RSA hardware components on an FPGA board.
- Utilize the capabilities of an FPGA to accelerate the RSA algorithm by leveraging parallel processing and other hardware optimization techniques, thus making it suitable for applications requiring quick cryptographic processing.

## Technical Skills

**Languages:** Java, HTML, CSS, JavaScript, SQL

**Developer Tools:** GitHub, VS Code, Eclipse

**Frameworks:** React, Bootstrap

## Achievements

- Institute Rank-52 out of 1100 geeks by geeksforgeeks
- Leetcode Contest highest ranking 1340
- WINNER(x2) of AKTU STATES SPORTSFEST 2021-22
- WINNER(x2) of AKTU ZONALS SPORTSFEST 2022-23.
- Open Source Contributor at GSSOC'22 and Hacktoberfest'22.