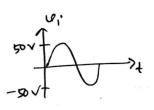
- ~ ~ < ~ < 0

=> -3< VD <0

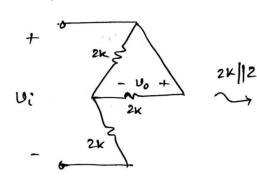
Here,

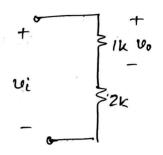
$$Vi, RMG = \frac{Vi, max}{\sqrt{2}}$$

=> 
$$Vi$$
,  $max = 25\sqrt{2}$ .  $\sqrt{2}$ 



(a) for +ve hc,





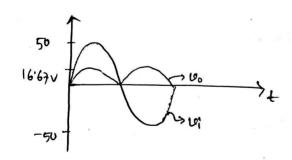
Voltage division trule,

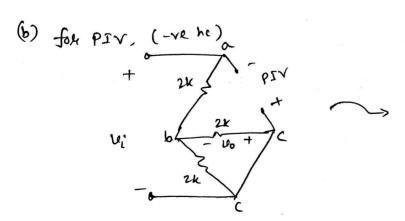
$$v_0 = \frac{1k}{1k + 2k}$$
 vi

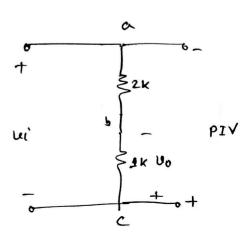
$$\Rightarrow v_0 = \frac{v_1}{3}$$

for -ve he

$$u_0 = -\frac{u_1}{3}$$







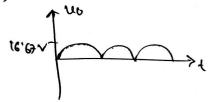
PIV = -Ui

(c) 
$$v_i \rightarrow sine wave \rightarrow v_i - avg = 0$$

$$v_0 \rightarrow full - wave \rightarrow v_0 - avg = \frac{2}{\pi} \times v_0, ma > \frac{2}{3'1416} \times 16'67$$

$$= 10'61 \text{ V}$$

(e)
(n) Only half wave tectifiers can be considered as dippers since vo=0
fort in any half cycle of half-wave. Since the given circuit legresents
a full-wave,



to Can't be considered

for +ve hc,

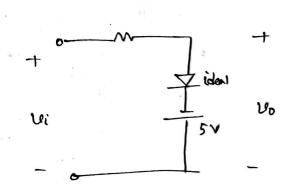
$$v_0 = -5 \, \text{V}$$
 vo is taken

 $across diode$ 

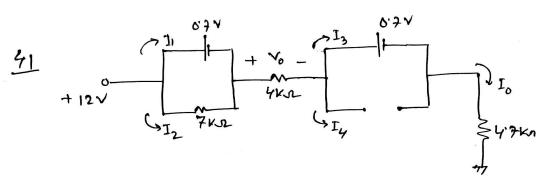
for -ve hc,

 $v_0 = v_i$ 

Liode should be RBed



for -ve he



Hew,

KVL,

$$-12 + 6.7 + 4I_0 + 0.7 + 4.7 I_0 = 0$$

$$\Rightarrow I_0 = 1.22 \text{ mA}$$

again, KVL, 
$$-12 + 7I_2 + 4J_0 + 0.7 + 4.7J_0 = 0 \implies I_2 = 0.098 \text{ mA}$$

$$G \approx 0.1 \text{ mA}$$

$$I_2 = \frac{0.7}{7} = 0.1 \text{ mA}$$

$$50$$
,  $I_1 = I_0 - I_2 = 1.15$  mA

Finally,

$$I_1 = 172 \text{ mA}$$
 $I_2 = 01 \text{ mA}$ 

for +ve hc,

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=> Vo=Vi-Uc= -25-7.9 => Vo=-32.9 ∨

- Ui + Uc + Uo =0

