**Bhakhar Prafulkumar Shambhubhai**

Assistant Professor, Department of Electronics and Communication Engineering,

Birla Vishvakarma Mahavidyalaya College of Engineering

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# **RESUME**

## **Personal Details:**

Name [in full]: Bhakhar Prafulkumar Shambhubhai

Date of Birth: June 01, 1971

Place of Birth: Prempara – 362 130, Taluka: Visavadar,

Dist: Junagadh, Gujarat, INDIA.

Sex and Marital Status: Male, Married

Present Occupation: Assistant Professor,

Department of Electronics and Communication Engineering,

B V M Engineering College, Vallabh Vidyanagar

Pin Code - 388 120, Anand, Gujarat, INDIA.

Languages Known: Gujarati, Hindi and English

## **Contact Details:**

Mailing/ Permanent Address: Hari Aum”

Plot No. 20/2, Opp.: Bansi Avenue

Nr. Dhobighat, B/h. BVM College,

Vallabh Vidyanagar-388 120, Dist: Anand, Gujarat, INDIA

Mobile No: +91 98791 20904.

**Important Note:** My family got immigration visa of USA and my case is under administrative processing due to my Ph. D. and will be clear within a week or two.

## **Academic Qualifications:**

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| Exam Passed | Examining Body | Month & Year of Passing | Marks / Out of | Grade or Class |
| 12th  Science | G.H.S.E.B. (Gandhinagar) | March - 1989 | 245/450 | Second |
| B.E. E&TC | JTM College Of Engg.  (North Maha. Uni.) | January - 1997 | 876/1500 | Higher  Second |
| M.E.  EC | D.D.I.T. (Nadiad)  Gujarat University | February - 2000 | 1217/2000 | First |
| Ph. D. (Pursuing) | C. U. Shah University | Admitted in  May-2013 | -NA- | -NA- |

## **Experience:**

**From 2nd January 2007 to till date as an Assistant Professor**

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| Name of Organization | B V M College of Engineering affiliated to Gujarat Technological University and approved by All India Council for Technical Education (AICTE). |
| Address of Organization | B V M College of Engineering, Vallabh Vidyanagar - 388 120,  Dist: Anand, Gujarat, INDIA.  Phone No: +91 - 2692 230104 |
| Designation | Assistant Professor, recognized as undergraduate teacher for Electronics and Communication Engineering, Gujarat Technological University, Ahmedabad, Gujarat and Post Graduate teacher for Electronics and Communication Engineering by Sardar Patel University, Vallabh Vidyanagar, Gujarat. |
| Duration | 11 years (From 2nd January 2007 to till date). |
| Activities of Organization | Running undergraduate level program for various engineering disciplines like Electronics, Electronics and Communication, Mechanical, Production, Electrical, Information Technology, Computer, Civil engineering and postgraduate program for Computer, Mechanical, Structural Engineering and Electrical Engineering etc. |

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| Job Profile at Birla Vishvakarma Mahavidyalaya College of Engineering | 🡪Teaching theories and laboratories of various subjects like Electronics circuits, Digital circuits, Wave propagation and antennas, Microwave engineering, Communication technologies, Optical fiber communication and electromagnetic engineering of Electronics and Communication Engineering at undergraduate level.  🡪 Guiding final year students for project work, seminar and group discussion in different areas of electronics and communication engineering.  🡪 Motivating students of second year electronics and communication engineering for their personality development, educational problems and providing moral support for critical thinking and solution.  🡪 Actively involved in co-curricular and extra-curricular activities, industrial visits and training for students to well-known industrial organizations.  🡪 Working as member of cultural committee of the institute. |

#### *Additional Experience:*

* From August 2002 to 31st December 2006, I have worked as a Lecturer at A. D. Patel Institute of Technology, New Vallabh Vidyanagar-388 121, Dist: Anand, Gujarat. Contact No. 02692 – 233680.

* From January 2000 to August 2002, I have worked as an Automation Engineer at G M Engineering Company B/h.: S. T. Workshop, Opp: Wire House, Gondal Road, Rajkot.
* From 1st April 1997 to 19th July 1998 I have worked as a Trainee Engineer at Pioneer Induction Heating Furnace making company at GIDC, Vidyanagar, Dist.: Anand, Gujarat.

*Seminar Attended:*

1. “Science and Technology Exhibition” and “ Education Exhibition” organized by Charutar Vidyamandal, at A. D. Patel Institute of Technology (ADIT) on 13th, 14th, & 15th August 2004 as a part of 58th independence day celebration as exhibition designer and project guide.

2. “Exploring MATLAB for Science Computation and Engineering Analysis” from 8th July to 11th July 2004 Organized by Electronics & Communication Engineering Department, ADIT.

3. “Latest Trends in Communication System and Technology”. Organized by Electronics & Communication department, ADIT on 13th March 2004

4. “National Symposium on Data and Knowledge engineering” organized by ADIT on 21st February 2004

5. “WTO-Opportunity or Threat to Technical Education in the Context of Excellence and Quality Assurance” at B&B Institute of Technology, Vallabh Vidyanagar on Friday, 25th April 2003

6. “Role of Technical Education for Sustainable Development” at ADIT, New Vallabh Vidyanagar on October 13th, 2002

## **Professional References:**

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| --- |
| **Prof. (Dr.) Bhargav Goradia**  Head of the Department  Electronics and Communication Engineerin, Birla Vishvakarma Mahavidyalaya College of Engineering  VALLABH VIDYANAGAR - 388 120, ANAND, GUJARAT, INDIA  Mobile No. +91 – 98797 93374 |
| **Prof. (Dr.) Indrajit Patel**  Principal, Birla Vishvakarma Mahavidyalaya College of Engineering  VALLABH VIDYANAGAR - 388 120, ANAND, GUJARAT, INDIA  Mobile No. +91 – 98980 34464 |
| **Prof. Dr. Ved Vyas Dwivedi, *(Ph.D. Guide)***  Pro VC, C. U. Shah University,  Surendranagar-Ahmedabad Highway,  Near Kothariya Village, Wadhwan City - 363 030.  Dist. Surendranagar. Gujarat. India. Ph. +91 - 2752-247711 Fax. +91 - 2752-247712  Mobile No. +91 - 98252 34361 |

**List of Publications:**

1. **Praful S. Bhakhar** and Rutvij Joshi, “Storage Facility for C.R.O. Using Micro-processor 8085” *J. of Eng.,* A. D. Patel Institute of Tech. vol. 3, no. 1 pp. 59-62, Dec. 2006.

2. Jagdish M. rathod, **Praful S. Bhakhar**, Vipul M. Dabhi, Yogesh P. Kosta and Shakti Kosta “Antenna System with Real Time Frequency Variation Blue Tooth Applications,” *J. of Eng. And Techn*., Sardar Patel Univ., vol. 20, pp. 59-63, Dec. 2007.

3. **Praful S. Bhakhar**, Jagdish M. Rathod, Vipul M. Dabhi, Yogesh P. Kosta and Shakti Kosta, “A Re-configurable Liquid Antenna System,” *J. of Eng. And Techn.*, Sardar Patel Univ., vol. 20, pp. 64-67, Dec. 2007.

4. R. C. Joshi and **Praful S. Bhakhar** “The Technical Overview on SMM Receiver for Terahertz Astronomy,” *Int. Conf. on microw. and optoelectronics*, Dr. B. A. Marathawada Univ., Aurangabad(Maharashtra), India, Dec. 2007.

5. **Praful S Bhakhar**, Ved Vyas Dwivedi, “Analytical Investigation on Novel Microstrip Asymmetric Impedance Couplers for RF and Wireless Applications,” Inventi Reid: Microwave, 2013(3): 1-6, 2012. **(Ph. D. work)**

6. **Praful S. Bhakhar**, Ved Vyas Dwivedi, Pravin R. Prajapati, “Directivity Enhancement of Miniaturized Directional Coupler using Defected Ground Structure,” *Int. Conf. on Comm. and Signal Processing*, Dr. B. A. Techn. Univ., Lonere, Maharashtra, India, Dec. 2016. **(Ph. D. work)**

7. **Praful S. Bhakhar**, Ved Vyas Dwivedi, “ Design of a Symmetrical Backward Microstrip Coupler using Epsilon Negative Material for Lower C-band Applications,” *Int. J. of Res. and Scientific Inn.*, vol. 4, issue 9, pp. 118-123, Sep. 2017. **(Ph. D. work)**

8. **Praful S. Bhakhar**, Ved Vyas Dwivedi, “Symmetrical Impedance Microstrip Coupled Line Coupler using Fractal DGS for Lower C-band Applications,” *Int. J. of Microw. and Opt. Techn*. **(Ph. D. work)**

*[Communicated on 27th November 2017 in USA based Scopus indexed journal]*

9. **Praful S. Bhakhar**, Ved Vyas Dwivedi, Pravin R Prajapati “Miniaturized Microstrip Symmetrical Impedance 3 dB Coupler using Fractal DGS with Improved Parameters,” *Int. J. of IETE*. **(Ph. D. work)**

*[Communicated on 13th August 2017 in Thomson Router indexed journal]*