

**Compact  
Device  
Model  
Equations  
and  
Parameters**

**Qucs-S  
Schematic  
Capture/  
GUI**

**Device/  
circuit  
netlist:**

**Model.cir**

**Xyce  
or  
Ngspice  
or  
SPICE OPUS**

**Qucs-S  
or  
Octave  
Post  
Simulation  
Processing**

**Verilog-A  
module  
synthesis  
from  
subcircuit  
schematic**

**Module.va**

