# Seyed Armin Vakil Ghahani

**■**Computer Science & Engineering Department, University Park, PA 16802



#### **EDUCATION**

## Pennsylvania State University, University Park, PA

Aug 2018 - Now

- Ph.D. Student in Computer Science & Engineering Department
  - Advisor: Prof. Mahmut Taylan Kandemir
  - GPA: 4/4
  - Relevant Graduate Courses: Computer Architecture, Data Structures & Algorithms, Binary-level Analysis, Language-based Security, Operating Systems, Emerging Technologies, Compiler Construction

## Sharif University Of Technology, Tehran, Iran

Sep 2013 - Jul 2018

- Bachelor of Science (B.S.) in Computer Engineering Hardware
  - Thesis: Cache Replacement Policy Based on Expected Hit Count Advisor: Prof. Hamid Sarbazi-Azad

### RESEARCH INTERESTS

- Computer Architecture
- Memory Systems
- Virtual Memory
- Virtualized Systems
- Persistent Memory

#### **PUBLICATIONS**

- **Armin Vakil**, Mahmut Taylan Kandemir, Jagadish Kotra "DSM: A Case for Hardware-Assisted Merging of DRAM Rows with Same Content", *In Proceedings of the ACM on Measurement and Analysis of Computing Systems*, (SIGMETRICS 2020)
- Mohammad Bakhshalipour, Aydin Faraji, Armin Vakil, Farid Samandi, Pejman Lotfi-Kamran, Hamid Sarbazi-Azad "Reducing Writebacks Through In-Cache Displacement", ACM Transactions on Design Automation of Electronic Systems, (TODAES 2019)
- Armin Vakil, Sara Mahdizadeh Shahri, Mohammad Bakhshalipour, Pejman Lotfi-Kamran, Hamid Sarbazi-Azad "Making Belady-Inspired Replacement Policies More Effective Using Expected Hit Count." arXiv preprint, (arXiv 2018)
- Armin Vakil, Sara Mahdizadeh Shahri, Mohammad-Reza Lotfi-Namin, Mohammad Bakhshalipour, Pejman Lotfi-Kamran, Hamid Sarbazi-Azad, "Cache Replacement Policy Based on Expected Hit Count", *IEEE Computer Architecture Letters*, (CAL 2017)

## WORK EXPERIENCE

■ Software Developer, I-Cliqq

Jan 2018 – Aug 2018

- Designing Embroidery Software
- Software Developer, Viratech Sharif, Tehran, Iran

Sep 2015 – Sep 2016

- Traffic Simulator (C++) Network Simulator
- Add tunneling protocol between link, internet, and transport layer

## RESEARCH EXPERIENCE

## ■ Pennsylvania State University

- ♦ **DRAM Memory** DRAM memories need refresh operations because they lose their content/charge over time. The overhead of these refreshes increases with larger DRAM memories. My research in this area reduces the memory refresh overhead in virtualized systems by leveraging the same-content values in DRAM.
- ♦ **Virtual Memory** Huge pages reduce the virtual-to-physical address translation by reducing the pressure on TLB and increasing the TLB reach. However, allocating huge pages in presense of memory fragmentation has high overheads and I try to address this problem in my research.
- ⋄ Persistent Memory Exploring persistent memory programming challenges and opportunities.

#### Sharif University of Technology

♦ **Cache Replacement Policies** - My B.Sc. thesis project is on predicting the correlation of reuse-distance between cache blocks and remaining hit count of each cache block. In our research, we proposed a cache replacement policy that leverages the correlation between reuse-distance and the remaining hit count.

THE A CITINIC	- Tarabina Assistant at Damanlannia State Hairrania	
TEACHING EXPERIENCE	<ul> <li>Teaching Assistant at Pennsylvania State University</li> <li>Introduction to Computer Architecture (CMPEN 431)</li> </ul>	Spring 2020
EAPERIENCE		3pring 2020 319, Spring 2019
	■ Teaching Assistant at Sharif University of Technology	713, 3pmg 2013
		Fall 2016, 2017
	*	ring & Fall 2017
	Digital Design	Spring 2017
	Advanced Logic Design	Fall 2016
	Discrete Structures	Spring 2016
	Advanced Programming	Fall 2014, 2015
	• Fundamental Of Programming Spi	ring & Fall 2014
	■ High School Teacher	2013 - 2018
	• Teaching Combinatorics, Graph Theory, Algorithm, and C++ Programming	
DDECENTATIONS	- DSM: A Cose for Hardware Assisted Marging of DDAM Davis with Same Conten	+
PRESENTATIONS	<ul> <li>DSM: A Case for Hardware-Assisted Merging of DRAM Rows with Same Conten</li> <li>ACM SIGMETRICS</li> </ul>	June 2020
	• ACIVI SIGIVIETRICS	Julie 2020
SKILLS	<ul> <li>Programming Languages: C/C++, Python, Verilog, R, Shell, Assembly</li> </ul>	
■ Simulators: gem5, BadgerTrap, DRAMsim2, BigHouse, Ramu		ΓΙ, SimpleSSD,
	ChampSim	, 1
	■ Tools & Frameworks: Qemu, Pin, DynamoRIO, LLVM, Google Protobuf, gRPC, O	Qt
	<ul><li>Operating Systems: Ubuntu(Native), Windows</li></ul>	
	■ Type Setting: L⁴TEX, Microsoft Office	
HONORS	<ul> <li>Qualified for 2nd Cache Replacement Championship (CRC-2)</li> </ul>	¥ 004 <b>=</b>
AND	Cache Replacement Policy Based on Expected Hit Count	Jun 2017
AWARDS		2012 – Jan 2015
	• Silver Medal in 22nd Iran National Olympiad in Informatics(INOI)	Sep 2012
COMMUNITY	■ Sharif AI Challenge (Contest Organizer) Jan	2015 – Jan 2017
SERVICE	Undergraduate Programming Contest	
	• C++ Client	
	■ 1st Gateuino Contest (Contest Organizer)	May 2016
	• Founded a new hardware contest for undergraduate freshman and sophomore	v
COURSE	■ Parallel Distributed File System	Dec 2019
COURSE PROJECTS	Based on gRPC and Google Protobuf	
	<ul> <li>Based on gRPC and Google Protobuf</li> <li>Binary Instrumentation</li> </ul>	May 2019
	<ul> <li>Based on gRPC and Google Protobuf</li> <li>Binary Instrumentation</li> <li>Dynamic Binary Instrumentation for dynamic libraries during runtime of</li> </ul>	May 2019
	<ul> <li>Based on gRPC and Google Protobuf</li> <li>Binary Instrumentation</li> <li>Dynamic Binary Instrumentation for dynamic libraries during runtime of providing persistency guarantees</li> </ul>	May 2019 applications for
	<ul> <li>Based on gRPC and Google Protobuf</li> <li>Binary Instrumentation</li> <li>Dynamic Binary Instrumentation for dynamic libraries during runtime of providing persistency guarantees</li> <li>Hospital Management System</li> </ul>	May 2019
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■ English: Fluent