# Seyed Armin Vakil Ghahani

**■**Computer Science & Engineering Department, University Park, PA 16802



#### **EDUCATION**

### Pennsylvania State University, University Park, PA

Aug 2018 - Now

- Ph.D. Student in Computer Science & Engineering Department
  - Advisor: Prof. Mahmut Taylan Kandemir
  - GPA: 4/4
  - **Relevant Graduate Courses**: Fundamentals of Computer Architecture, Algorithm Design and Analysis, Binary-level Analysis, Language-based Security, Operating System Design, Emerging Technologies, Compiler Construction

# Sharif University Of Technology, Tehran, Iran

Sep 2013 - Jul 2018

- Bachelor of Science (B.S.) in Computer Engineering Hardware
  - Thesis: Cache Replacement Policy Based on Expected Hit Count Advisor: Prof. Hamid Sarbazi-Azad
  - GPA: 16.47/20 (CE Major Coursework: 18.1/20)

# RESEARCH INTERESTS

- Computer Architecture
- Memory Systems
- Virtual Memory
- Virtualized Systems
- Persistent Memory

#### **PUBLICATIONS**

- Armin Vakil, Mahmut Taylan Kandemir, Jagadish Kotra "DSM: A Case for Hardware-Assisted Merging of DRAM Rows with Same Content", In Proceedings of the ACM on Measurement and Analysis of Computing Systems, (SIGMETRICS 2020)
- Mohammad Bakhshalipour, Aydin Faraji, Armin Vakil, Farid Samandi, Pejman Lotfi-Kamran, Hamid Sarbazi-Azad "Reducing Writebacks Through In-Cache Displacement", ACM Transactions on Design Automation of Electronic Systems, (TODAES 2019)
- Armin Vakil, Sara Mahdizadeh Shahri, Mohammad Bakhshalipour, Pejman Lotfi-Kamran, Hamid Sarbazi-Azad "Making Belady-Inspired Replacement Policies More Effective Using Expected Hit Count." arXiv preprint, (arXiv 2018)
- Armin Vakil, Sara Mahdizadeh Shahri, Mohammad-Reza Lotfi-Namin, Mohammad Bakhshalipour, Pejman Lotfi-Kamran, Hamid Sarbazi-Azad, "Cache Replacement Policy Based on Expected Hit Count", *IEEE Computer Architecture Letters*, (CAL 2017)

# WORK EXPERIENCE

■ Software Developer, I-Cliqq

Jan 2018 – Aug 2018

- Designing Embroidery Software
- Software Developer, Viratech Sharif, Tehran, Iran

Sep 2015 – Sep 2016

- Traffic Simulator (C++) Network Simulator
- Add tunneling protocol between link, internet, and transport layer

# RESEARCH EXPERIENCE

### ■ Pennsylvania State University

- ♦ **DRAM Memory** DRAM memories need refresh operations because they lose their content/charge over time. The overhead of these refreshes increases with larger DRAM memories. My research in this area reduces the memory refresh overhead in virtualized systems by leveraging the same-content values in DRAM.
- $\diamond$  **Virtual Memory** Huge pages reduce the virtual-to-physical address translation by reducing the pressure on TLB and increasing the TLB reach. However, allocating huge pages in presense of memory fragmentation has high overheads and I try to address this problem in my research.
- ♦ **Persistent Memory** Exploring persistent memory programming challenges and opportunities.

## Sharif University of Technology

♦ **Cache Replacement Policies** - My B.Sc. thesis project is on predicting the correlation of reuse-distance between cache blocks and remaining hit count of each cache block. In our research, we proposed a cache replacement policy that leverages the correlation between reuse-distance and the remaining hit count.

TEACHING	■ Teaching Assistant at Pennsylvania State University	
EXPERIENCE	Introduction to Computer Architecture (CMPEN 431)	Spring 2020
EM EMENCE	Computer Organization and Design (CMPEN 331)	Fall 2018, 2019, Spring 2019
	■ Teaching Assistant at Sharif University of Technology	3 3, 3 3, 4
	Computer Architecture	Fall 2016, 2017
	Digital System Design	Spring & Fall 2017
	• Digital Design	Spring 2017
	Advanced Logic Design	Fall 2016
	Discrete Structures	Spring 2016
	Advanced Programming	Fall 2014, 2015
	Fundamental Of Programming	Spring & Fall 2014
	■ High School Teacher	2013 – 2018
	<ul> <li>Teaching Combinatorics, Graph Theory, Algorithm, and C++ Prog</li> </ul>	gramming
NOTABLE		
PROJECTS	Graduate Projects:  Loop Analysis (Compiler Construction)	May 2020
	<ul> <li>Loop properties analysis based on LLVM</li> </ul>	-
	<ul> <li>Parallel Distributed File System (Operating System Design)</li> </ul>	Dec 2019
	<ul> <li>Based on gRPC and Google Protobuf</li> </ul>	
	<ul> <li>Binary Instrumentation (Binary-Level Program Analysis)</li> </ul>	May 2019
	<ul> <li>Instrumentation for DLLs during runtime of applications for providing persistency guarantees</li> </ul>	
	■ Efficient Undo Logging Implementation (Fundamentals of Computer Architecture) Dec 2018	
	<ul> <li>Rethinking undo logging state-of-the-art design for efficiently updating undo-logging metadata</li> </ul>	
Undergraduate Projects:		
	■ Domain-Specific Language for Financial Calculations (Compiler I	
	• Implementing a DSL for Financial Contracts based on ANTLR an	
	<ul> <li>Hospital Management System (Real-time Systems)</li> </ul>	Jan 2017
	Patient's condition monitoring scheduler	N
	• Chat (Computer Networks)	May 2016
	• Server-Client Chat system over TCP network based on C++ and C	
	Linux Development (Operating System)  Implementing a system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the MAC address of network  The continuous system cell to provide the matter system cell to provide the cell to provide the matter system cell to provide the cell to	Mar 2016 – Jul 2016
<ul> <li>Implementing a system call to provide the MAC address of network interfaces to the user space</li> <li>Adding proc files to provide details, number of occurred interrupts, enable/disable, and show</li> </ul>		
number of sk_buff data structures for each network interface		
	■ Trax Game (FPGA National Contest)	Apr 2016
	Two player game based on Verilog	71pi 2010
	• NoC (Digital System Design)	Jan 2016
	3D Mesh Network on Chip based on Verilog	5th 2010
	Judge	Mar 2015
	<ul> <li>Designing and implementing a judge system for evaluating codes</li> </ul>	
	■ Plants vs Zombies (Advanced Programming - C++)	Jul 2014
	Based on Qt Creator	
	<ul> <li>Suduko (Introduction to Programming)</li> </ul>	Jan 2014
	<ul> <li>Graphical Suduko game based on GTK</li> </ul>	
	<ul> <li>Billiard (Introduction to Programming)</li> </ul>	Jan 2014
	<ul> <li>Graphical Billiard game based on GTK</li> </ul>	
HONORS	<ul> <li>Qualified for 2nd Cache Replacement Championship (CRC-2)</li> </ul>	
AND	Cache Replacement Policy Based on Expected Hit Count	Jun 2017
AWARDS	<ul> <li>Undergraduate grant from the Iranian National Elites Foundation</li> </ul>	Sep 2012 – Jan 2015
TIVI II DO	Silver Medal in 22nd Iran National Olympiad in Informatics(INOI)	Sep 2012 San 2013
	onver median median mananan organiputa in informatico(xi.vor)	3cp 2012
COMMUNITY	<ul> <li>Sharif AI Challenge (Contest Organizer)</li> </ul>	Jan 2015 – Jan 2017
SERVICE	Undergraduate Programming Contest	
	■ 1st Gateuino Contest (Contest Organizer)	May 2016
	• Founded a new hardware contest for undergraduate freshman and	-
PRECENTATIONS - DCM. A Consideral Land Annual Land Consideration Conside		
PRESENTATIONS	<ul> <li>DSM: A Case for Hardware-Assisted Merging of DRAM Rows with</li> </ul>	
	• ACM SIGMETRICS	June 2020

**SKILLS** 

■ Programming Languages: C/C++, Python, Verilog, R, Shell, Assembly

■ Simulators: gem5, BadgerTrap, DRAMsim2, BigHouse, Ramulator, CACTI, SimpleSSD, ChampSim

■ Tools & Frameworks: Qemu, Pin, DynamoRIO, LLVM, ANTLR, Google Protobuf, gRPC, Qt

• Operating Systems: Ubuntu(Native), Windows

■ Type Setting: LATEX, Microsoft Office

LANGUAGES

Persian: NativeEnglish: Fluent