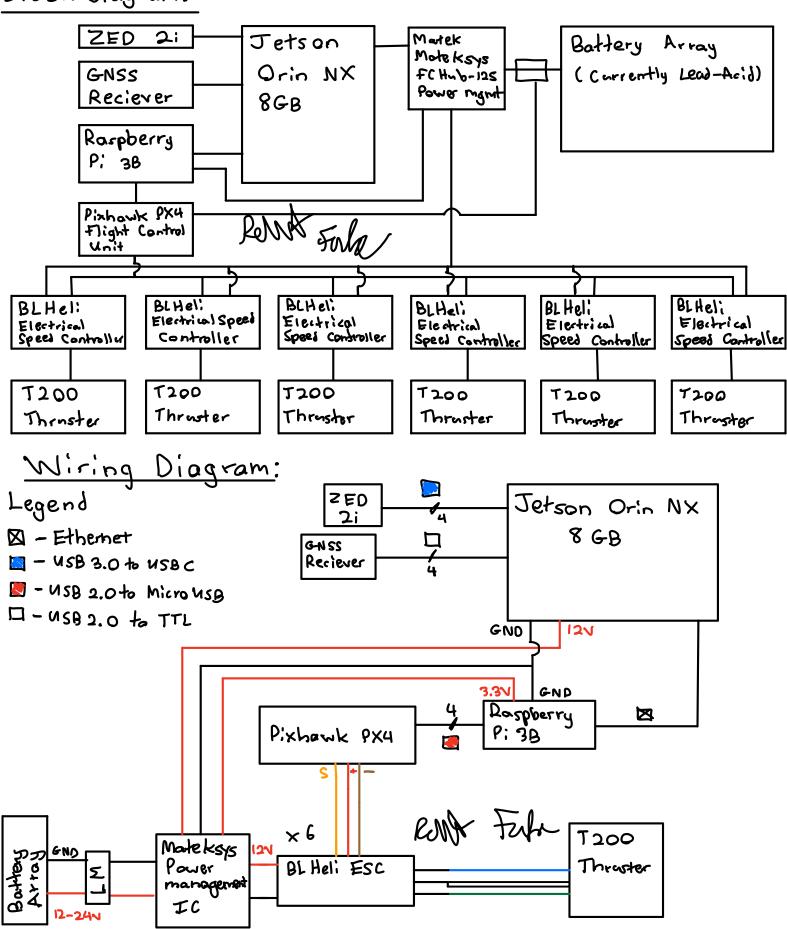
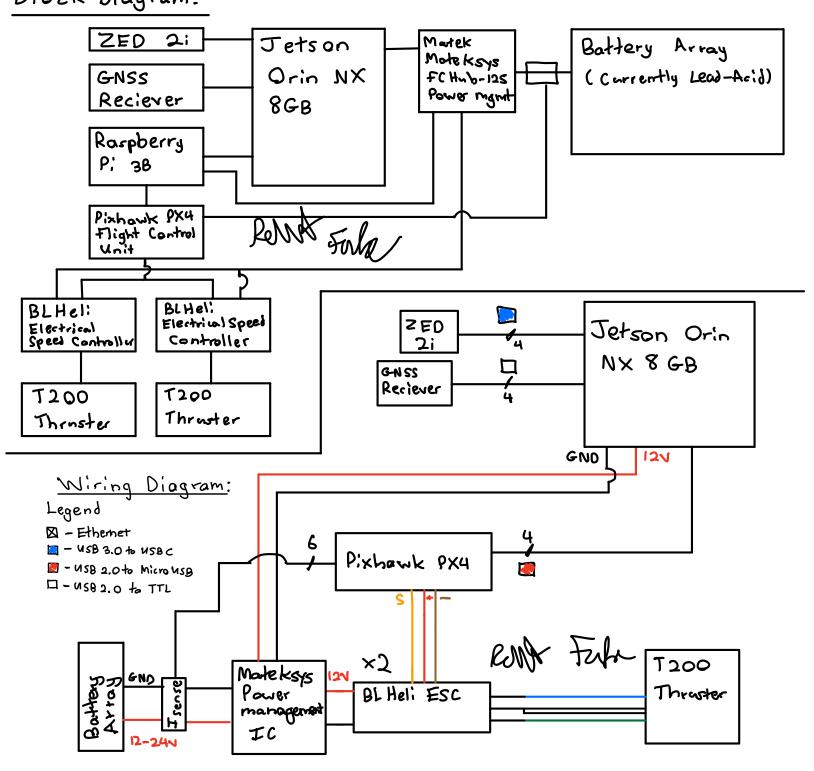
Initial Design: (May-Aug 2024)

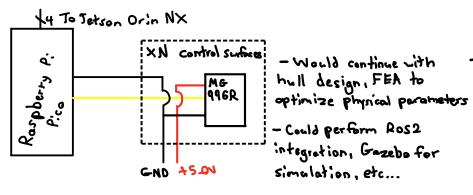
Block Diagram:



Prototype Iteration/Revision (Sept-Dec 2024) Block Diagram:



Rost Figa



Potential Improvements:

- -Pico is powered by USB VDD on Jetson.
 Use + 5.0v from Mateksys FCHUB.
 for serve actuation VDD, PWM from Orin.
- Example servos: MG996R (4.8-604, 13kg)
 (0°-180°, Pulse length 500ms-2500ms)
 GPIO pin used for PWM generation
 Shouldn't motter, consider PWM clock
 Scaling in Signal generation (1.25MHz)