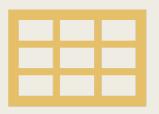
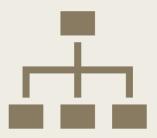
CS 211 RECITATIONS WEEK 10

Wenjie Qiu Teaching Assistant Office hour: Thu noon – 1pm wenjie.qiu@rutgers.edu

Content





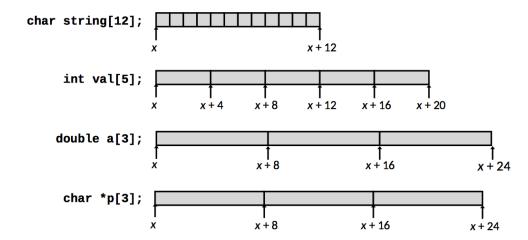
Layout of arrays

Layout of structures

LAYOUT OF ARRAYS

Array Allocation

- Basic Principle
 - T A[L];
 - Array of data type T and length L
 - Contiguously allocated region of L * sizeof(T) bytes in memory



Array Accessing Example

```
int get_digit
  (zip_dig z, int digit)
{
   return z[digit];
}
```

Assembly

```
# %rdi = z
# %rsi = digit
movl (%rdi,%rsi,4), %eax # z[digit]
```

- Register %rdi contains starting address of array
- Register %rsi contains array index
- Desired digit at %rdi + 4*%rsi
- Use memory reference (%rdi, %rsi, 4)

Array Loop Example

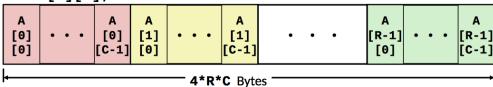
```
void zincr(zip_dig z) {
   size_t i;
   for (i = 0; i < ZLEN; i++)
   z[i]++;
}</pre>
```

```
# %rdi = z
 movl
        $0, %eax
                        # i = 0
                      # goto middle
        .L3
 jmp
.L4:
                        # loop:
 addl
        $1, (%rdi,%rax,4) # z[i]++
                   # i++
        $1, %rax
 addq
                      # middle
.L3:
        $4, %rax # i:4
 cmpq
                        # if <=, goto loop</pre>
        . L4
 jbe
 ret
```

Multidimensional (Nested) Arrays

- Declaration
 - T A[R][C];
 - 2D array of data type T
 - R rows, C columns
 - Type *T* element requires *K* bytes
- Array Size
 - R * C * K bytes
- Arrangement
 - Row-Major Ordering

int A[R][C];



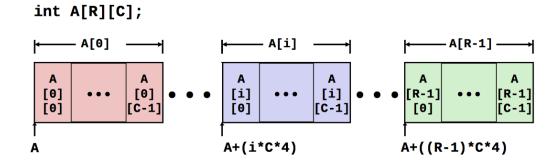
• • • A[0][C-1]

 $A[R-1][0] \cdot \cdot \cdot A[R-1][C-1]$

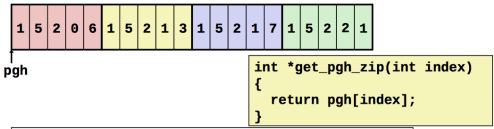
A[0][0]

Nested Array Row Access

- Row Vectors
 - A[i] is array of C elements
 - Each element of type *T* requires *K* bytes
 - Starting address A + i* (C * K)



Nested Array Row Access Code



```
# %rdi = index
leaq (%rdi,%rdi,4),%rax # 5 * index
leaq pgh(,%rax,4),%rax # pgh + (20 * index)
```

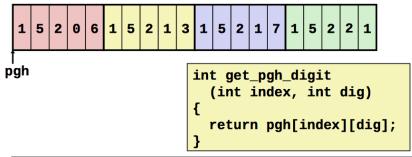
Row Vector

- pgh[index] is array of 5 int's
- Starting address pgh+20*index

Machine Code

- Computes and returns address
- Compute as pgh + 4*(index+4*index)

Nested Array Element Access Code



```
leaq (%rdi,%rdi,4), %rax # 5*index
addl %rax, %rsi # 5*index+dig
movl pgh(,%rsi,4), %eax # M[pgh + 4*(5*index+dig)]
```

Array Elements

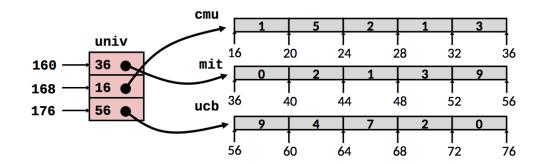
- pgh[index][dig] is int
- Address: pgh + 20*index + 4*dig
 - = pgh + 4*(5*index + dig)

Multi-Level Array Example

```
zip_dig cmu = { 1, 5, 2, 1, 3 };
zip_dig mit = { 0, 2, 1, 3, 9 };
zip_dig ucb = { 9, 4, 7, 2, 0 };
```

```
#define UCOUNT 3
int *univ[UCOUNT] = {mit, cmu, ucb};
```

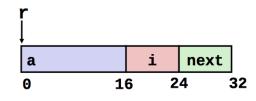
- Variable univ denotes array of 3 elements
- Each element is a pointer
 - 8 bytes
- Each pointer points to array of int's



LAYOUT OF STRUCTURES

Structure Representation

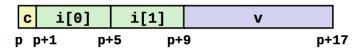
```
struct rec {
   int a[4];
   size_t i;
   struct rec *next;
};
```



- Structure represented as block of memory
 - Big enough to hold all of the fields
- Fields ordered according to declaration
 - Even if another ordering could yield a more compact representation
- Compiler determines overall size + positions of fields
 - Machine-level program has no understanding of the structures in the source code

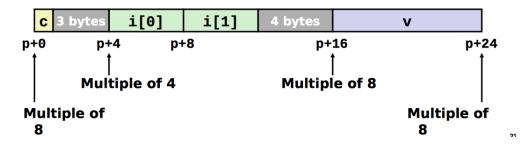
Structures & Alignment

Unaligned Data



```
struct S1 {
  char c;
  int i[2];
  double v;
} *p;
```

- Aligned Data
 - Primitive data type requires K bytes
 - Address must be multiple of K



Specific Cases of Alignment (x86-64)

- 1 byte: **char**, ...
 - no restrictions on address
- **2** bytes: **short**, ...
 - lowest 1 bit of address must be 02
- 4 bytes: int, float, ...
 - lowest 2 bits of address must be 002
- 8 bytes: double, long, char *, ...
 - lowest 3 bits of address must be 000₂
- 16 bytes: long double (GCC on Linux)
 - lowest 4 bits of address must be 0000₂

Meeting Overall Alignment Requirement

- For largest alignment requirement K
- Overall structure must be multiple of K

```
struct S2 {
  double v;
  int i[2];
  char c;
} *p;
```

