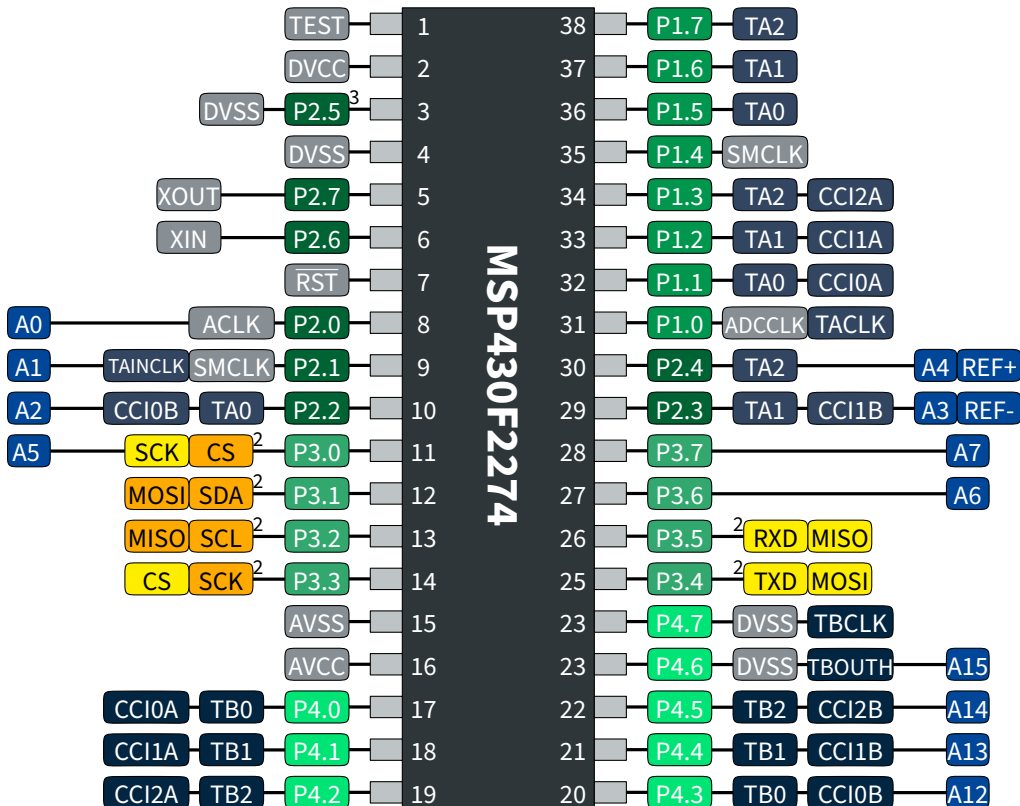


ADC10AE

PxSEL=1 PxDIR=0 PxDIR=1

PxSEL=0 PxSEL=1 PxDIR=0 PxDIR=1

ADC10AE



Power supply, Basic Clock Module+

Port P1<sup>1</sup> Port P2<sup>1</sup> Port P3 Port P4

Timer A

Timer B

USCIA

USCIB

ADC10, OpAmp

Leipzig University of Applied Sciences

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<sup>1</sup> All pins of ports P1 and P2 can be used as an interrupt source.<sup>2</sup> Direction of serial communication pin doesn't care.<sup>3</sup> R<sub>OSC</sub> by setting DCOR=1.