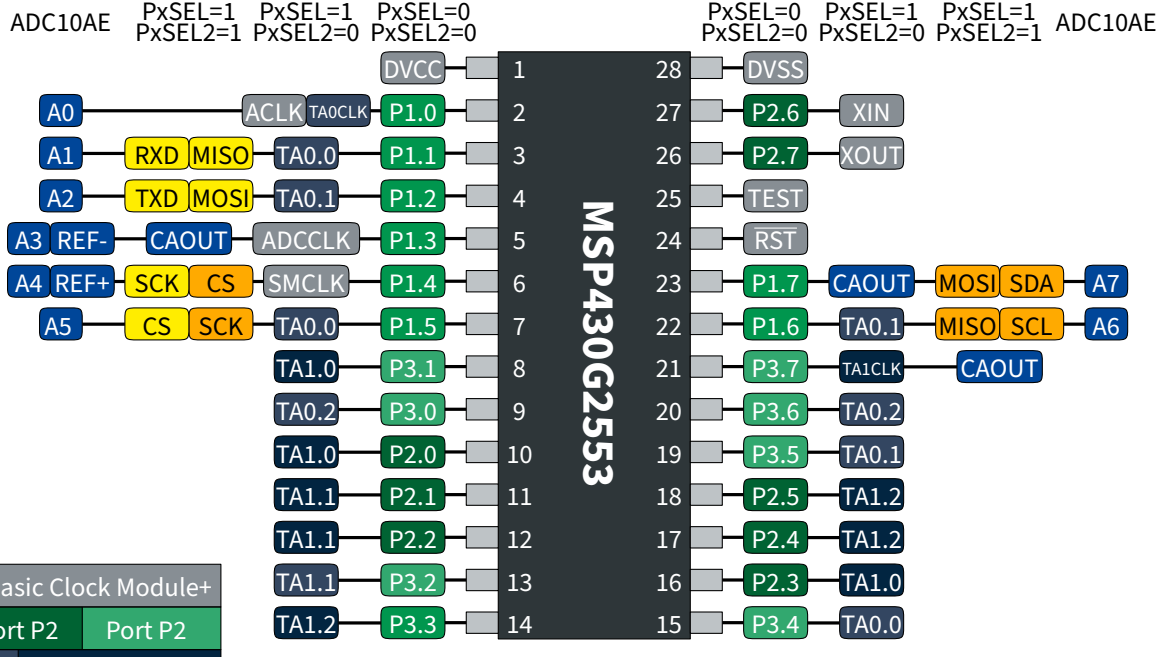

Power supply, Basic Clock Module+		
Port P1	Port P2	Port P2
Timer0_A3	Timer1_A3	
USCIA	USCIB	
ADC10, Comparator A+		

- All pins of ports P1 and P2 can be used as an interrupt source.
- Capacitive sensing is enabled by setting PxSEL=0 and PxSEL2=1.

Leipzig University of Applied Sciences
Faculty of Engineering
Robert Fromm
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