Left side

ADC10AE		PxSEL=11		PxSEL=01		PxSEL=00	
						DVCC	1
A0				ACLK	TA0CLK	P1.0	2
A1		RXD	MISO	TA0.0		P1.1	3
A2		TXD	MOSI	TA0.1		P1.2	4
A3	REF-	CAOUT		ADCCLK		P1.3	5
A4	REF+	CLK	STE	SMCLK		P1.4	6
A5		STE	CLK	TA0.0		P1.5	7
				TA1.0		P2.0	8
				TA1.1		P2.1	9
				TA1.1		P2.2	10

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Right side

	PxSEL=00	PxSEL=01	PxSEL=11		ADC10AE
20	DVSS				
19	P2.6	XIN			
18	P2.7	XOUT			
17	TEST				
16	$\overline{RST}$				
15	P1.7	CAOUT	MOSI	SDA	A7
14	P1.6	TA0.1	MISO	SCL	A6
13	P2.5	TA1.2			
12	P2.4	TA1.2			
11	P2.3	TA1.0			

Legend

Pin number
Power supply, Basic Clock Module+
Port P1, Port P2
Timer0_A3, Timer1_A3
USCIA
USCIB
ADC10, Comperator A+