# GPIO1 (0x42000000)

Is configured as input.

|  |  |
| --- | --- |
| Bit’s | Meaning |
| [31:16] | 16 bit data input 1 |
| [15:0] | 16 bit data input 2 |

# GPIO2 (0x42000008)

Is configured as output. Bit 31 is changed via three state register to input while measuring.

|  |  |
| --- | --- |
| Bit’s | Meaning |
| [31:31] | Input measurement is ready |
| [30:24] | 7 bit sample rate output |
| [23:16] | 8 bit sample number output |
| [15:8] | 8 bit configuration value 1 |
| [7:0] | 8 bit configuration value 2 |

# Three state register (0x4200000c)

Used for Triggering Measurement start.

|  |  |
| --- | --- |
| Bit’s | Meaning |
| [31:31] | Set to 1 to Start measurement (configure GPIO2 bit 31 as input) should be set to 0 after measurement |
| [30:0] | Should be 0 every time to keep GPIO bit [30:0] configured as output |

# BRAM Read (0x40000000)

Read data from BRAM starting with sample 0 at address (0x40000000) and ending with sample n at (0x40000000+n\*4).

|  |  |
| --- | --- |
| Bit’s | Meaning |
| [31:16] | 16 bit data input 1 |
| [15:0] | 16 bit data input 2 |