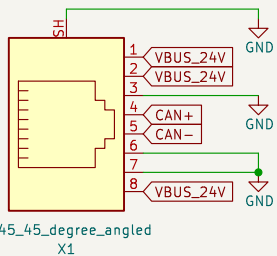
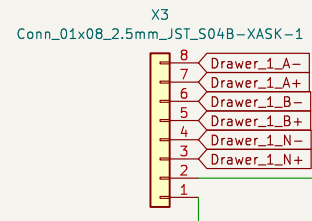


Input

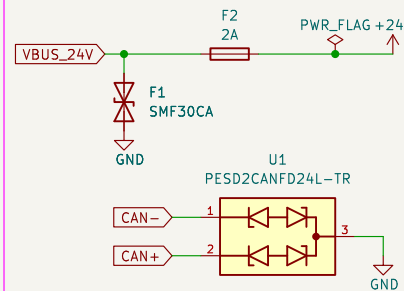
CAN and Power Input



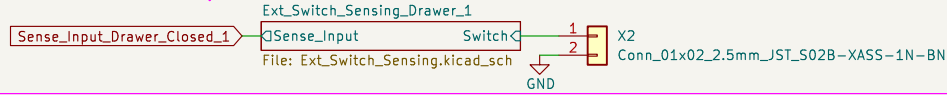
Drawer Encoder Sensor Input



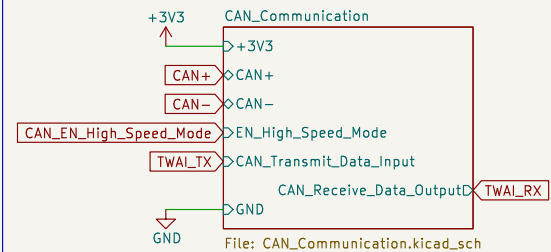
Input protection



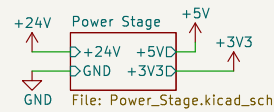
Drawer Closed Sensor Input



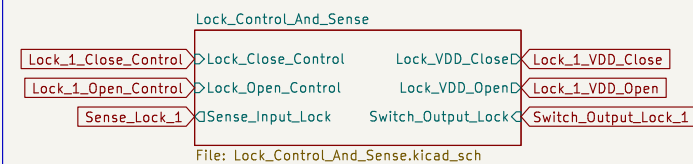
CAN Communication



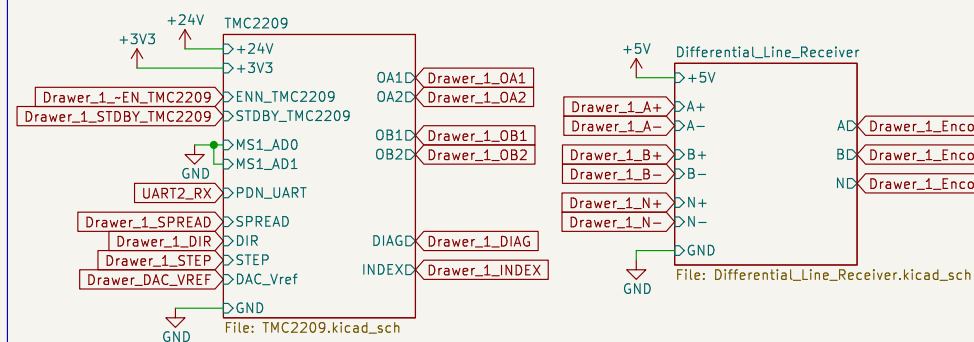
Power Stage



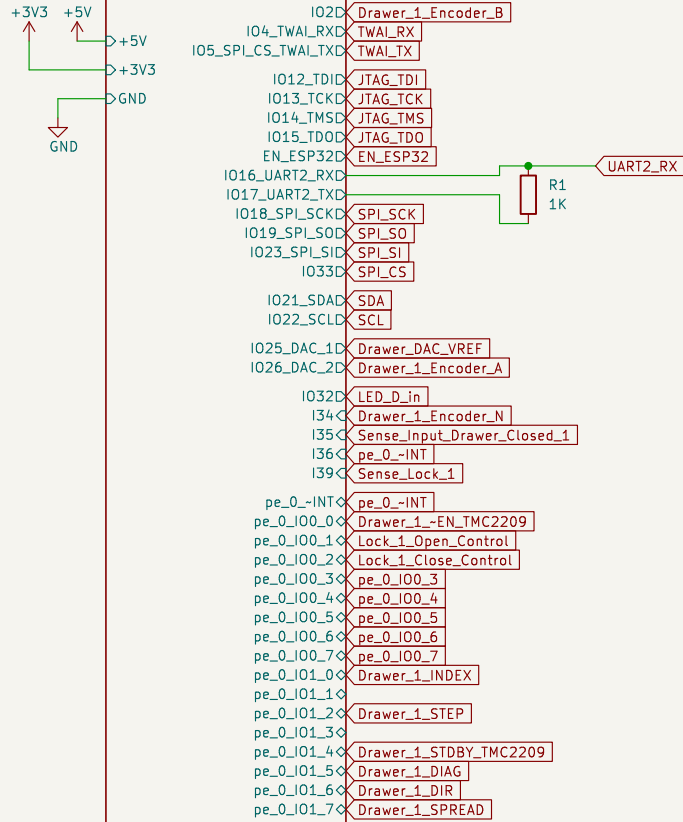
Lock Control and Sense



Electrical Drawer with Encoder



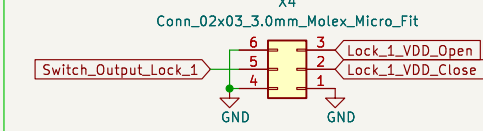
ESP32



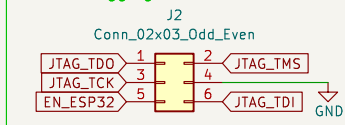
File: ESP32_WROOM_32.kicad_sch

Output

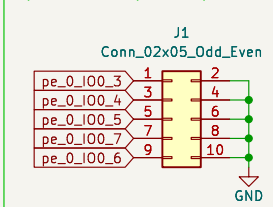
Locks



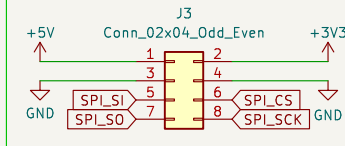
JTAG Debugging



Spare Port Expander pins



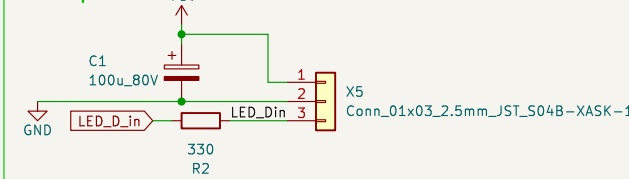
SPI



Electrical Drawer

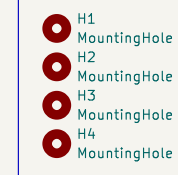


LED Output



Mounting Holes

M3



M2 for cable ties



Sheet: /
File: Drawer_Controller_v4_hotfix.kicad_sch

Title:

Size: A3

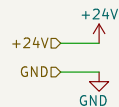
Date:

Rev:

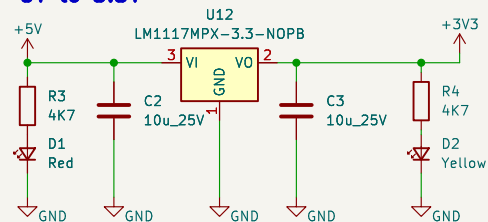
KiCad E.D.A. 8.0.6

Id: 1/12

Input



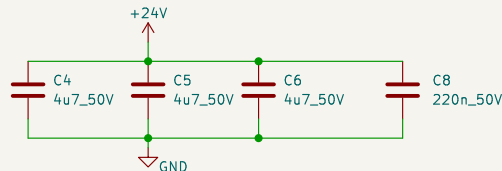
5V to 3.3V



24V to 5V

Input Capacitor

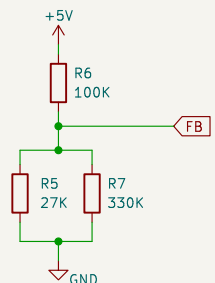
A minimum of 10 μF of ceramic capacitance is required on the input of the LMR33630. This must be rated for at least the maximum input voltage that the application requires; preferably twice the maximum input voltage. This capacitance can be increased to help reduce input voltage ripple and maintain the input voltage during load transients. In addition, a small case size, 220-nF ceramic capacitor must be used at the input, as close as possible to the regulator. This provides a high frequency bypass for the control circuits internal to the device. For this example a 4.7- μF , 50-V, X7R (or better like COG) ceramic capacitor is chosen. The 220 nF must also be rated at 50 V with an X7R or COG dielectric. Please mind that the small capacitor should have a smaller footprint to have smaller ESL. <https://www.signalintegrityjournal.com/articles/1509-the-myth-of-three-capacitor-values>



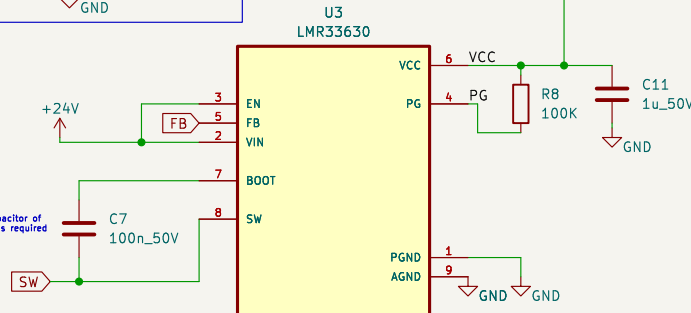
Reference Voltage

Output Voltage Set-Point:
 $R1 = R2 / (V_{\text{Out}}/1V) - 1$
 $R1 = 100k / (5V - 1V) = 25k$

Please mind:
R2 is recommended to be app. 100k Ω
For R1 = 24.9k is chosen as recommended in the datasheet.
 $27k || 330k = 27k * 330k / (27k + 330k) = 24,958$



A high-quality ceramic capacitor of 100 nF and at least 10 V is required



Output Filter

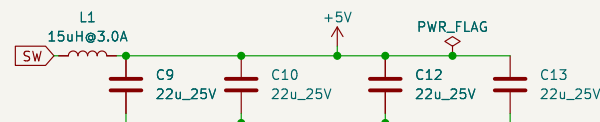
Inductor Selection
The parameters for selecting the inductor are the inductance and saturation current. The inductance is based on the desired peak-to-peak ripple current and is normally chosen to be in the range of 20% to 40% of the maximum output current. Experience shows that the best value for inductor ripple current is 30% of the maximum load current. Note that when selecting the ripple current for applications with much smaller maximum load than the maximum available from the device, the maximum device current should be used. Equation 4 can be used to determine the value of inductance. The constant K is the percentage of inductor current ripple. For this example, $K = 0.3$ was chosen.
$$L = \frac{(V_{\text{IN}} - V_{\text{OUT}}) \times (V_{\text{OUT}} / V_{\text{IN}})}{f_{\text{sw}} \times K \times I_{\text{OUTMax}}}$$
$$= \frac{(24V - 5V) \times (5V / 24V)}{(400\text{kHz} \times 0.3 \times 2.3A)} = 14.34\mu\text{H}$$

Therefore we take the next available standard inductor value of 15uH.

For this example, a $\Delta V_{\text{OUT}} \leq 250 \text{ mV}$ for an output current step of $\Delta I_{\text{OUT}} = 2 \text{ A}$ is required. Equation 6 gives a minimum value of 52 μF and a maximum ESR of 0.11 Ω . More output capacitance can be used to improve the load transient response. Ceramic capacitors can easily meet the minimum ESR requirements. In some cases, an aluminum electrolytic capacitor can be placed in parallel with the ceramics to help build up the required value of capacitance. In general, use a capacitor of at least 10 V for output voltages of 3.3 V or less and a capacitor of 16 V or more for output voltages of 5 V and above.

For the capacitor we use $4 \times 25V \ 22\mu\text{F} \ X5R \pm 10\% \ 1206$ Multilayer Ceramic Capacitors MLCC (CL31A226KAHNNNE). If you check the specifications under <https://product.samsungsem.com/mlcc/cl31a226KAHNNN.do>

You can find, that the DC Bias at 5V DC is -37.96% of the original capacity, so
 $C_0 = 4 \times 22\mu\text{F} \times 0.62 = 54.56\mu\text{F}$
To find out the R_{ESR} of the capacitor we can check the graph given in the above link and check the ESR value at the self-resonant frequency where the Impedance |Z| is minimal. This is at least what they did with the capacitor they took in the datasheet. The link to the capacitor of the datasheet can be found here: https://product.tdk.com/en/search/capacitor/ceramic/mlcc/info?part_no=C3216X5R0J476M160AC
Following this procedure, the ESR value for ONE CL31A226KAHNNNE is something like:
 $R_{\text{ESR}1\text{uc}} = 3.82\text{m}\Omega\text{m} = 0.00382 \text{ Ohm}$



Sheet: /Power Stage/
File: Power_Stage.kicad_sch

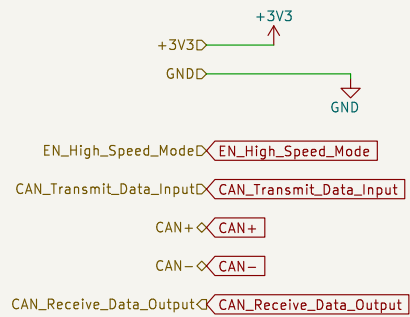
Title:

Size: A4
KiCad E.D.A. 8.0.6

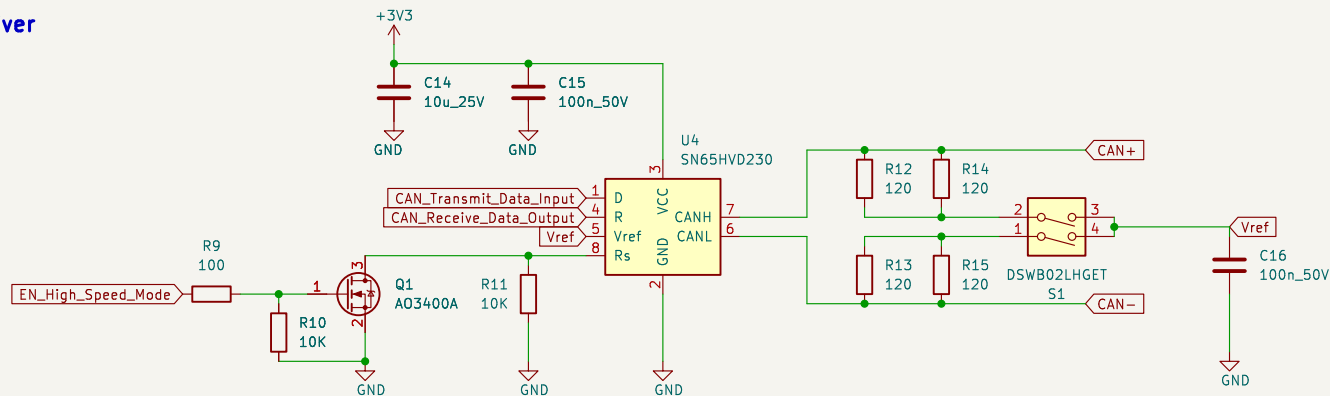
Date:

Rev:
Id: 2/12

Input/Output



CAN Transceiver



The RS pin (pin 8) on the SN65HVD230 and SN65HVD231 provides three different modes of operation: high speed mode, slope control mode, and low-power mode. The high speed mode of operation is selected by connecting the RS pin to ground, allowing the transmitter output transistors to switch on and off as fast as possible with no limitation on the rise and fall slopes. The rise and fall slopes can also be adjusted by connecting a resistor in series between the RS pin and ground. The slope will be proportional to the pin's output current. With a resistor value of 10 k Ω the device will have a slew rate of ~ 15 V/ μ s, and with a resistor value of 100 k Ω the device will have a slew rate of ~ 2 V/ μ s.

Sheet: /CAN_Communication/
File: CAN_Communication.kicad_sch

Title:

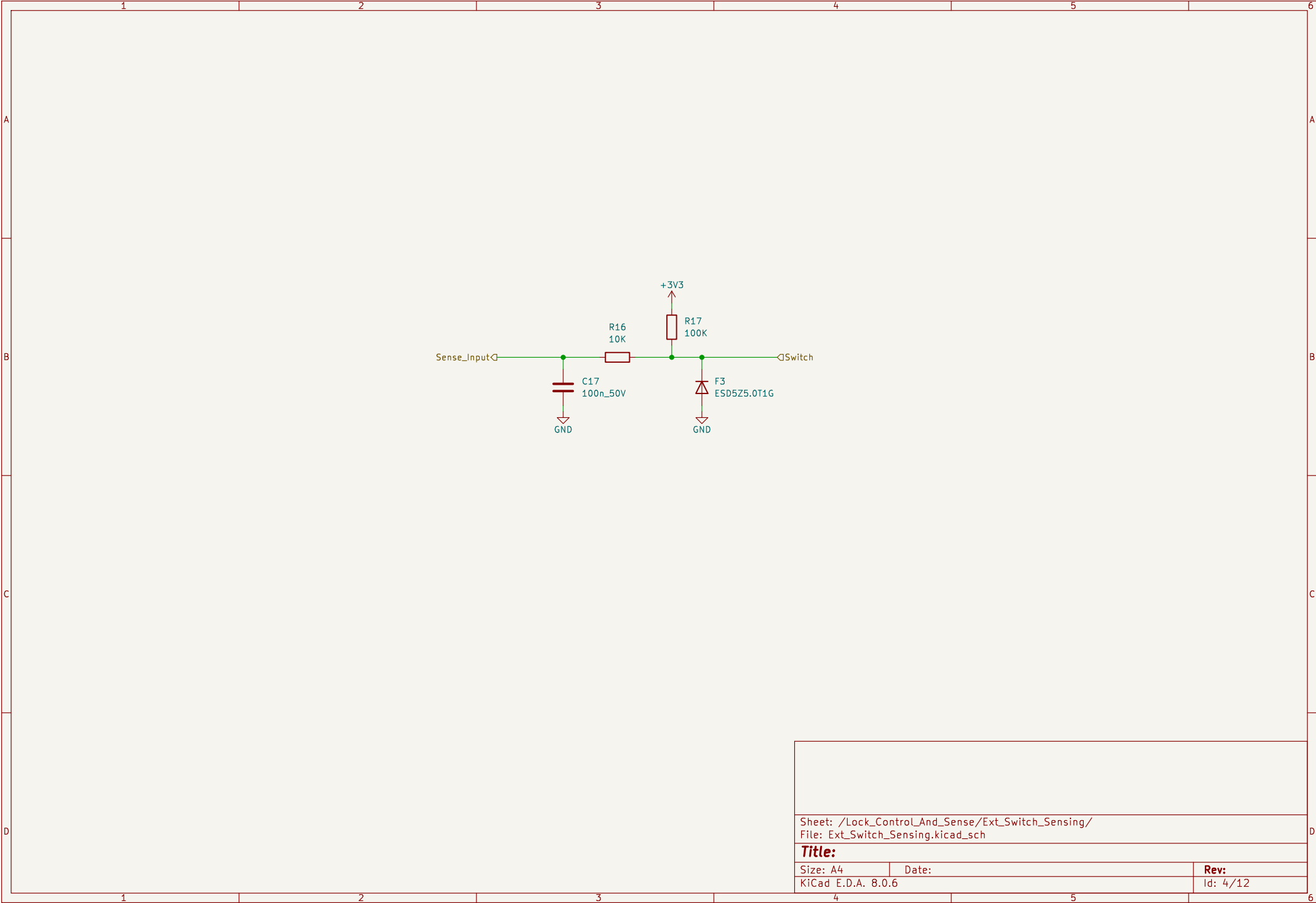
Size: A4

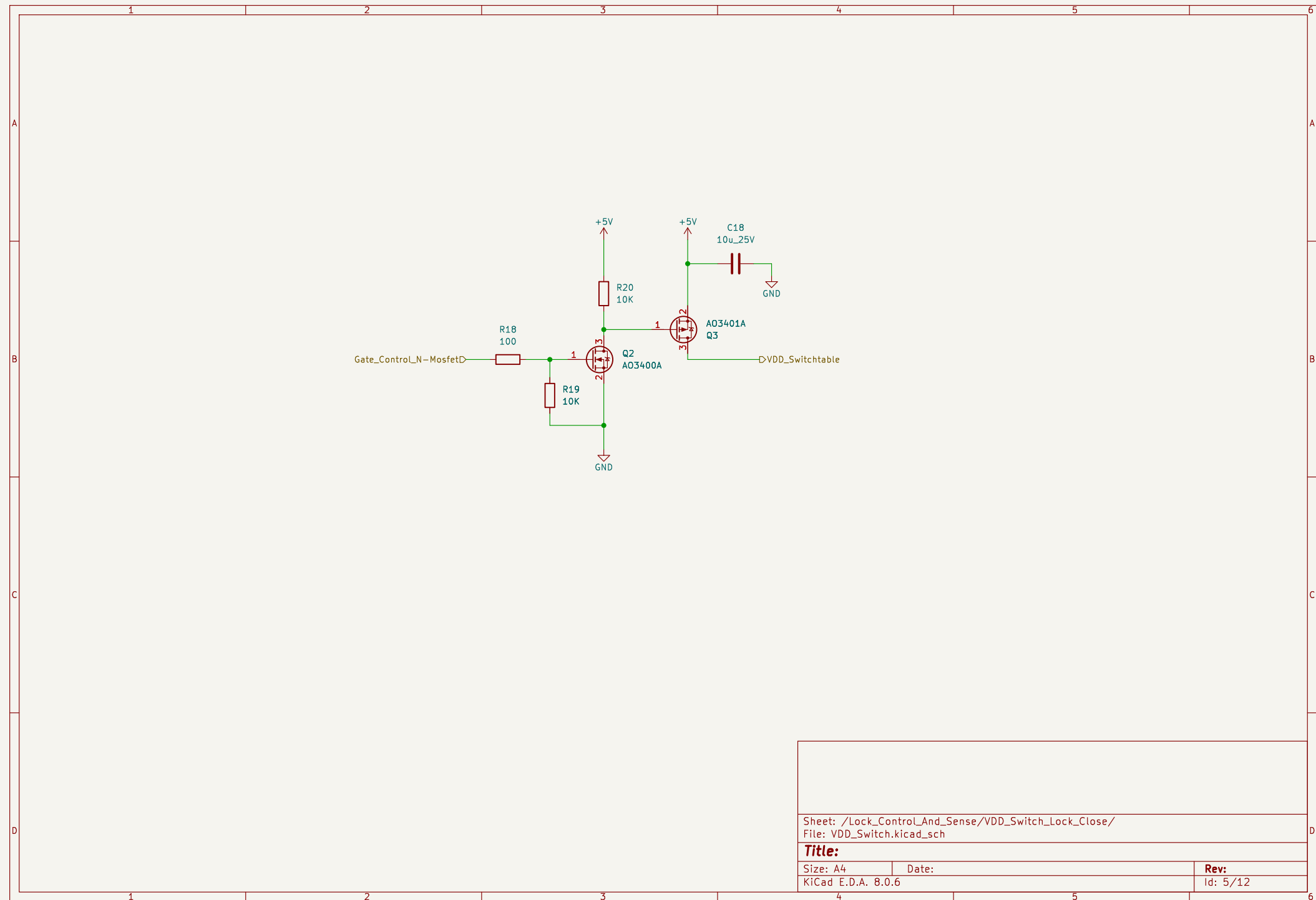
Date:

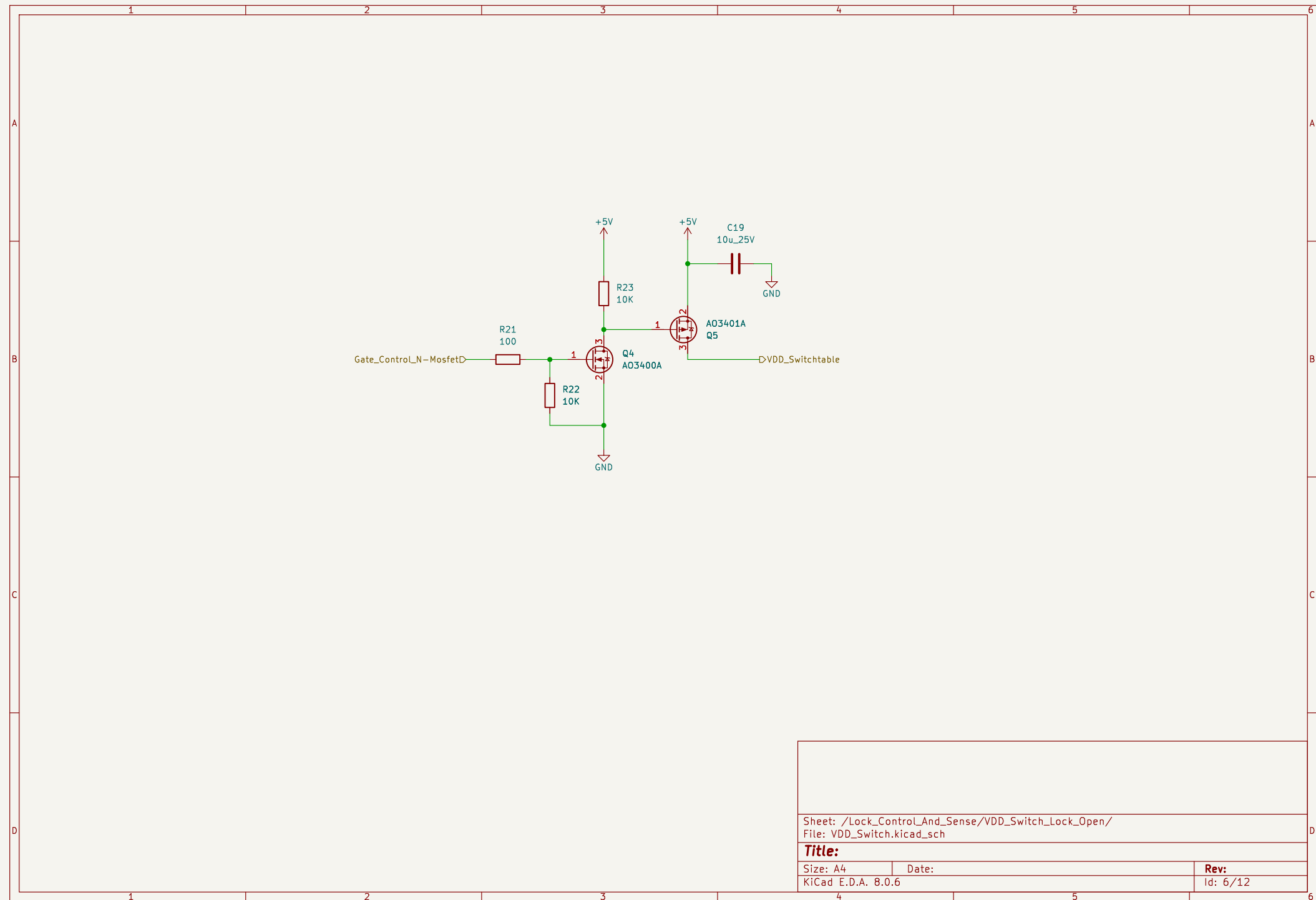
KiCad E.D.A. 8.0.6

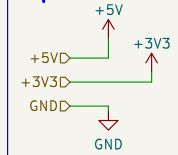
Rev:

Id: 3/12

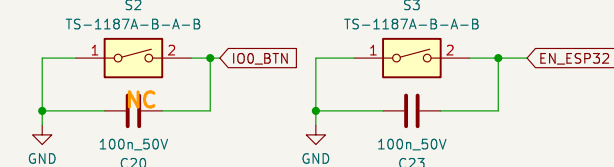




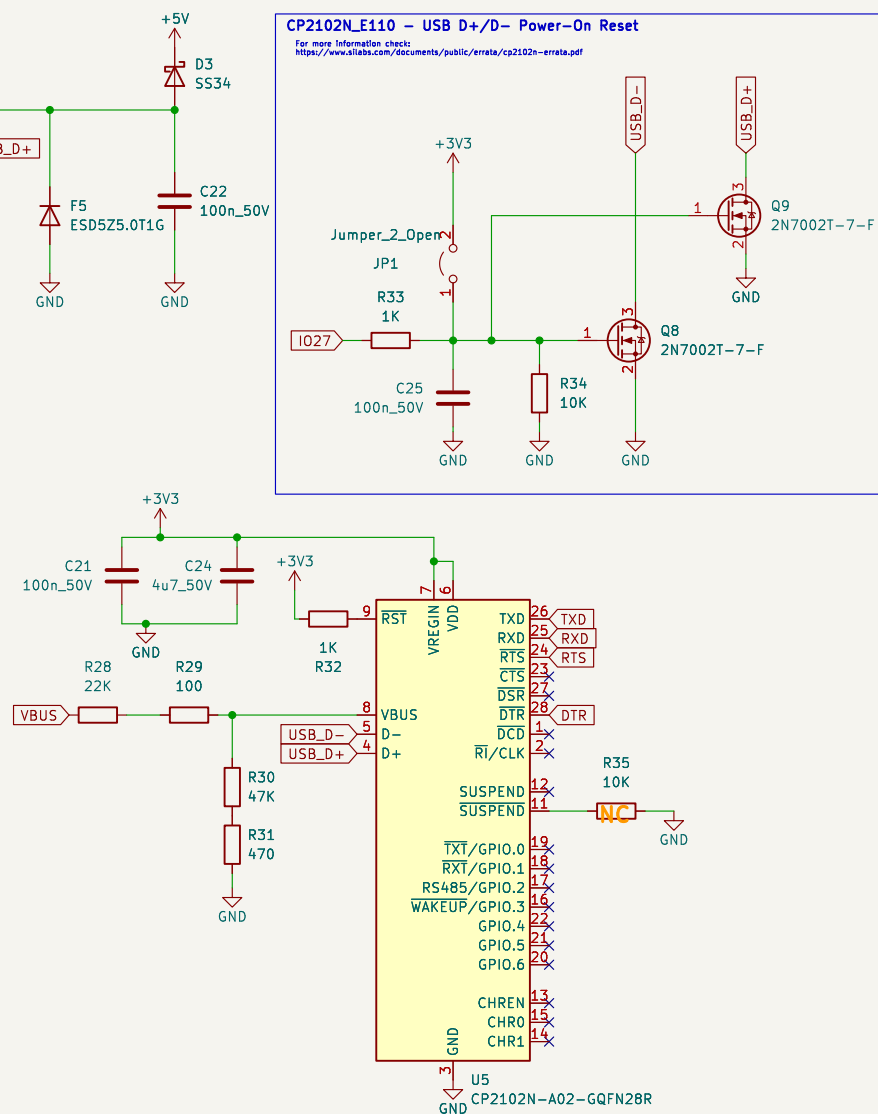
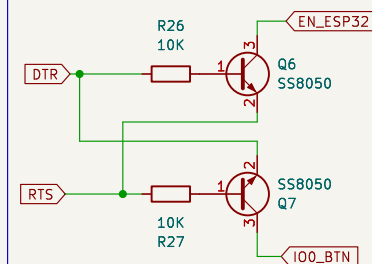
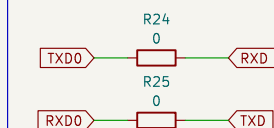
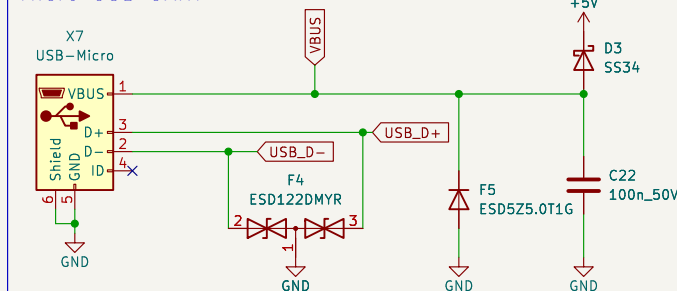




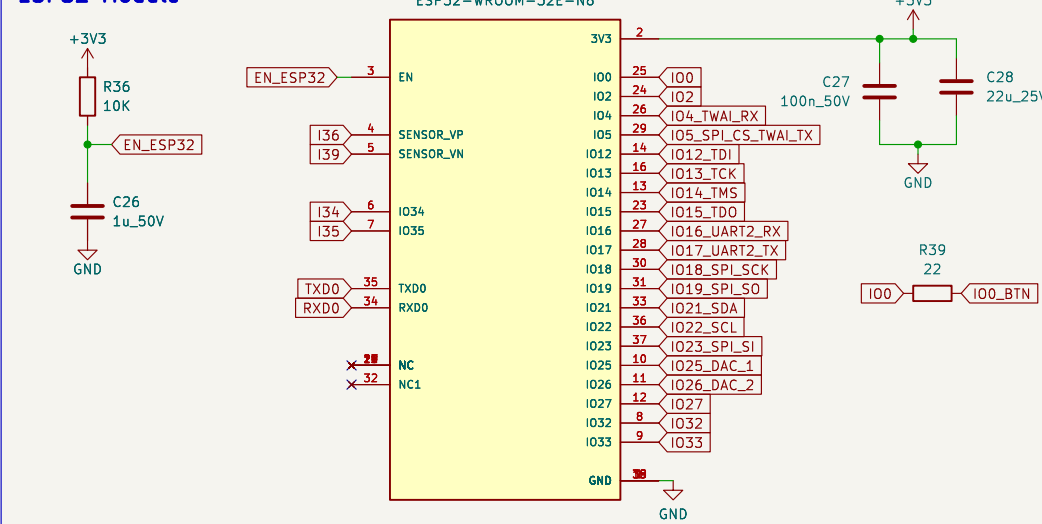
Switches



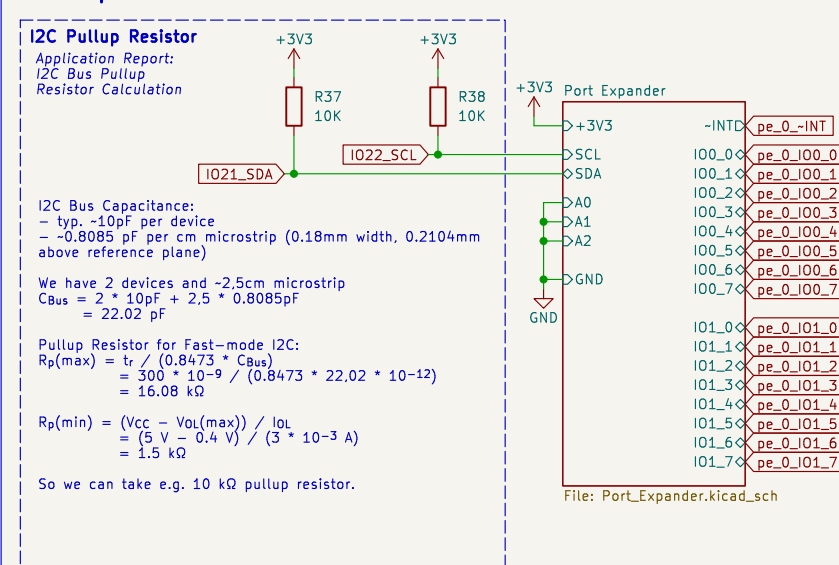
Micro USB UART



ESP32 Module



Port Expander



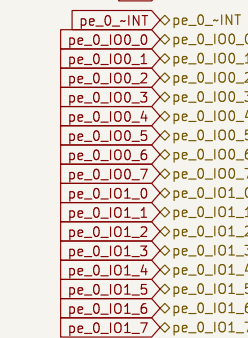
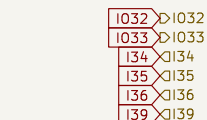
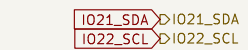
Output



JTAG Debug Interface



More information:
FT232H Debugging ESP32



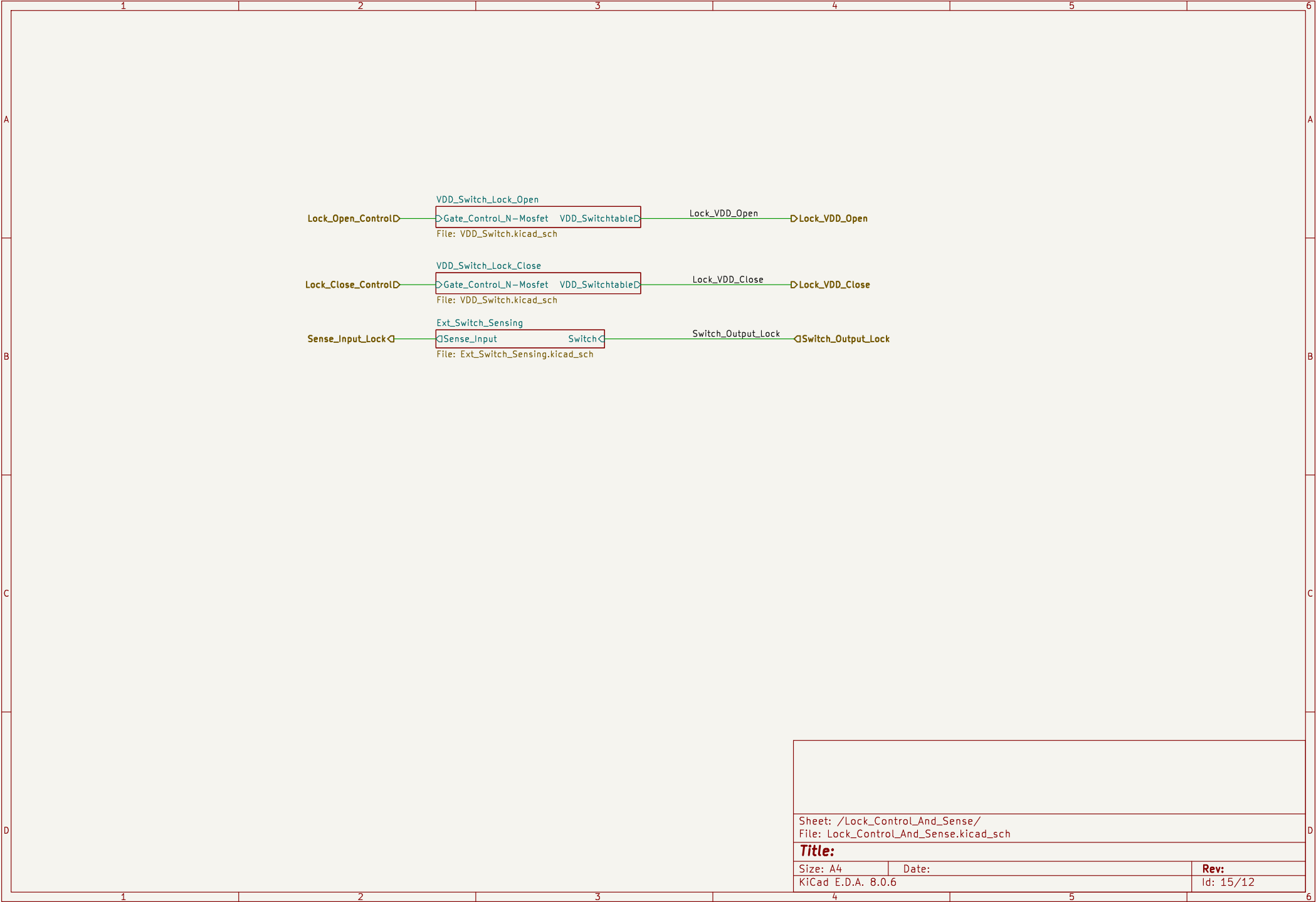
Datasheets & References:
https://www.espressif.com/sites/default/files/documentation/esp32-wroom-32e_esp32-wroom-32ue_datasheet_en.pdf
https://dl.espressif.com/dl/schematics/esp32_devkitc_v4-sch.pdf
https://www.youtube.com/watch?v=5_p0YV-JlfU&t=4185s

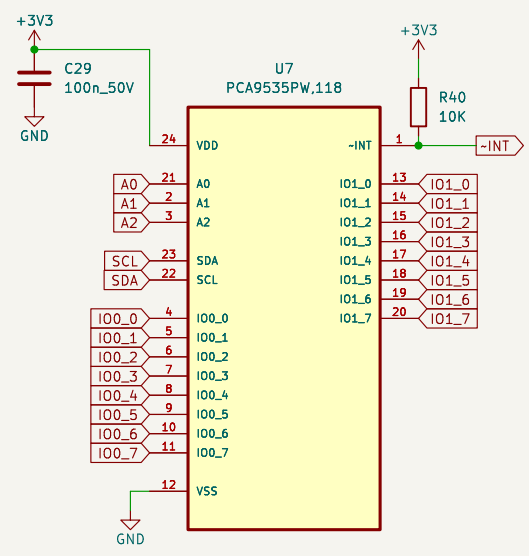
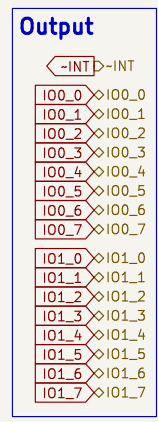
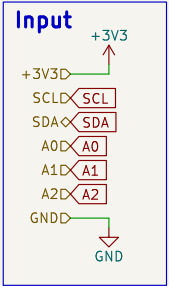
Sheet: /ESP32_WROOM_32/
File: ESP32_WROOM_32.kicad_sch

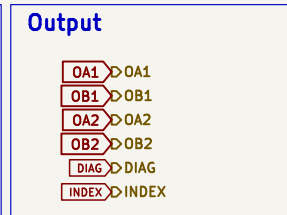
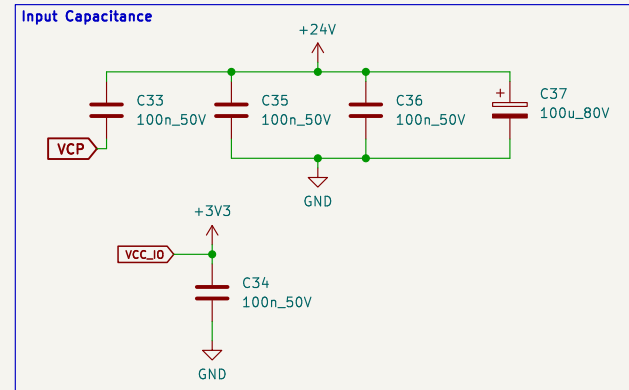
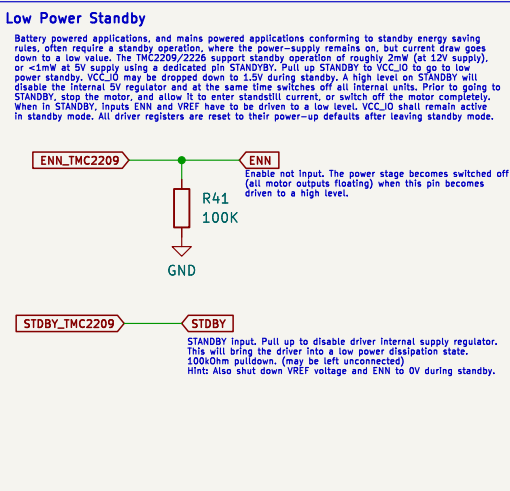
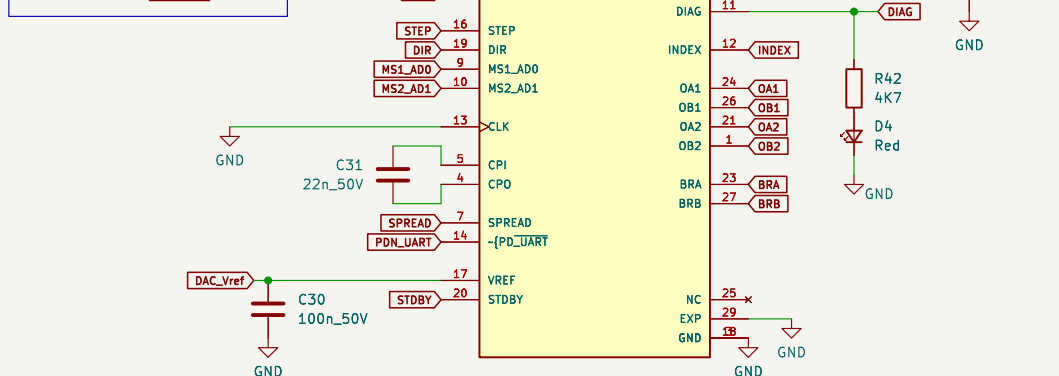
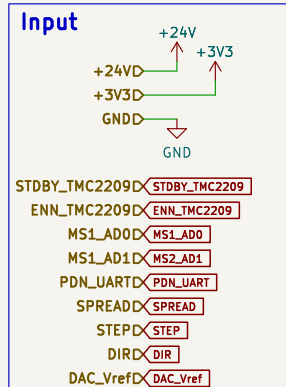
Title:

Size: A3	Date:
KiCad E.D.A. 8.0.6	

Rev:
Id: 7/12







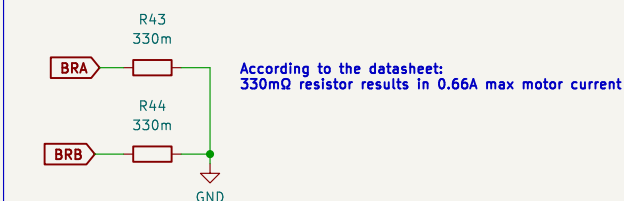
Sense Resistor

Sense resistors should be carefully selected. The full motor current flows through the sense resistors. Due to chopper operation the sense resistors see pulsed current from the MOSFET bridges. Therefore, a low-inductance type such as film or composition resistors is required to prevent voltage spikes causing ringing on the sense voltage inputs leading to unstable measurement results. Also, a low-inductance, low-resistance PCB layout is essential. Any common GND path for the two sense resistors must be avoided, because this would lead to coupling between the two current sense signals. A massive ground plane is best.

The sense resistor needs to be able to conduct the peak motor coil current in motor standstill conditions, unless standby power is reduced. Under normal conditions, the sense resistor conducts less than the coil RMS current, because no current flows through the sense resistor during the slow decay phases. A 0.5W type is sufficient for most applications up to 1.2A RMS current.

Be sure to use a symmetrical sense resistor layout and short and straight sense resistor traces of identical length. Well matching sense resistors ensure best performance.

A compact layout with massive ground plane is best to avoid parasitic resistance effects.



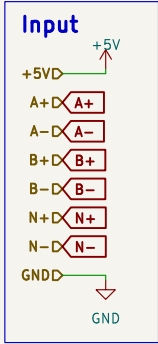
Sheet: /TMC2209/
File: TMC2209.kicad_sch

Title:

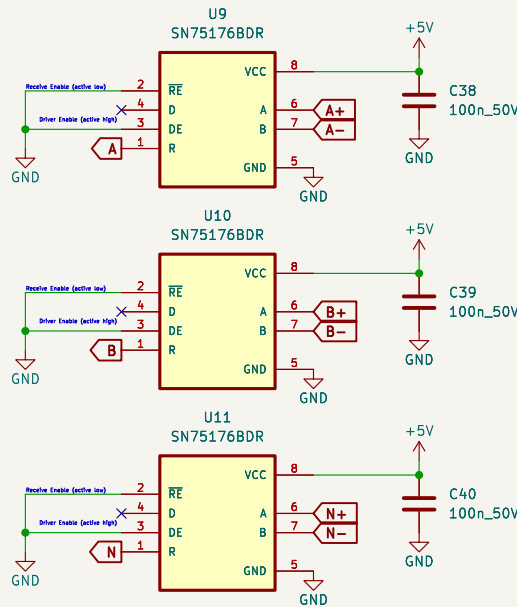
Size: A4	Date:
----------	-------

Rev:

Id: 72/12



Differential Line Receiver



Output



Sheet: /Differential_Line_Receiver/
File: Differential_Line_Receiver.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 8.0.6

Rev:

Id: 74/12

