

2 Input Multiplexer

Name	Value	0 ns	50 ns	100 ns	150 ns	200 ns
In0	0					
In1	1					
s	1					
Z	1					

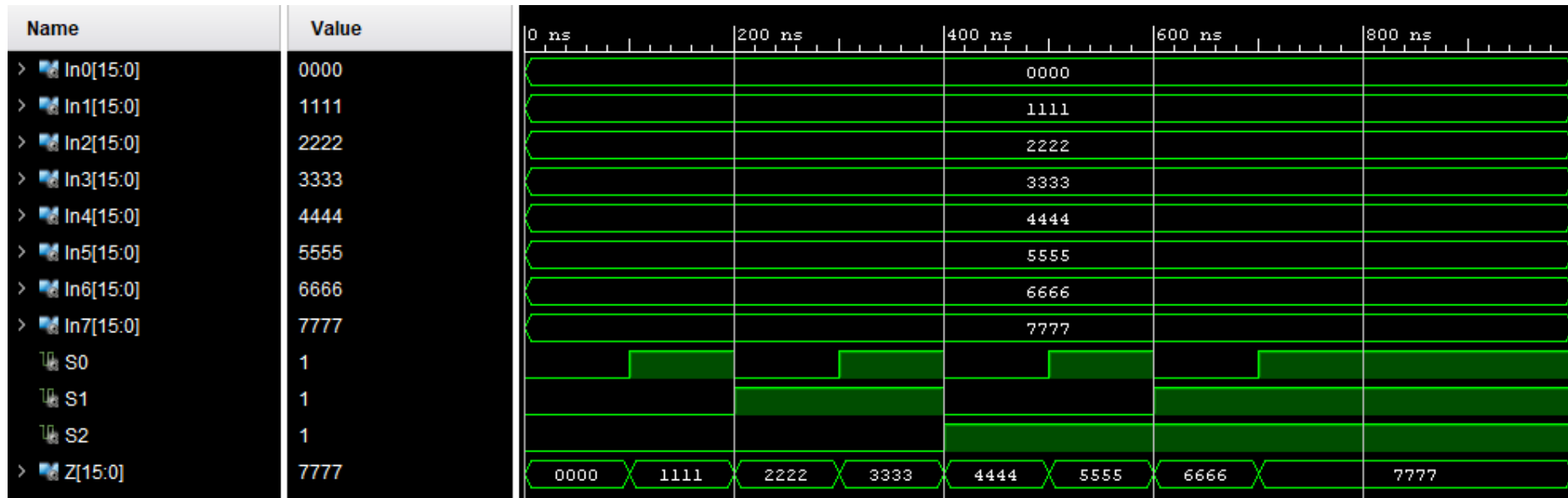
4 Input Multiplexer

Name	Value	0 ns	100 ns	200 ns	300 ns
In0	0				
In1	1				
In2	0				
In3	1				
S0	1				
S1	1				
Z	1				

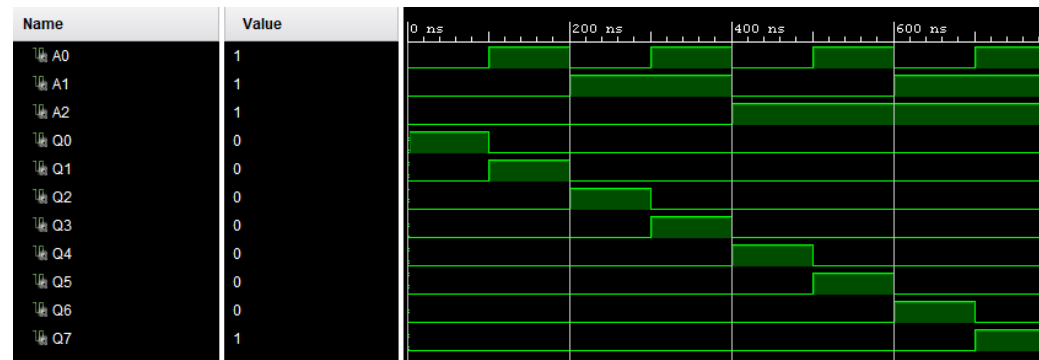
16 Bit 2 Input Multiplexer

Name	Value	0 ns	50 ns	100 ns	150 ns	200 ns	250 ns
In0[15:0]	0000				0000		
In1[15:0]	1111				1111		
s	1						
Z[15:0]	1111						

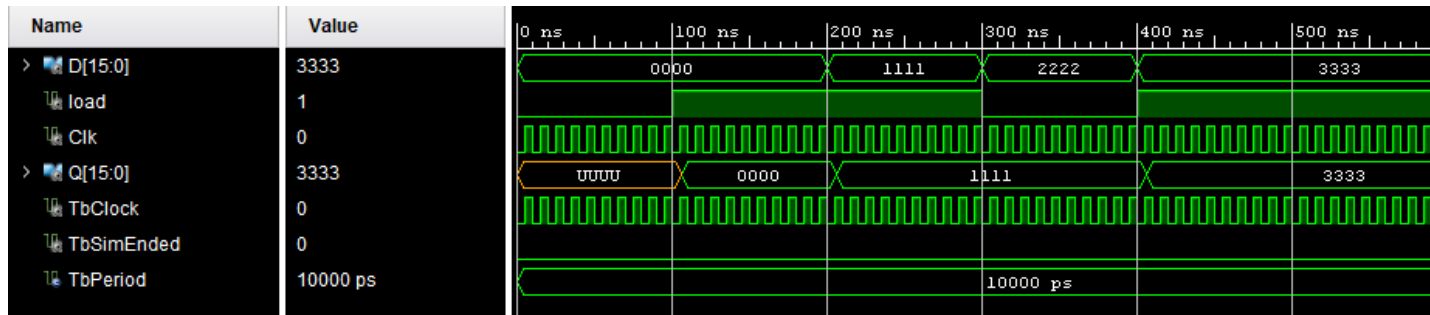
16 Bit 8 Input Multiplexer



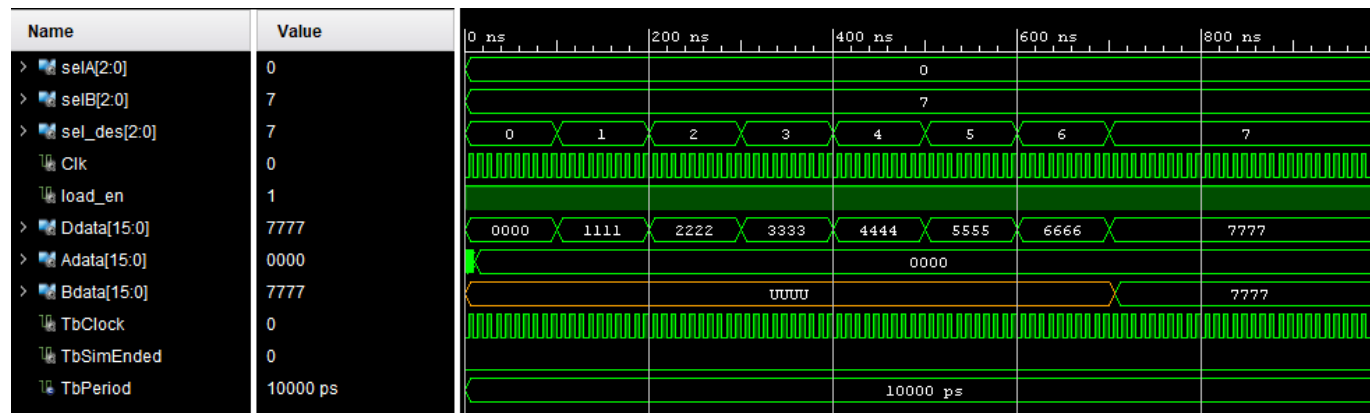
3 To 8 Decoder



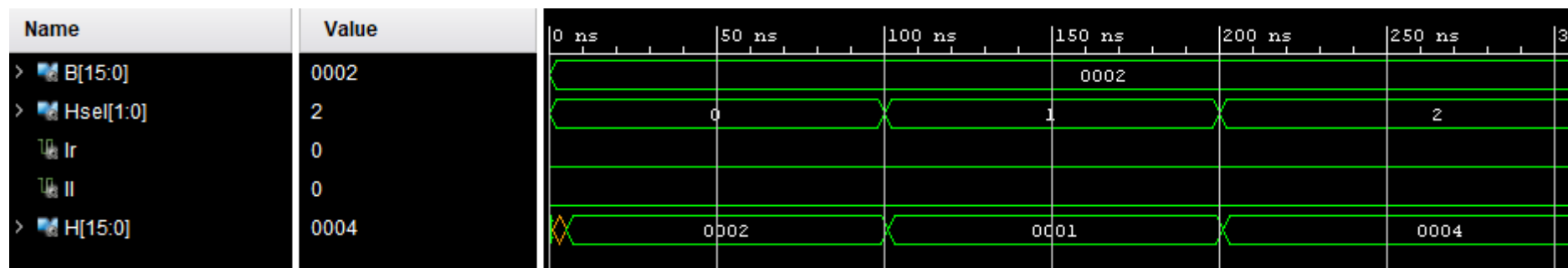
Register



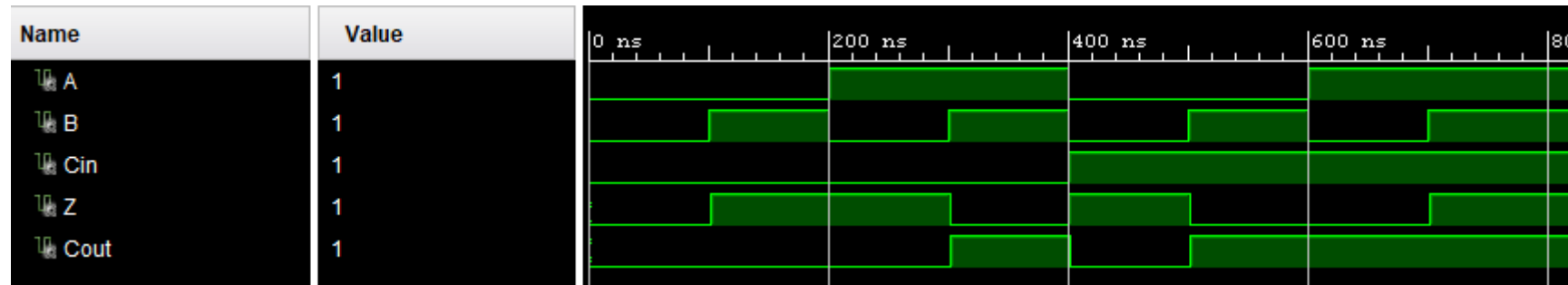
Register File



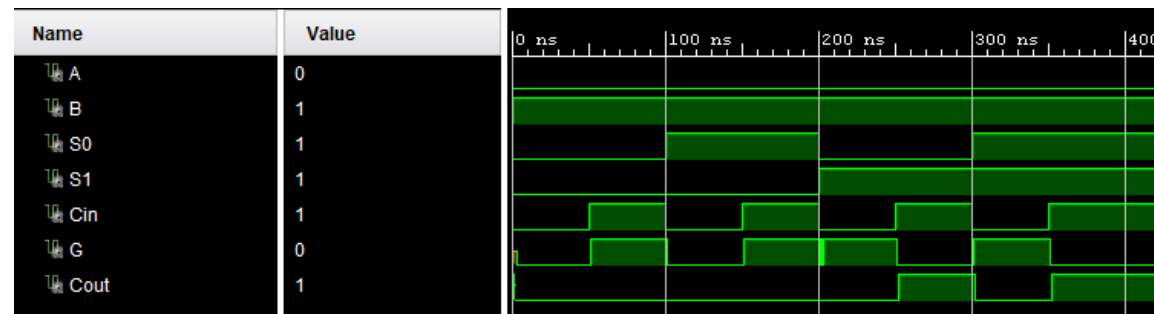
Shifter



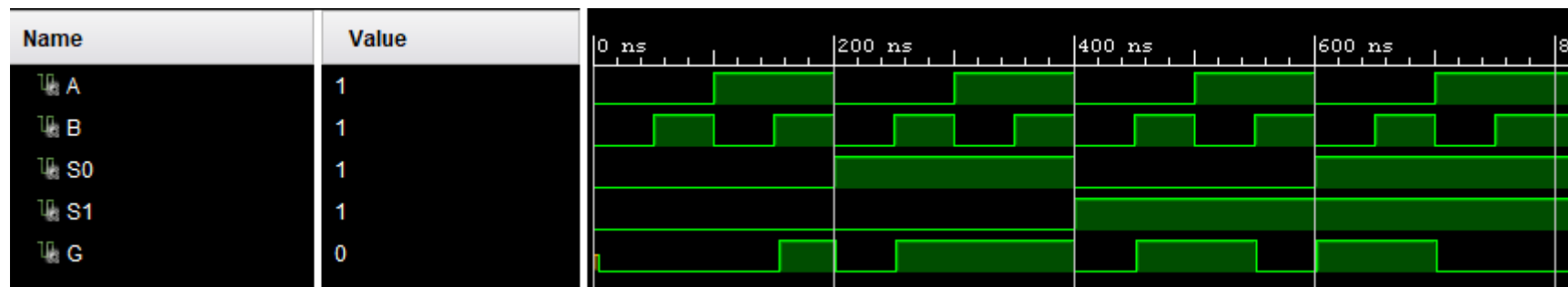
Full Adder



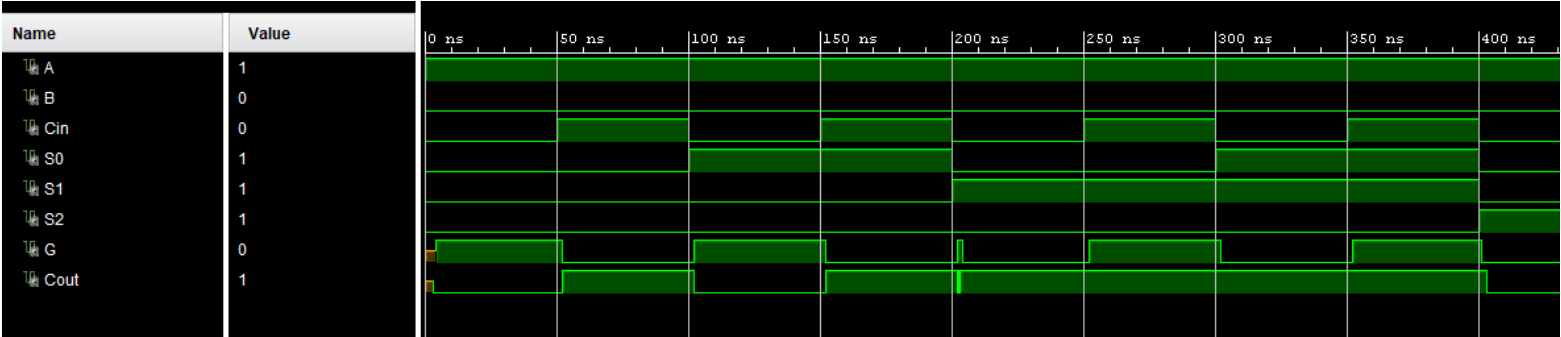
Arithmetic Bit Slice



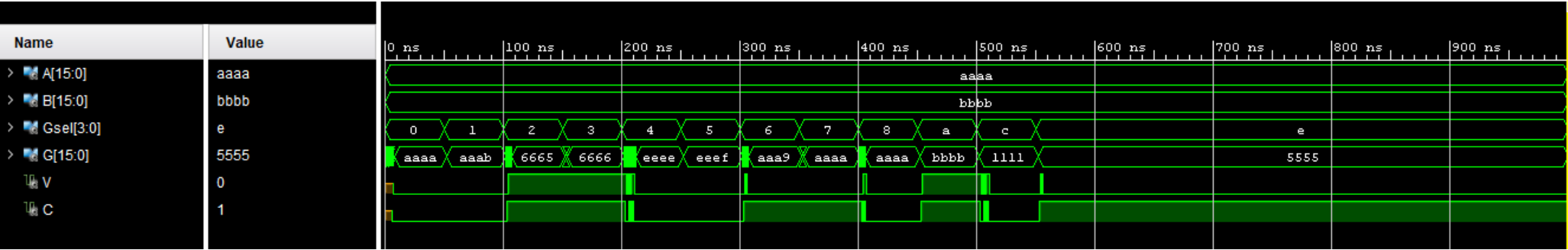
Logic Bit Slice



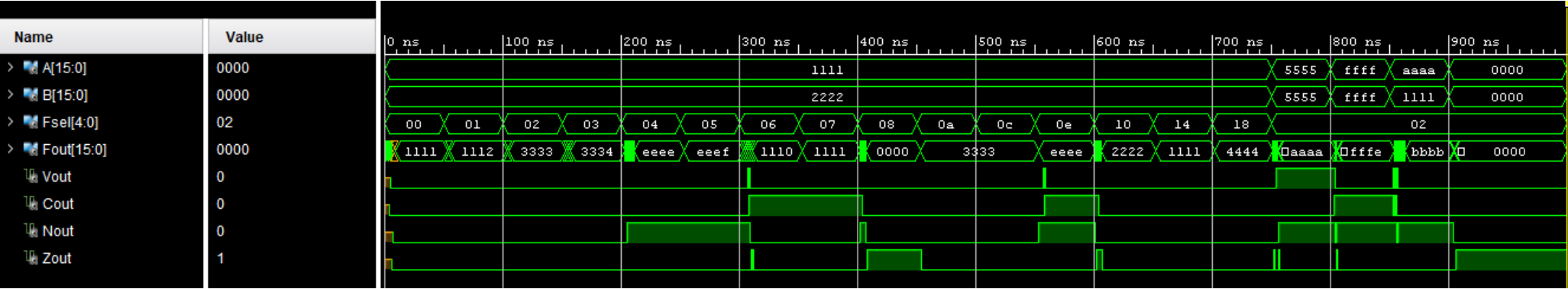
ALU Bit Slice



ALU



Functional Unit



Datapath

