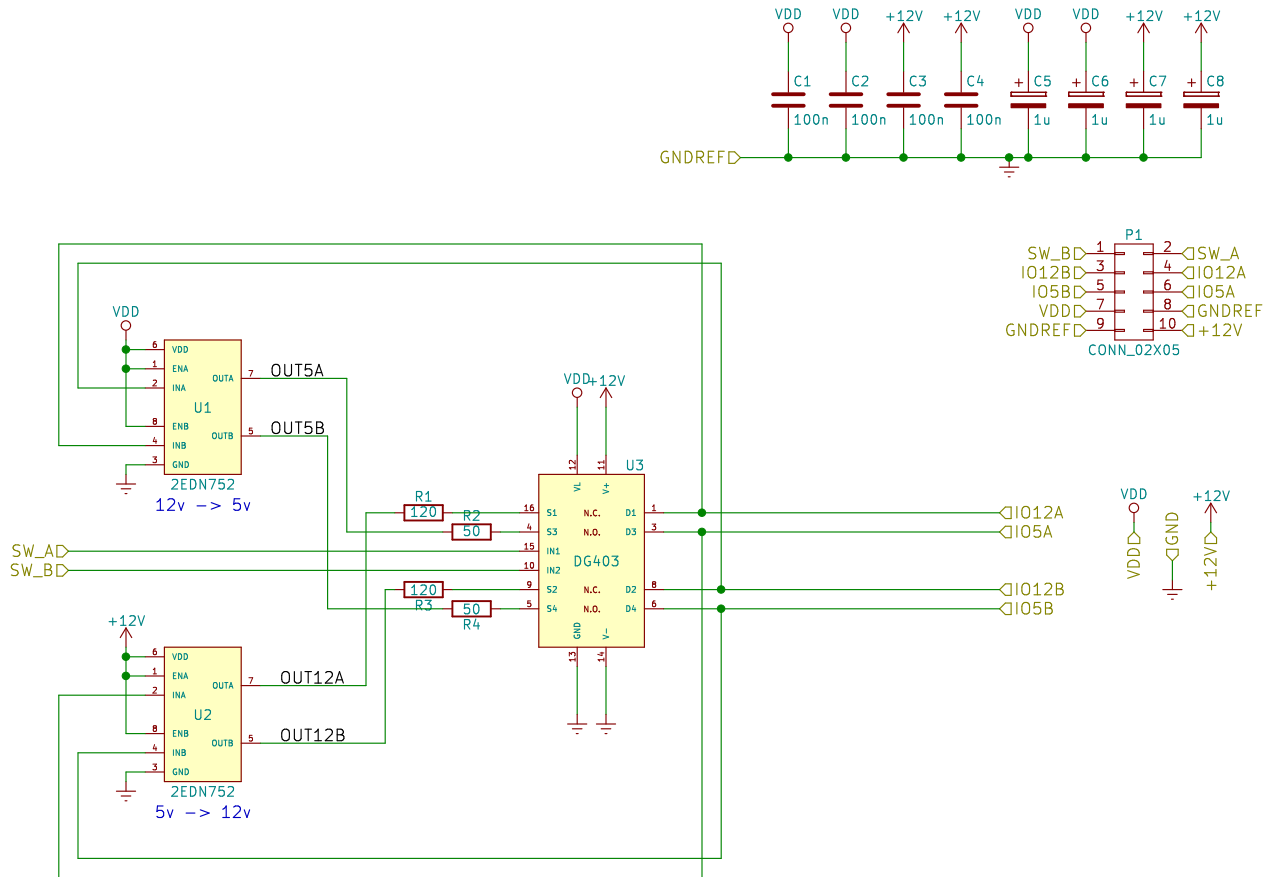


5V/12V UNIDIRECTIONAL LEVEL SHIFTER

VDD is logic-level voltage: 5v or 3v3.

SW_A and SW_B are logic-level inputs. For 1, translation is from IO12 to IO5. For 0, translation is from IO5 to IO12.



Maximum current into a DG403 terminal is 100mA.

For 5v driving some input that is currently at 0v, this means there must be resistance of at least 50 ohms. For 12v, this is 120 ohms.



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Sheet: /
File: FridenTester.sch

Title: 5v/12v Level shifter

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