# Improving the Efficiency of Dislocality Constraints for an Automated Software Mapping in Safety-Critical Systems

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**Abstract:** This is a brief overview of the paper, which should be 70 to 150 words long and include the most relevant points. This has to be a single paragraph.

Keywords: Model-based Systems Engineering; Software Deployment; Constraint Programming

#### 1 Introduction

Engineering complex and safety-critical systems, such as flight control systems aboard an airplane, is still challenging and costly. Despite recent advancements in our model-based tool suites and engineering methods, the design of these systems still bears risk and uncertainties with regard to its outcome. The formalization and automation of crucial engineering tasks appears to be a promising approach to tackle these challenges [Ch07]. Systems in these areas are engineered to implement a complex interplay between mechanical elements, electronic components, as well as (embedded) software. Therefore, their design has to mirror this interplay and requires the development of a hardware and software architecture.

In practice, these architectures can often be developed independent of each other, but their integration in the final system requires a *link* between the software components and their hardware resources. Creating this *link* is referred to as the *deployment* of the software components. Constructing a deployment requires the systems engineer to *map* software components to resources and to *schedule* the access to shared resources. Therefore, mapping refers to a *spatial allocation*, while scheduling refers to a *temporal allocation* of software components.

The construction of a deployment is an engineering task, which not only affects the fulfillment of functional requirements by providing the necessary, but also affects the satisfaction of non-functional requirements, such as safety and reliability. Redundancy and fault tolerance can only be achieved, if critical software components are deployed accordingly.

The construction of a deployment is a very elaborate task with zero tolerance for errors as they may jeopardize the correctness of the system. At the same time, it requires a detailed

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understanding of the requirements of all software components and the capabilities of all hardware resources in the system. Due to the sensitivity and complexity of this task, its formalization and automation is a valuable research goal.

# 2 Automated Construction of Deployments

In order to achieve an automated construction of a deployment and to argue its correctness, a formalization of the mapping problem is required. For smaller mapping problems, this has been successfully achieved based on Linear Integer Programming [Da06; Ku09], SMT-based solvers [VS13] or evolutionary algorithms [Wh11]. However, these approaches reach their limits when larger, real-world mapping problems with limited gradient information to guide a search process are considered. The authors instead chose to transform a mapping problem into a semantically equivalent *Constraint Satisfaction Problem (CSP)* [Ap03] and solve this CSP with *Constraint Programming* techniques [RBW06]. The advantages of using Constraint Programming in comparison to other techniques lie in the availability of powerful modeling elements, such as an ALLDIFFERENT constraint, and the ease with which custom search heuristics can be implemented.

## 2.1 Constraint Satisfaction Problems

Constraint Programming refers to a set of techniques in artificial intelligence and operations research. These techniques assist in finding solutions for problems based on variables, which are affected by constraints. Each constraint defines valid or invalid solutions for a subset of these variables. In this paper, a subclass of constraint satisfaction problems is used to express mapping problems: *finite domain integer constraint satisfaction problems* in which each variable has a finite integer domain. Solutions for this problem class can be obtained by applying a combination of *search* techniques – including backtracking – and constraint *propagation* techniques for value elimination.

To illustrate the modeling approach of Constraint Satisfaction Problems, consider the well-known Map Coloring problem as an example. This problem asks, whether it is possible to color a map with only four colors in such a way, that neighboring countries have different colors. It can be formulated as a CSP by assigning an integer variable  $x_i$  for each country with the index i. The domain of each variable corresponds to the four colors:  $D_{x_i} = \{0, 1, 2, 3\}$ . In order to model the restrictions of this problem, a constraint is added for each pair of adjacent countries. If country  $x_i$  is adjacent to country  $x_j$ , then  $x_i \neq x_j$  is required. The search algorithm is now responsible to select a variable and test a value of its domain. Assuming a simple "first variable, first value" strategy, the variable  $x_0$  would be chosen and set to the value 0 as a test. This would be propagated to all variables which are directly linked to  $x_0$  by a constraint, so that the value 0 gets removed from their domains. This removal may lead to other value removals in indirectly linked variables and is processed

until a fix point is reached. If a contradiction is encountered or the domain of a variable becomes empty, backtracking is initiated, so that the next value of the variable  $x_0$  is tested. Otherwise, the search algorithm continues with the next uninstantiated variable.

This example also shows, that the propagation of the NotEqual constraint is weak, because it affects only two variables and invalidates only 4 out of the 16 possible value combinations between two variables.

#### 2.2 Toolsuite ASSIST

As a proof of concept for the ongoing research toward an automated construction of deployments based on Constraint Satisfaction Problems, the toolsuite Architecture Synthesis for Safety-Critical Systems (ASSIST) [Hi14] was developed by the authors. It is publicly available under the Eclipse Public License 2.0 and uses the constraint solver Choco 4 [PFL16] internally.

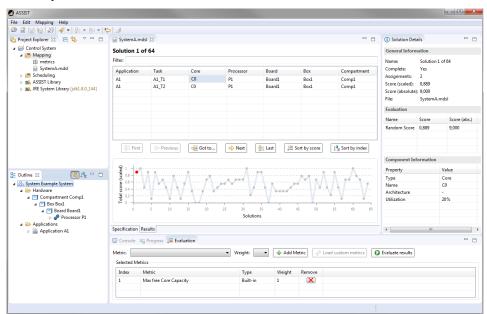


Fig. 1: Screenshot of ASSIST with a specification for a control system

ASSIST (see Figure 1) allows a systems engineer to automatically construct and optimize mappings and schedules based on textual specifications of the software components and hardware resources, dislocality, dissimilarity and colocality requirements, and also optimization goals. The textual specifications in ASSIST conform to a domain-specific

language which allows to hide the intricacies of a formal specification. Using a domain-specific language is expedient to enable systems engineers without a formal education in computer science to precisely specify a deployment problem.

# 3 Ensuring Fault Tolerance by Requiring Dislocality

In order to achieve fault tolerance and reliability, it is essential to support "significant differences" in the choice of resources to which critical software components are deployed to. For instance, a simple redundancy requirement between two software components, may force the systems engineer to allocate these software components to different processing boards in different locations aboard an airplane. Furthermore, systematic errors and undetected design flaws in hardware components may be addressed by choosing dissimilar hardware resources, for example processors or memory blocks from different vendors.

Due to the importance of choosing "different" resources for fault tolerance and reliability in safety-critical systems, engineering tools for an automated construction of deployments need to be able to fully support these choices. ASSIST supports the engineer by offering dislocality and dissimilarity requirements as part of the domain specific language. They can be used to enforce "differences" for the choice of resources for the mapping of software components. Finding an efficient formulation to express the semantics of each requirement as a Constraint Satisfaction Problem is challenging, but also essential in order to provide an effective toolsuite for the engineering of safety-critical systems.

In order to illustrate the challenges and to present specific modeling improvements, the *dislocality* requirement is used as an central example in this paper. Please note, that the concepts developed for *dislocality* requirements can also be reused for *dissimilarity* requirements.

The semantics of a dislocality requirement can be illustrated with the following deployment problem (see Figure 2). In the example system, there are three *applications* consisting of one or more *tasks*. Each task has to be mapped to exactly one of the *processors* in the system. It is assumed, that the processor contains multiple cores, so that multiple tasks can be mapped to a single processor. However, in order to ensure fault tolerance, a *dislocality* requirement is added for all applications. This means, that the applications must not share a processor, so that a faulty processor affects only one application.

Expressing the basic deployment problem with constraints is straight forward. Each task i in the system is represented by an integer variable  $X_i \in \{0, 1, ..., n-1\}$ . The domain of each variable  $X_i$  corresponds to the indices of the n processors in the system. This formulation is still missing the constraints for the dislocality requirements.

In the simple case, if all applications consist of only one task, this can be achieved with a single allDifferent constraint (see [BCR14]) over all variables  $X_i$ . This situation is

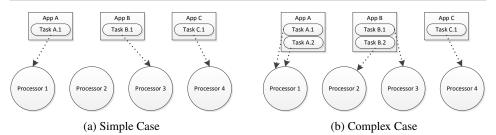


Fig. 2: An Example Deployment Problem - Mapping Tasks to Processors

depicted in Figure 2 (a). Fortunately, Choco 4 already contains an implementation for an allDifferent constraint based on the algorithm of Régin [Ré94].

In real-world systems, applications usually consist of more than just one task and tasks of the same application can typically share the same processor. This situation is more complex and depicted in Figure 2 (b). Unfortunately, in this case, the previous approach of applying a single allDifferent constraint over all variables  $X_i$  can no longer be used. It would prevent solutions in which a processor is shared by multiple tasks of the same application. The existing allDifferent constraint only ensures, that values for a list of variables are different. However, the complex case requires an advanced allDifferent constraint operating with a list of a list of variables and ensuring that the combined values for each list of variables are different. Unfortunately, such a constraint is neither part of the Global Constraint Catalog [BCR14] nor available in Choco 4.

# 4 Modeling Complex Dislocality Requirements

This section introduces three alternative implementations for the semantics of an advanced allDifferent constraint. Their performance and impact on resolution time will be analyzed and compared to each other in the next section.

However, before continuing with the description of possible implementations, the semantics of an advanced allDifferent constraint should be described more precisely. The example system in Figure 2 (b) consists of the applications A, B and C. Each of these applications contains one or more tasks, for example  $A = \{A_1, A_2\}$ ,  $B = \{B_1, B_2\}$  and  $C = \{C_1\}$ . An advanced allDifferent for all applications would be operating over a list of a list of variables, for example:

allDifferent 
$$\{\{A_1, A_2\}, \{B_1, B_2\}, \{C_1\}\}$$

Assuming that  $A^*$  combines the values for each task in application A:

$$A^{\star} \in \mathcal{P}(\mathbb{N}) = A_1 \cup A_2$$

and  $B^*$  and  $C^*$  do the same for the applications B and C, then the advanced allDifferent constraint would ensure, that the sets  $A^*$ ,  $B^*$  and  $C^*$  are pairwise disjunct.

### **Element-wise Approach**

One option to implement the advanced allDifferent constraint semantic is to apply the already existing allDifferent constraint for every subset *s* with

$$s = \{a, b, c \mid a \in A, b \in B, c \in C\}$$

For systems with a large amount of applications and several tasks within each application, the amount of constraints, that will be added to the constraint solver by this approach, may significantly affect the resolution time. However, this approach can be realized with the tools already available in Choco 4 and does not require any additional implementation of custom propagators and constraints<sup>3</sup>.

#### **Instantiation-only Approach**

In order to address the drawbacks of the first approach, another option is to implement a custom constraint and propagator<sup>4</sup> for Choco 4 that is able to operate on a list of a list of variables. The propagator only reacts on instantiation events for any of the variables. If an instantiation is detected, then the value of the instantiated variable will be removed from the values of all variables in all the other lists. This approach has the advantage, that only one constraint needs to be added to the constraint solver for each dislocality requirement. However, this instantiation-only approach leads to a weak propagation as it only removes values, when one of the variables is instantiated. Therefore, this approach relies on the application of clever search strategies in order to be efficient.

# Combined Union-Variables and Instantiation-Only Approach

The third approach tries to build upon the instantiation-only approach and improve the strength of its propagation. For this purpose, a new integer variable  $X^*$  will be added for each list of variables  $X = \{X_1, \ldots, X_n\}$  that were provided to the advanced allDifferent constraint. The new variable  $X^*$  will contain the union of the remaining values of all variables in X. This can be achieved with a custom propagator<sup>5</sup> similar to the already existing PropSetIntValuesUnion propagator in Choco 4. With these new "union variables," being available, adding a simple allDifferent constraint linking all of the union variables, should improve propagation. Please note, that these "union variables" must not be added to

 $<sup>^3 \</sup> https://github.com/RobertHilbrich/assist-public/blob/master/ch.hilbri.assist.mapping/src/ch/hilbri/assist/mapping/solver/constraints/DislocalityConstraint.xtend$ 

<sup>&</sup>lt;sup>4</sup> https://github.com/RobertHilbrich/assist-public/blob/master/ch.hilbri.assist.mapping/src/ch/hilbri/assist/mapping/solver/constraints/choco/PropAllDiffListsOfListsInst.java

https://github.com/RobertHilbrich/assist-public/blob/master/ch.hilbri.assist.mapping/src/ch/hilbri/assist/mapping/solver/constraints/choco/PropIntValuesUnion.java

This approach will be combined with the instantiation-only approach from the previous section. In comparison to the instantiation-only approach, the handling of union-variables requires the addition of a new variable and a propagator for each list of variables, so there is some overhead to be expected.

Based on the description of these three approaches to implement the semantics of an advanced allDifferent constraint, the next sections will describe experiments conducted by the authors and preliminary results to assess and compare the performance and efficiency of each approach.

# 5 Experiments

Benchmarking the performance and efficiency of these approaches requires several example to work on. For this purpose, the authors developed a generator<sup>6</sup> for synthetic mapping problems that resemble the typical characteristics of mapping problems in safety-critical systems. Twenty randomized examples<sup>7</sup> with at least one solution were generated for the experiments. They exhibit the following properties<sup>8</sup>.

In each example, there are *two* compartments, each containing between *four* and *six* boxes. Each box contains between *four* and *six* processing boards and every processing board is equipped with *one* or *two* processors each comprising of up to *four* cores. The examples contain between *16* and *24* applications and each of these applications have between *two* and *eight* tasks that need to be mapped to the cores on the processors. All tasks of an application are required to be placed on the same processing board to allow for memory-based communication (colocality requirement). Finally, every example features between *24* and *32* dislocality requirements and each of these requirements refers to at least *four* and up to *six* applications.

Every example was benchmarked on an Apple iMac 5k with 64GB of RAM by using ASSIST 2.3 and Choco 4.0.6. The experiments were done with the *Domain over Weighted Degree* as a variable selector strategy and *Minimum Value First* as a value selector strategy.

<sup>&</sup>lt;sup>6</sup> https://github.com/RobertHilbrich/assist-public/blob/master/ch.hilbri.assist.mapping. benchmarking/src/ch/hilbri/assist/mapping/benchmarking/generator/MappingExampleGenerator.xtend

<sup>7</sup> https://github.com/RobertHilbrich/assist-public/tree/master/ch.hilbri.assist.mapping.tests/ resources

<sup>8</sup> https://github.com/RobertHilbrich/assist-public/blob/master/ch.hilbri.assist.mapping.tests/src/ch/hilbri/assist/mapping/tests/benchmarking/BenchmarkingTests.xtend

#### 6 Results

The amount of constraints and variables is depicted for each example in Figure 3 and Figure 4. These results show, that the element-wise approach leads to a substantial increase in the amount of constraints in the solver in comparison to the other two approaches. However, the increase of the variable count due to the addition of union-variables in the third approach appears to be rather negligible.

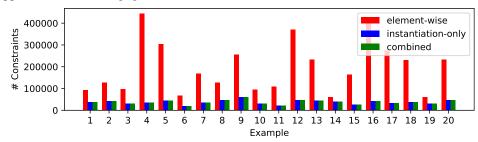


Fig. 3: Amount of constraints in the Choco solver in each example

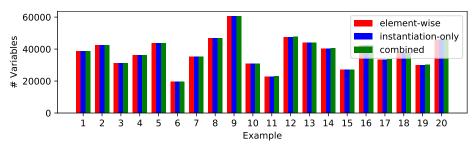


Fig. 4: Amount of variables in the Choco solver in each example

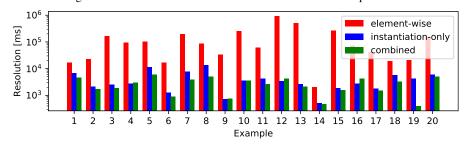


Fig. 5: Resolution time (in ms) to reach the first solution in each example

The total resolution time until the first solution has been reached is depicted in Figure 5. Please note the logarithmic scale on the y-axis. The results show, that the resolution time can be significantly reduced in all examples by adopting the instantiation-only approach. Further improvements to the instantiation-only approach can be achieved by applying the combined approach, but the additional gains are significantly smaller. In some examples,

the combined approach requires slightly more time for a resolution. However, the time differences in these cases are so small, that they may also be induced by external effects, such as garbage collection in Java or scheduling interferences in the operating system.

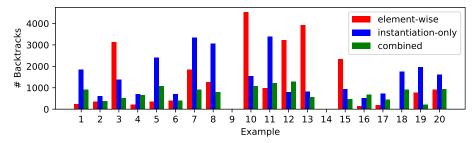


Fig. 6: Amount of backtracking occurring in each example

Backtracking occurs, when the search process in the constraint solver encounters a fail event, i.e. a variable has an empty domain after value propagation or one of the constraints determines a contradiction. Figure 6 shows the amount of backtracking that occurred before finding the first solution in each example. One would assume, that the element-wise approach exhibits the strongest propagation due to the large amount of additional allDifferent constraints, so that the amount of fails and backtracks should be smaller compared to the other approaches. In fact, the results are mixed. Some examples clearly show, that the propagation of the element-wise approach is significantly stronger compared to the other approaches. However, there are also other examples in which the situation is reversed.

#### 7 **Conclusions**

## References

- [Ap03] Apt, K. R.: Principles of constraint programming. Cambridge University Press, 2003.
- [BCR14] Beldiceanu, N.; Carlsson, M.; Rampo, J.-X.: Global Constraint Catalog, Technical Report T2012:03, ISSN: 1100-3154, SICS, Feb. 2014, URL: http: //www.emn.fr/z-info/sdemasse/gccat/.
- [Ch07] Chapman, R.: Correctness by construction: putting engineering (back) into software. In: Proceedings of the 2007 ACM international conference on SIGAda annual international conference. SIGAda '07, ACM, Fairfax, Virginia, USA, pp. 100-100, 2007, ISBN: 978-1-59593-876-3, URL: http://doi.acm.org/10. 1145/1315580.1315605.

- [Da06] Damm, W.; Metzner, A.; Eisenbrand, F.; Shmonin, G.; Wilhelm, R.; Winkel, S.: Mapping Task-Graphs on Distributed ECU Networks: Efficient Algorithms for Feasibility and Optimality. In: Embedded and Real-Time Computing Systems and Applications, 2006. Proceedings. 12th IEEE International Conference on. Pp. 87–90, 2006.
- [Hi14] Hilbrich, R.: Architecture Synthesis for Safety Critical Systems ASSIST, online, 2014, URL: http://github.com/roberthilbrich/assist-public.
- [Ku09] Kugele, S.; Haberl, W.; Tautschnig, M.; Wechs, M.: Optimizing Automatic Deployment Using Non-functional Requirement Annotations. In (Margaria, T.; Steffen, B., eds.): Leveraging Applications of Formal Methods, Verification and Validation. Vol. 17, Communications in Computer and Information Science, Springer Berlin Heidelberg, pp. 400–414, 2009, ISBN: 978-3-540-88478-1, URL: http://dx.doi.org/10.1007/978-3-540-88479-8\_28.
- [PFL16] Prud'homme, C.; Fages, J.-G.; Lorca, X.: Choco Documentation, TASC, INRIA Rennes, LINA CNRS UMR 6241, COSLING S.A.S., 2016, URL: http://www.choco-solver.org.
- [RBW06] Rossi, F.; van Beek, P.; Walsh, T., eds.: Handbook of Constraint Programming. ELSEVIER SCIENCE & TECHNOLOGY, 2006.
- [Ré94] Régin, J.-C.: A Filtering Algorithm for Constraints of Difference in CSPs. In: Proceedings of the Twelfth National Conference on Artificial Intelligence (Vol. 1). AAAI '94, American Association for Artificial Intelligence, Seattle, Washington, USA, pp. 362–367, 1994, ISBN: 0-262-61102-3, URL: http://dl.acm.org/citation.cfm?id=199288.178024.
- [VS13] Voss, S.; Schatz, B.: Deployment and Scheduling Synthesis for Mixed-Critical Shared-Memory Applications. In: Engineering of Computer Based Systems (ECBS), 2013 20th IEEE International Conference and Workshops on the. Pp. 100–109, 2013.
- [Wh11] White, J.; Dougherty, B.; Thompson, C.; Schmidt, D. C.: ScatterD: Spatial deployment optimization with hybrid heuristic/evolutionary algorithms. TAAS 6/3, p. 18, 2011.