These expressions are inside of input\_diff\_length.txt.

[20 pts]

* Different variable length:

V\_a<<v\_A

aBc\_-Var

Ram+Zxy\_

These expressions are inside of input\_simple\_expressions.txt.

[22 pts]

* REG-REG:

a\*a // MUL a with a.

* REG-CONST:

a\*23 // ADDI 23 to register and then MUL.

b/3 // ADDI 3 to registers and then DIV.

c-7 // ADDI -7 instead of moving 7 to reg and then SUB.

d+987 // ADDI d and 987.

* CONST-REG:

4 / f // Add 4 to register then SLL.

* CONST-CONST:

4 >> 2 // 4 >> 2 = 1 (Do not use registers!).

* UNARY:

(-d)\*d // SUB d with X0.

80^-1 // -81 (Do not use registers!).

(~c)/c // XORI c with -1.

~230 // ~230 (Do not use registers!).

These expressions are inside of input\_strong\_reduction.txt.

[22 pts]

* Strong reduction:

a\*2 // a << 1 (reg-const)

c/8 // c >> 3 (reg-const)

4\*b // b << 2 (const-reg)

These expressions are inside of input\_load\_upper\_imm.txt.

[10 pts]

* LUI:

e+6143 // LI 4096 & ADDI 2047

f/-6143 // LI -4096 & ADDI -2047

-6145<<c // LI -8192 & ADDI 2047

6145\*a // LI 8192 & ADDI -2047