Post dominance root node

```
%0:
%args = alloca { i64, { i64, i8* }* }, align 8
%.frame = alloca %nest.main, align 8
store { i64, { i64, i8* }* } %unnamed, { i64, { i64, i8* }* }* %args
%Size = getelementptr inbounds %nest.main, %nest.main* %.frame, i32 0, i32 0
\%1 = \text{getelementptr inbounds} \{ i64, \{ i64, i8* \}^* \}, \{ i64, \{ i64, i8* \}^* \}^* \}
... %args, i32 0, i32 0
% .len = load i64, i64* %1
store i64 %.len, i64* %Size
%2 = load i64, i64* %Size
%3 = bitcast %nest.main* %.frame to i8*
\%4 = \text{insertvalue} \{ i8^*, i32 (i8^*)^* \} \text{ undef}, i8^* \%3, 0 \}
%.func = insertvalue { i8*, i32 (i8*)* } %4, i32 (i8*)*
... @_D12D_SimpleLazy4mainFAAyaZ12__dgliteral2MFNaNiNfZi, 1
%5 = call i32 @ D12D SimpleLazy3fooFimLiZi({ i8*, i32 (i8*)* } %.func, i64
... %2, i32 1) #0
call void @ D3std5stdio T7writelnTiZQlFNfiZv(i32 %5) #1
ret i32 0
```

Post dominator tree for '\_Dmain' function