

Post dominance root node



```
graph TD; A[Post dominance root node] --> B["%0:  
%1 = bitcast i8* %.nest_arg to %nest.main*  
%Size = getelementptr inbounds %nest.main, %nest.main* %1, i32 0, i32 0  
%2 = load i64, i64* %Size  
%3 = call i32 @_D12D_SimpleLazy3barFmZi(i64 %2) #0  
ret i32 %3"]; style A fill:#fff,stroke:#000,stroke-width:1px; style B fill:#fff,stroke:#000,stroke-width:1px;
```

%0:

%1 = bitcast i8* %.nest_arg to %nest.main*

%Size = getelementptr inbounds %nest.main, %nest.main* %1, i32 0, i32 0

%2 = load i64, i64* %Size

%3 = call i32 @_D12D_SimpleLazy3barFmZi(i64 %2) #0

ret i32 %3

Post dominator tree for '_D12D_SimpleLazy4mainFAAyaZ12__dgliteral2MFNaNiNfZi' function