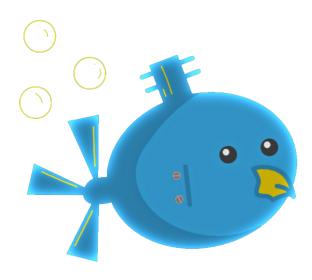
# California State University, Los Angeles

# **Software Design Document**



#### RoboSub

#### Members

Thomas Benson, David Camacho, Bailey Canham, Brandon Cao, Roberto Hernandez, Andrew Heusser, Hector Mora-Silva, Bart Rando, Victor Solis

## **Table of Contents**

| Table of Contents |  |  |  |  |  |
|-------------------|--|--|--|--|--|
| Re                | evision History  | 3  |  |  |  |
| 1                 | Introduction1.1 Purpose1.2 Document Conventions1.3 Intended Audience and Reading Suggestions1.4 System Overview  | 4<br>4<br>4<br>4<br>4  |  |  |  |
| 2                 | Design Considerations2.1 Assumptions and Dependencies2.2 General Constraints2.3 Goals and Guidelines2.4 Development Methodology  | 4<br>4<br>4<br>4   |  |  |  |
| 3                 | Architectural Representation   | 4  |  |  |  |
| 4                 | System Architecture  | 4  |  |  |  |
| 6                 | Policies and Tactics  5.1 Specific Product Used 5.2 Requirements Traceability 5.3 Testing the Software 5.4 Engineering Trade-Offs 5.5 Guidelines and Convetions 5.6 Protocols 5.7 Maintianing the Software 5.8 Interfaces 5.9 System Deliverables 5.10 Abstractions  Detailed System Design 6.1 Name of Module 6.1.1 Responsibilities 6.1.2 Contraints 6.1.3 User Interactions 6.1.4 Resources 6.1.5 Interface/Exports | 4<br>4<br>4<br>4<br>4<br>4<br>4<br>4<br>4<br>6<br>6<br>6<br>6<br>6<br>6<br>6 |  |  |  |
| 7                 | Detailed Lower Level Component Design 7.1 Name of Class or File  | 6<br>6<br>6<br>6<br>6<br>6<br>6<br>6<br>6                                    |  |  |  |

| 8  | 8 Detailed Lower Level Component                                 | t Design       | 6 |  |  |  |
|----|--|----------------|---|--|--|--|
|    | 8.1 Name of Class or File  |                | 6 |  |  |  |
|    |  |                | 6 |  |  |  |
|    |  | PSPEC)         | 6 |  |  |  |
|    |  |                | 6 |  |  |  |
|    | - C  |                | 6 |  |  |  |
|    |  | ny             | 6 |  |  |  |
|    |  | ons            | 6 |  |  |  |
|    |  |                | 6 |  |  |  |
|    | - C  |                | 6 |  |  |  |
|    | 8.1.9 Processing Detail For I                                    | Each Operation | 6 |  |  |  |
| 9  | 9 User Interface   |                | 6 |  |  |  |
| ,  |  |                | 6 |  |  |  |
|    |  | S              | 6 |  |  |  |
|    | · ·  | S              | 6 |  |  |  |
|    | 716 0001 11101111100 110 11 2 11 G 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 |                | Ü |  |  |  |
| 10 | 10 User Interface  |                | 6 |  |  |  |
|    | 10.1 Overview of User Interface                                  |                | 6 |  |  |  |
|    | 10.2 Screen Frameworks or Images                                 | 8              | 6 |  |  |  |
|    | 10.3 User Interface Flow Diagrams                                | S              | 6 |  |  |  |
| 11 | 11 Database Design   |                | 6 |  |  |  |
| 10 | 10 Detakasa Darian   |                | , |  |  |  |
| 12 | 12 Database Design   |                | 6 |  |  |  |
| 13 | 3 Requirements Validation and Verification                       |                |   |  |  |  |
| 14 | 14 Requirements Validation and Verification                      |                |   |  |  |  |
| 15 | 15 Glossary  |                |   |  |  |  |
| 16 | 16 Glossary  |                | 6 |  |  |  |
|    | ·  |                |   |  |  |  |
| 17 | 17 References  |                | 6 |  |  |  |
| 18 | 18 References  |                | 6 |  |  |  |

# **Revision History**

| Version | Description                                | Date            |
|---------|--|-----------------|
| 1.0     | First release of Software Design Document. | 9 December 2022 |

Table 1: Revision History

#### 1 Introduction

- 1.1 Purpose
- 1.2 Document Conventions
- 1.3 Intended Audience and Reading Suggestions
- 1.4 System Overview
- 2 Design Considerations
- 2.1 Assumptions and Dependencies
- 2.2 General Constraints
- 2.3 Goals and Guidelines
- 2.4 Development Methodology
- 3 Architectural Representation
- 4 System Architecture
- 5 Policies and Tactics
- 5.1 Specific Product Used
- 5.2 Requirements Traceability
- 5.3 Testing the Software
- 5.4 Engineering Trade-Offs
- 5.5 Guidelines and Convetions
- 5.6 Protocols
- 5.7 Maintianing the Software
- 5.8 Interfaces
- 5.9 System Deliverables
- 5.10 Abstractions

### 6 Detailed System Design

- 6.1 Name of Module
- 6.1.1 Responsibilities
- 6.1.2 Contraints
- 6.1.3 User Interactions
- 6.1.4 Resources
- 6.1.5 Interface/Exports

### 7 Detailed Lower Level Component Design

- 7.1 Name of Class or File
- 7.1.1 Classifications
- 7.1.2 Processing Narrative(PSPEC)
- 7.1.3 Interface Description
- 7.1.4 Processing Details
- 7.1.5 Design Class Heirarchy
- 7.1.6 Restrictions/Limitations
- 7.1.7 Performance Issues
- 7.1.8 Design Contraints
- 7.1.9 Processing Detail For Each Operation
- 8 User Interface
- 8.1 Overview of User Interface
- 8.2 Screen Frameworks or Images
- 8.3 User Interface Flow Diagrams
- 9 Database Design
- 10 Requirements Validation and Verification
- 11 Glossary
- 12 References