

[illegible]

Output Selector

The diagram illustrates an output selector circuit. It includes a 5V input (V_Bu5D) connected to a 100k resistor (R1) and the anode of a diode (D2, PMEG40130ERX 40V 3A). The other end of R1 and the cathode of D2 are connected to ground (GND). The diode's anode is also connected to a 5V input (V_BOOSTQ) through a 1N4148 diode (D1). The output of the circuit is taken from a 5V pin (5V) which is connected to a 1N4148 diode (D2) and a 100k resistor (R2) to ground. The output is also connected to a 5V pin (5V) which is connected to a 1N4148 diode (D2) and a 100k resistor (R2) to ground. The output is also connected to a 5V pin (5V) which is connected to a 1N4148 diode (D2) and a 100k resistor (R2) to ground.

Connector

VCC

1
2
3
4
5
6
7
8
9
10

V_BUS
V_BATT
BAT1_5V
NEO_OUT1
NEO_IN1
3.3V
5V


J4 Conn_01x10_Socket

GND

3.3V Reg

Neopixel Level Shifter

Batt Sig



The diagram shows a circuit for monitoring a battery. A green line represents the battery connection, starting from a ground symbol (GND) at the bottom. It goes up through a red rectangular component labeled R9 100. After R9, the line splits: one branch goes right to a red circular component labeled 10K, and the other branch continues up through another red rectangular component labeled R10 6.8k. The output of R10 is labeled D BATT_SIG.

