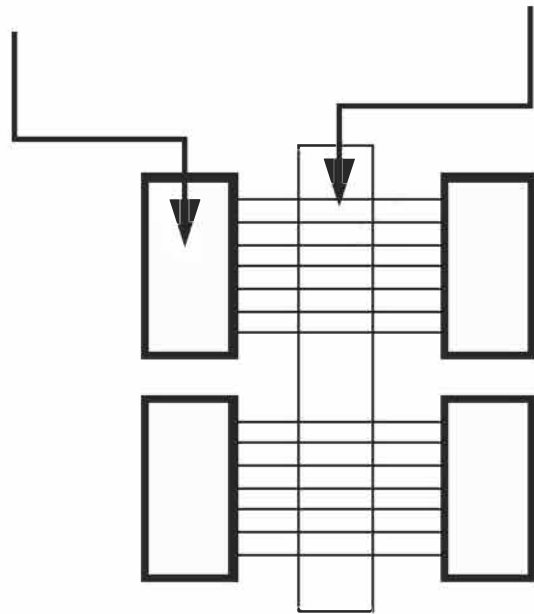


PLD Blocks

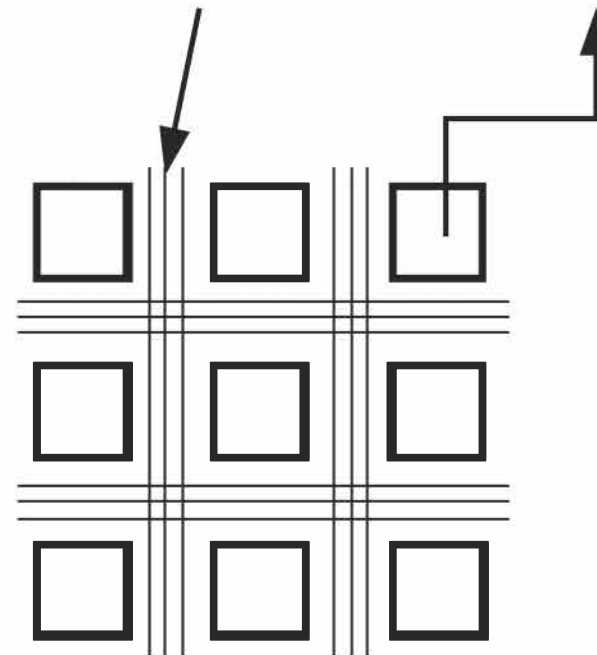
Interconnections



HIERÁRCHICAL PLD

Interconnections

Logic Blocks



SYMETRICAL MATRIX