

4 Terminal Configuration and Functions

4.1 Pin Diagram – RGZ Package (Top Side View)

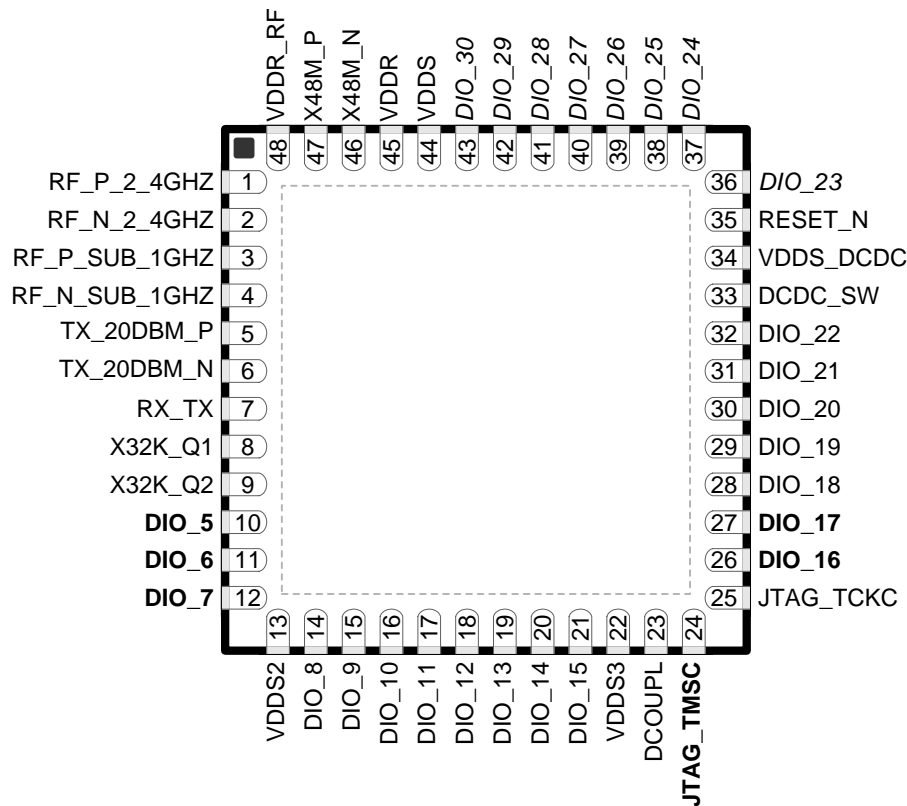


Figure 4-1. RGZ (7-mm x 7-mm) Pinout, 0.5-mm Pitch

The following I/O pins marked in Figure 4-1 in **bold** have high-drive capabilities:

- Pin 10, **DIO_5**
- Pin 11, **DIO_6**
- Pin 12, **DIO_7**
- Pin 24, **JTAG_TMSC**
- Pin 26, **DIO_16**
- Pin 27, **DIO_17**

The following I/O pins marked in Figure 4-1 in *italics* have analog capabilities:

- Pin 36, *DIO_23*
- Pin 37, *DIO_24*
- Pin 38, *DIO_25*
- Pin 39, *DIO_26*
- Pin 40, *DIO_27*
- Pin 41, *DIO_28*
- Pin 42, *DIO_29*
- Pin 43, *DIO_30*

3 Terminal Configuration and Functions

3.1 Pin Diagram

Figure 3-1 shows pin names and locations for the CC1125 device.

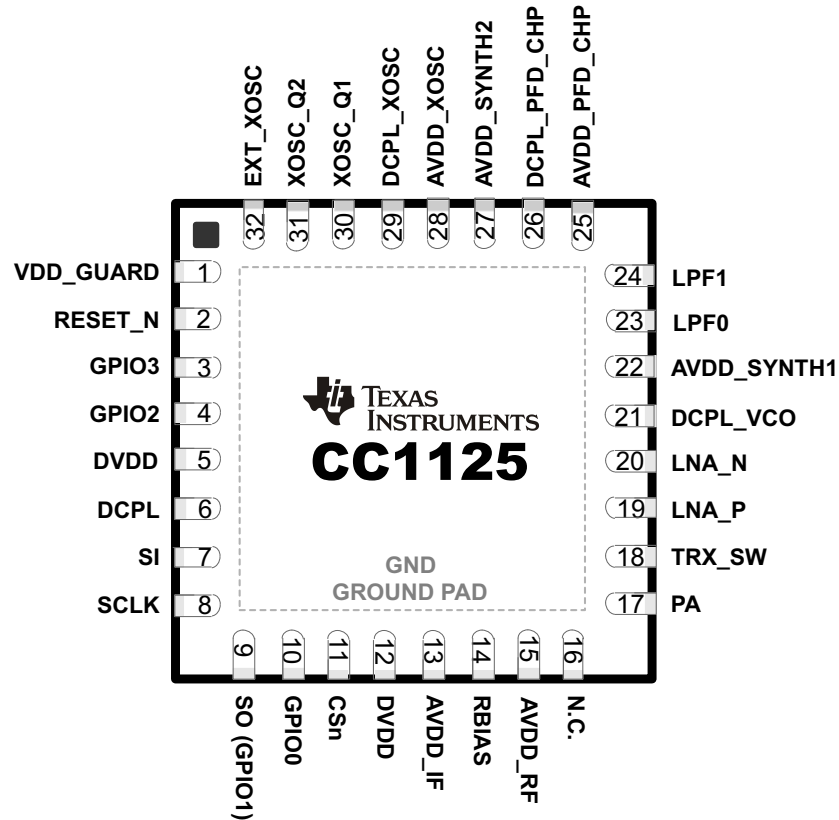
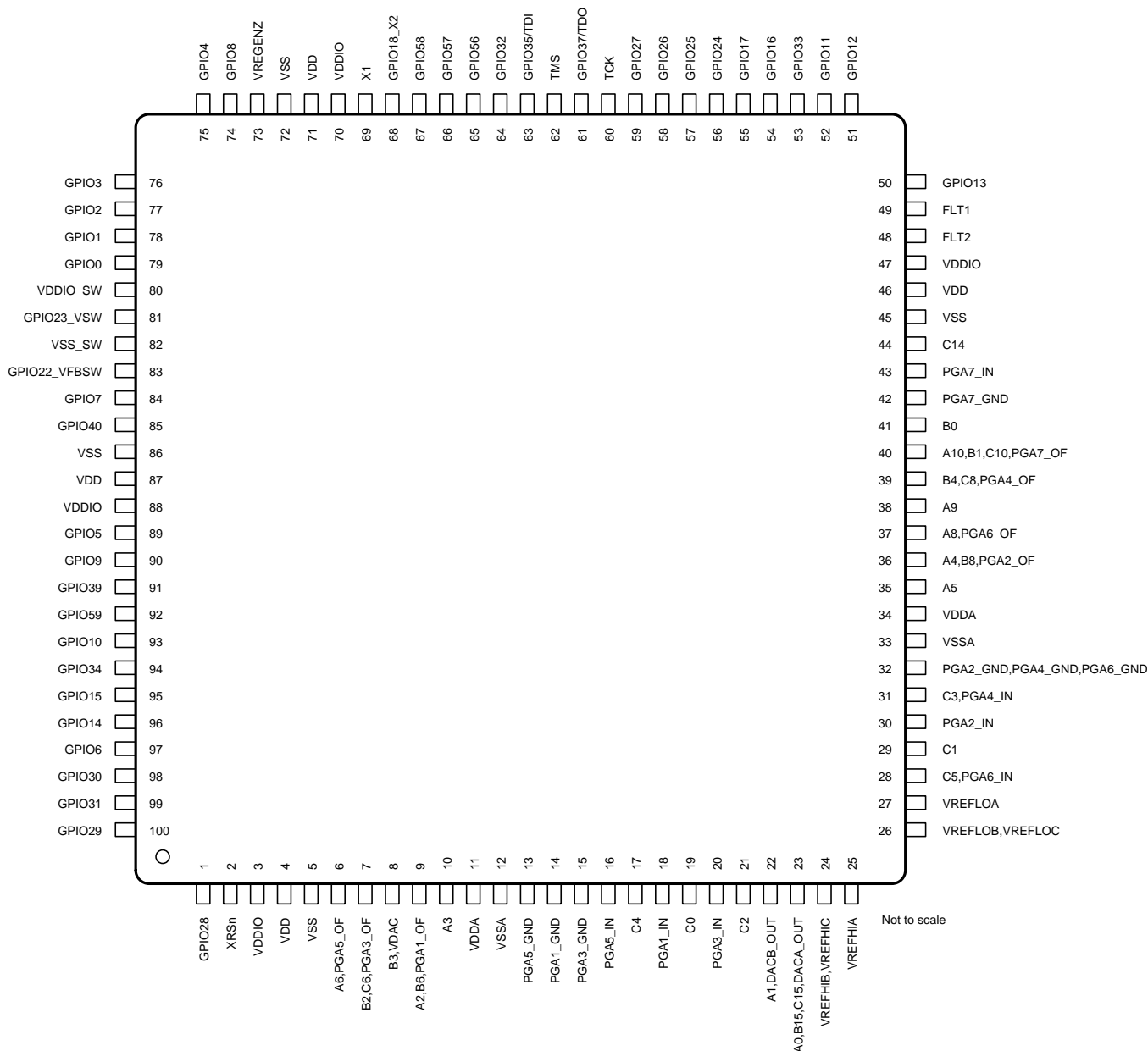


Figure 3-1. Package 5-mm × 5-mm QFN

4 Terminal Configuration and Functions

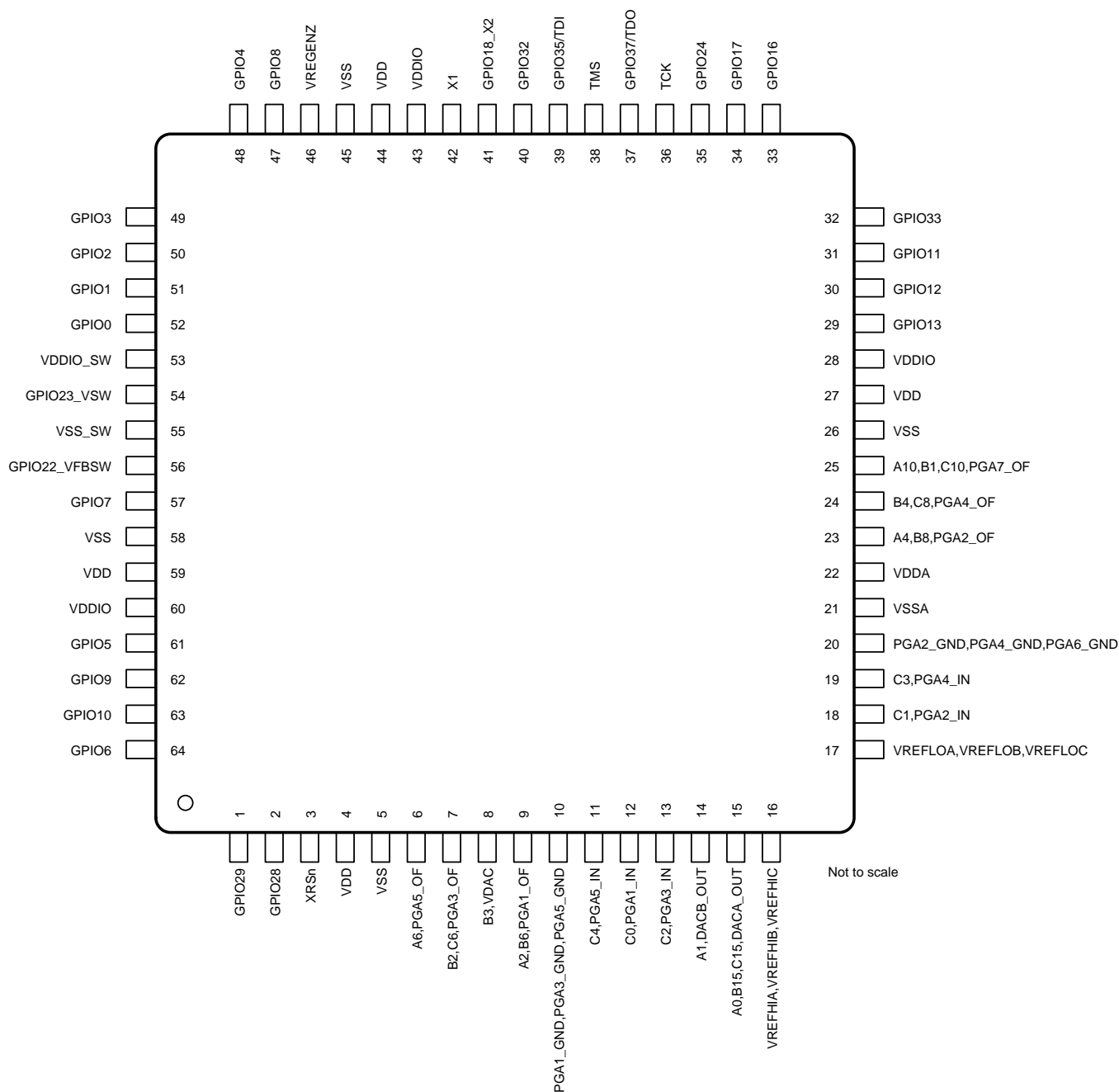
4.1 Pin Diagrams

Figure 4-1 shows the pin assignments on the 100-pin PZ Low-Profile Quad Flatpack. Figure 4-2 shows the pin assignments on the 64-Pin PM Low-Profile Quad Flatpack. Figure 4-3 shows the pin assignments on the 64-Pin PM Low-Profile Quad Flatpack for the Q-temperature device. Figure 4-4 shows the pin assignments on the 56-Pin RSH Very Thin Quad Flatpack No-Lead.



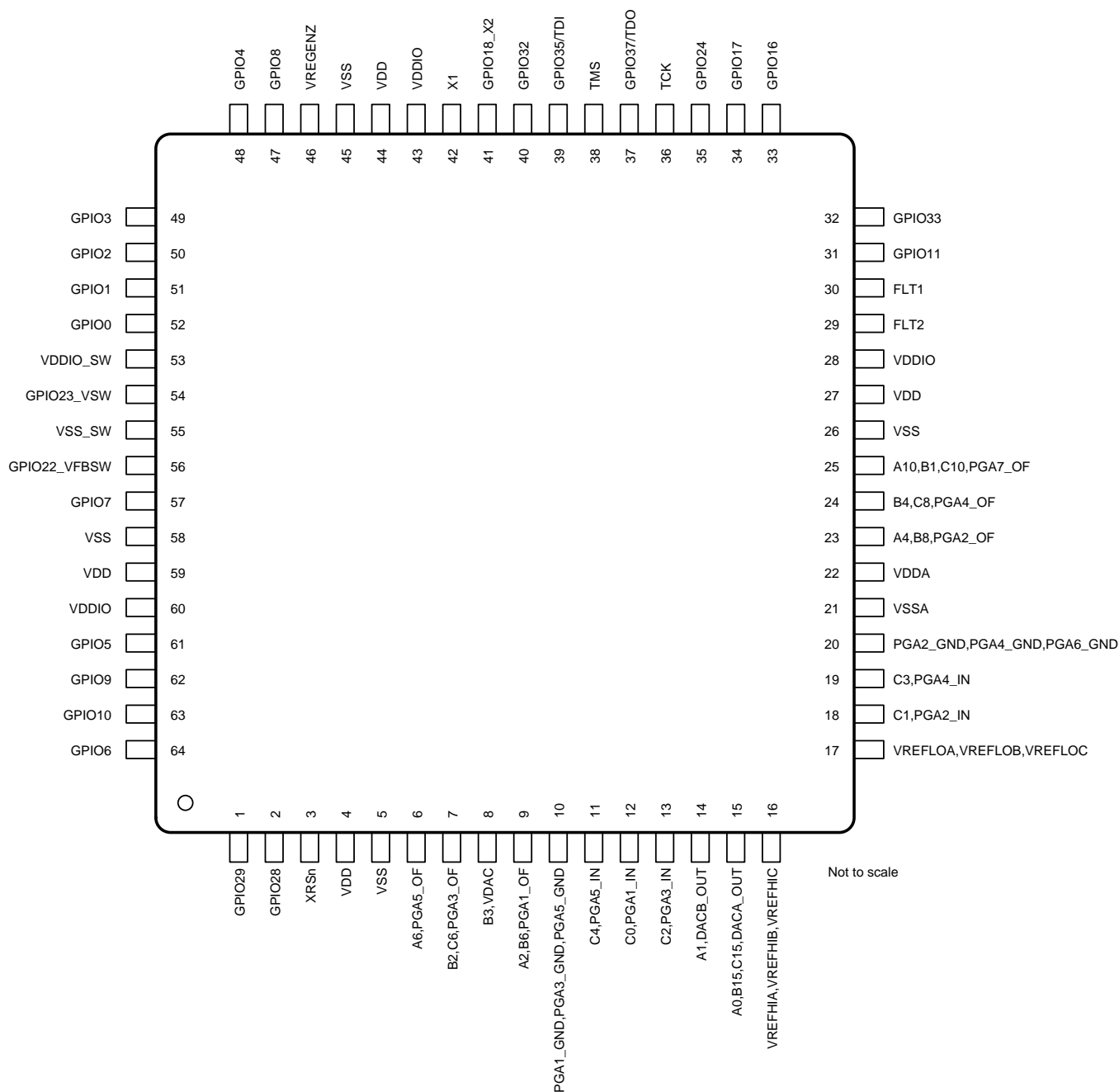
A. Only the GPIO function is shown on GPIO terminals. See Section 4.3 for the complete, muxed signal name.

Figure 4-1. 100-Pin PZ Low-Profile Quad Flatpack (Top View)



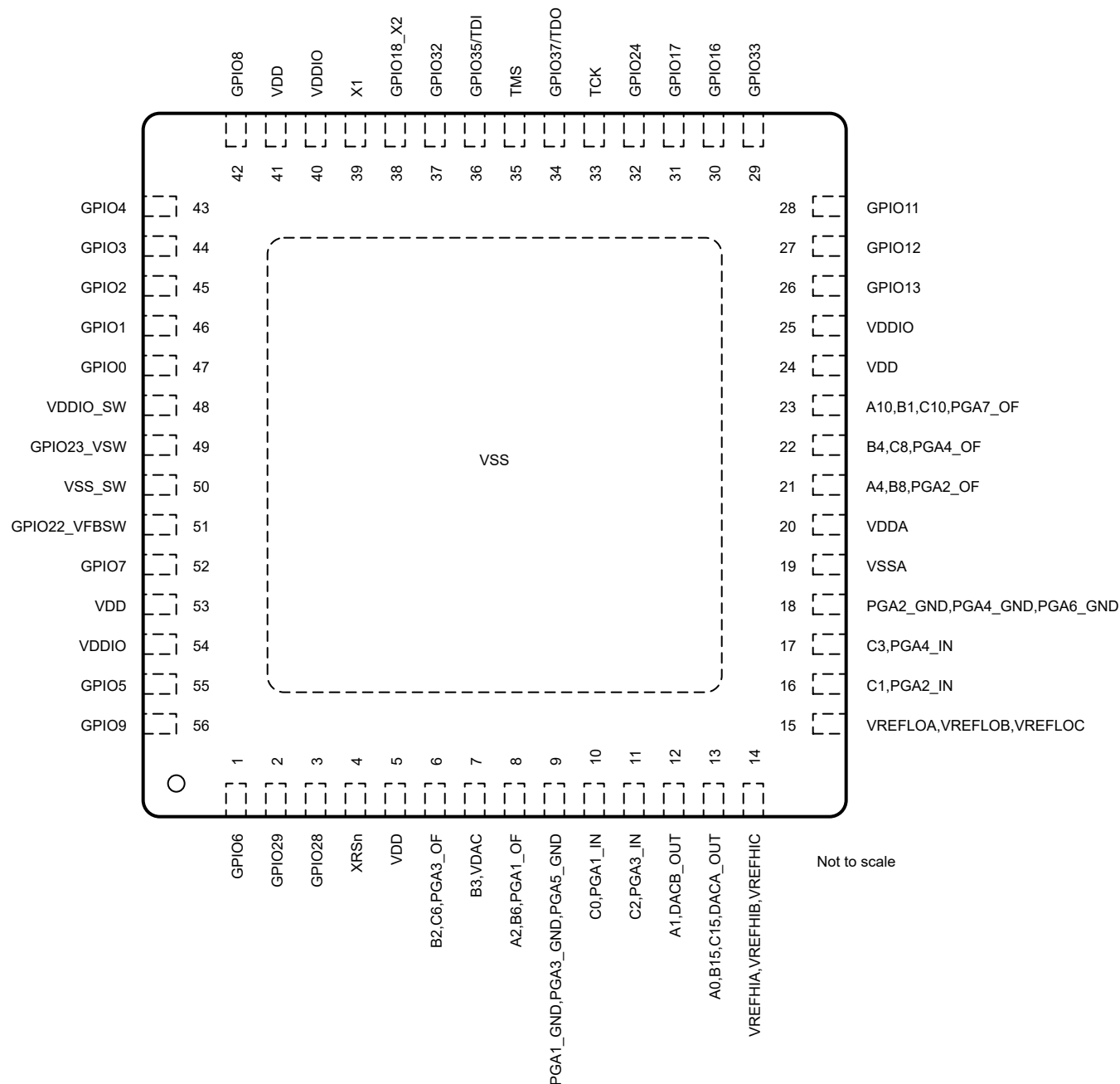
A. Only the GPIO function is shown on GPIO terminals. See [Section 4.3](#) for the complete, muxed signal name.

Figure 4-2. F280049/C/M, F280045, F280041/C 64-Pin PM Low-Profile Quad Flatpack (Top View)



A. Only the GPIO function is shown on GPIO terminals. See [Section 4.3](#) for the complete, muxed signal name.

Figure 4-3. F280048/C, F280040/C 64-Pin PM Low-Profile Quad Flatpack — Q-Temperature (Top View)



- Only the GPIO function is shown on GPIO terminals. See [Section 4.3](#) for the complete, muxed signal name.
- This figure shows the top view of the 56-pin RSH package. The terminals are actually on the bottom side of the package. See [Section 9](#) for the 56-pin RSH mechanical drawing.

Figure 4-4. 56-Pin RSH Very Thin Quad Flatpack No-Lead (Top View)