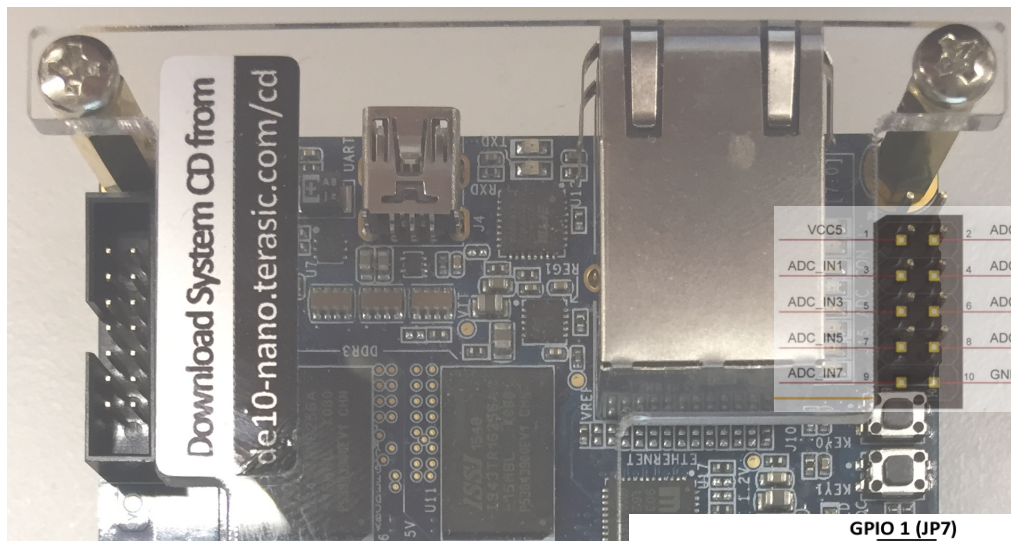


roboy de10-nano-soc PINOUT

LEGEND

SCK[0-2]	SPI Motor Control clock lines
MOSI[0-2]	SPI Motor Control Master Out Slave In lines
MISO[0-2]	SPI Motor Control Master In Slave Out
SS0[0-8]	SPI Slave Select lines of SPI bus 0 (the [] is the motor ID)
SS1[9-12]	SPI Slave Select lines of SPI bus 1 (the [] is the motor ID)
SS2[13-15]	SPI Slave Select lines of SPI bus 2 (the [] is the motor ID)
SCK[3-4]	SPI Sensor clock lines
MOSI[0-2]	SPI Sensor Master Out Slave In lines
MISO[0-2]	SPI Sensor Master In Slave Out
SS3[0-5]	SPI Slave Select lines of SPI bus 3
SS4[0-5]	SPI Slave Select lines of SPI bus 4
SCL[0-4]/SDA[0-4]	I2C clock/data line for four separate i2c cores
PWR_S	Power Sense, active low



SCL[0]	GPIO_0[35]	40	GPIO_0[34]	39	SDA[0]	GPIO_0[32]	37	MOSI[0]	GPIO_1[0]	1	GPIO_1[1]	MISO[0]	2
SCL[1]	GPIO_0[33]	38	GPIO_0[32]	37	SDA[1]	GPIO_0[30]	35	SCK[0]	GPIO_1[2]	3	GPIO_1[3]	SS0[0]	4
SCL[2]	GPIO_0[31]	36	GPIO_0[30]	35	SDA[2]	GPIO_0[28]	33	SS0[5]	GPIO_1[4]	5	GPIO_1[5]	SS0[6]	6
SCL[3]	GPIO_0[29]	34	GPIO_0[28]	33	SDA[3]	GPIO_0[26]	31	SS0[3]	GPIO_1[6]	7	GPIO_1[7]	SS0[4]	8
	GPIO_0[27]	32	GPIO_0[26]	31		GPIO_0[24]	29	SS0[1]	GPIO_1[8]	9	GPIO_1[9]	SS0[2]	10
	GND	30	3.3V	29		GPIO_0[22]	25	5V	11	12	GND		
	GPIO_0[25]	28	GPIO_0[24]	27		GPIO_0[20]	23	SS1[7]	GPIO_1[10]	13	GPIO_1[11]	SS1[8]	14
	GPIO_0[23]	26	GPIO_0[22]	25		GPIO_0[18]	21	MOSI[1]	GPIO_1[12]	15	GPIO_1[13]	MISO[1]	16
	GPIO_0[21]	24	GPIO_0[20]	23		GPIO_0[16]	19	SCK[1]	GPIO_1[14]	17	GPIO_1[15]	SS1[9]	18
	GPIO_0[19]	22	GPIO_0[18]	21		GPIO_0[14]	17	SS1[10]	GPIO_1[16]	19	GPIO_1[17]	SS1[11]	20
SS4[5]	GPIO_0[17]	20	GPIO_0[16]	19	SS4[4]	GPIO_0[12]	15	SS1[12]	GPIO_1[18]	21	GPIO_1[19]	SS1[13]	22
SS4[3]	GPIO_0[15]	18	GPIO_0[14]	17	SS4[2]	GPIO_0[10]	13	SS1[14]	GPIO_1[20]	23	GPIO_1[21]	MOSI[2]	24
SS4[1]	GPIO_0[13]	16	GPIO_0[12]	15	SS4[0]	GPIO_0[8]	9	MISO[2]	GPIO_1[22]	25	GPIO_1[23]	SCK[2]	26
SCK[4]	GPIO_0[11]	14	GPIO_0[10]	13	MISO[4]	GPIO_0[6]	7	SS2[15]	GPIO_1[24]	27	GPIO_1[25]	SS2[16]	28
	GND	12	AS	11		GPIO_0[4]	5	3.3V	29	30	GND		
MOSI[4]	GPIO_0[9]	10	GPIO_0[8]	9	SS3[5]	GPIO_0[2]	3	SS2[17]	GPIO_1[26]	31	GPIO_1[27]	SS2[18]	32
SS3[4]	GPIO_0[7]	8	GPIO_0[6]	7	SS3[3]	GPIO_0[0]	1	SS2[19]	GPIO_1[28]	33	GPIO_1[29]	SS2[20]	34
SS3[2]	GPIO_0[5]	6	GPIO_0[4]	5	SS3[1]			GPIO_1[30]	35	36	GPIO_1[31]		
SS3[0]	GPIO_0[3]	4	GPIO_0[2]	3	SCK[3]			GPIO_1[32]	37	38	GPIO_1[33]		
MISO[3]	GPIO_0[1]	2	MOSI[3]	GPIO_0[0]				GPIO_1[34]	39	40	GPIO_1[35]	PWR_S	

