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| **Relevant Skills** | |
| Assembly Language  Hardware Design Language (Verilog)  YAML  Embedded Systems  Solid Edge  Team Foundation Server  Visual Studio  C/C++/C#  WPF  Raspberry Pi  MATLab  Z-Wave Protocol | |
| **education** | |
| **Bachelor of Science in Computer Engineering - Utah Valley University** **May 2018**  **3.2 GPA on 4.0 scale**  Involved with IEEE UVU Chapter as Vice President of Publications.  NSF LEAP – Full Scholarship  Advanced High Performance Computer Architecture  Digital Signal Processing  VLSI Design  Software Engineering  Wireless and Mobile Communications  Senior Design Project  **Relevant Coursework Completed:**  Embedded Systems 1 & 2  Digital Design 1 & 2  Object Oriented Programming  Algorithms Data Structures | |
| **Work experience** | |
| **Software Engineer May 2018 – Present**  ***BAE Systems, Inc, Hill AFB, UT***   * Writing Developers Guide for ICBM Simulation software developed with MATLAB scripts   **Computer Science Technical Intern Dec 2016 – April 2018**  **BAE Systems, Inc, Hill AFB, UT**   * Developed, as a team, the Process Model Toolkit (PMT) and programmed in C# and XAML. * Communicate directly with government customer over New START Treaty to make improvements to PMT and present to customer and internal employees. * Attained clearance of SECRET and is current to attend meetings in Classified Environment | |
| **favorite projects** | |
|  | * Traffic Light   + Used FPGA to emulate a traffic light with code written in Verilog * Motorola Microcontroller   + Wrote Assembly Language commands to control PWM, Fuzzy Logic, etc. * Virtual Machine   + - Inputs Assembly Language as bit data into memory and executes multi-threaded functions * Z-Wave Home Assistant   + Implements Home Automation and Security on Raspberry Pi using Z-Wave Protocol |