Daniel Candeias-Jolin

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EDUCATION:

University of North Carolina at Charlotte, BS CpENG **Spring 2025**
University of North Carolina at Charlotte, MS CpENG **Spring 2026**

TECHNICAL SKILLS:

- **Languages:**
  English, Portuguese, Spanish
- **Programming Languages:**
  

- **Proficiency:**
  Git (version control), CLI (POSIX), Hardware/Software Co-Design with System-on-Chip solutions.

WORK EXPERIENCE:

UNC Charlotte Jun 2023- Present
Undergraduate Research Assistant. Charlotte, NC

- Collaborated with a mentor to build shared memory models and implement embedded operating systems.
- Conducted theoretical analysis of the hardware/software characteristics of 3D vertically stacked DRAM, paving the way for advanced memory solutions
- Quickly adapted to the research environment, learning, and employing advanced research methodologies to contribute to the project's objectives.
- Explored methodology of enhancing Python for high performance reconfigurable computing

ICCD, International Center for Community Development June 2022 – August 2022
Intern. Concord, NC

- Taught Mathematics and other STEM related topics
- worked closely with the DoD Star Base youth outreach team to provide fun, academic learning activities for the students, including but limited to model rockets, circuit boards, and chemical experiments.

PROJECTS:

- **Spread the World** is my initiative to make the internet accessible to disadvantaged children in impoverish countries. I've traveled to rural Michoacán to establish relations with a community of native Meso-American's (P'urhépecha) and acquired land to build an internet-cafe. My goal is to improve quality of living by increasing computer literacy.
- **Computers** are my passion, from programming low level computational devices to building high performance personal computers.

Research:

Office of Undergraduate Research.

- Worked on a project titled "Enhancing Python for Parallel Computing", where I presented at the OUR Summer Symposium
- Currently expanding and implementing my knowledge of software languages, hardware languages and FPGAs in tandem.

References upon request