Universidad Del Valle
Electrónica Digital
Dma Rodriguez

Seccion 12
14566

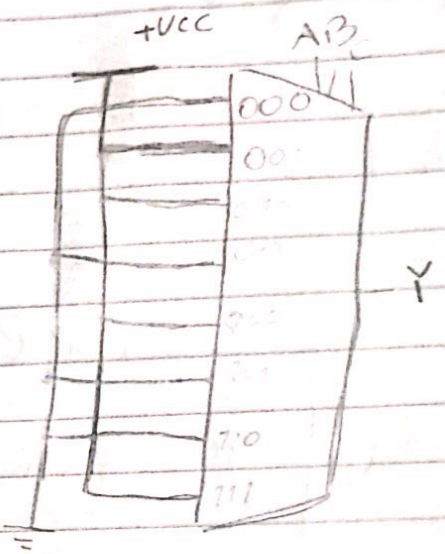
Laboratorio #5

Ejercicio 01

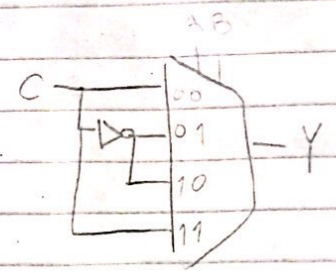
Tabla 01

A	B	C	Y
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

MUX 8:1



MUX 4:1



A
B
C

MUX 2:1

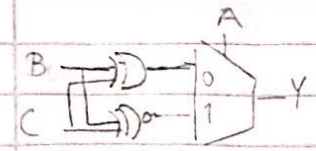
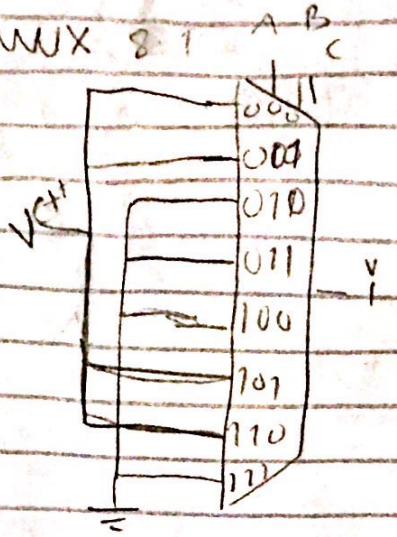


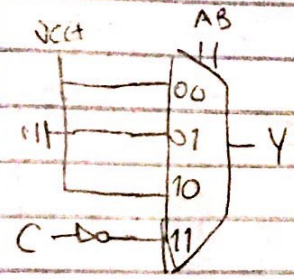
Tabla 02

A	B	C	Y
0	0	0	1
0	0	1	X (+)
0	1	0	0
0	1	1	0
1	0	0	X (-)
1	0	1	1
1	1	0	1
1	1	1	0

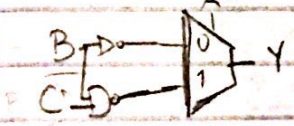
MUX 8:1



MUX 4:1



MUX 2:1



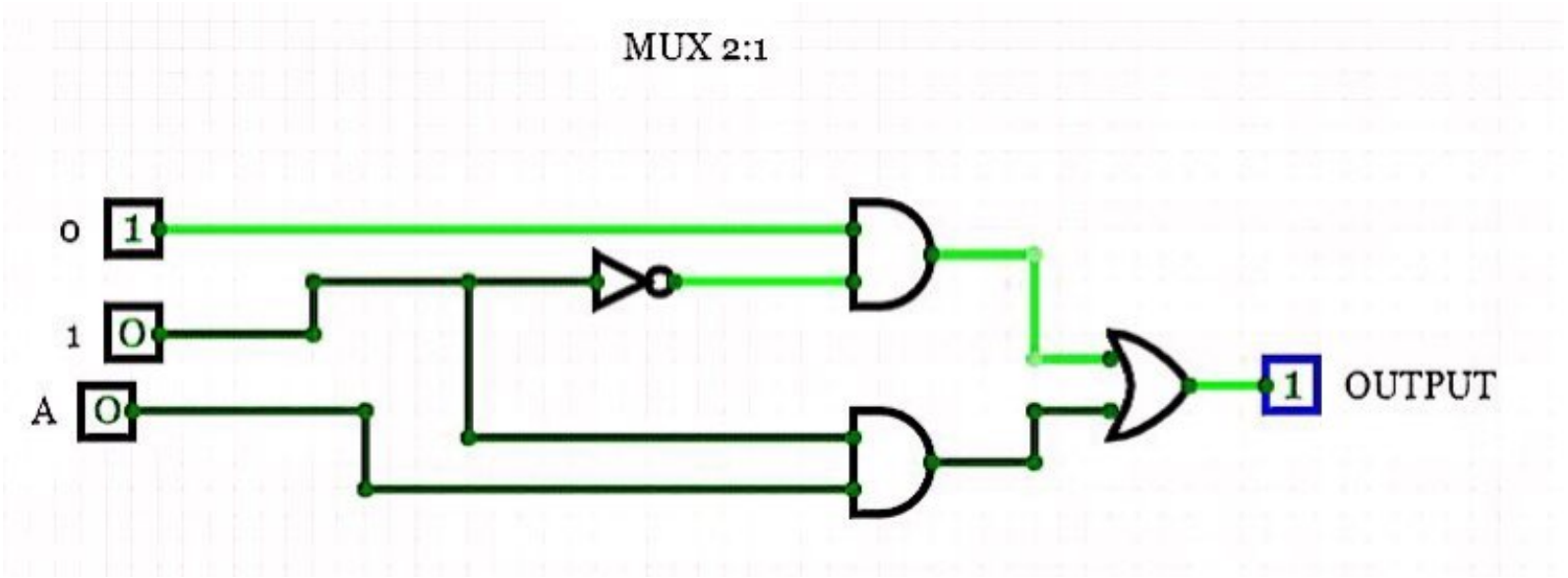
Ejercicio 05

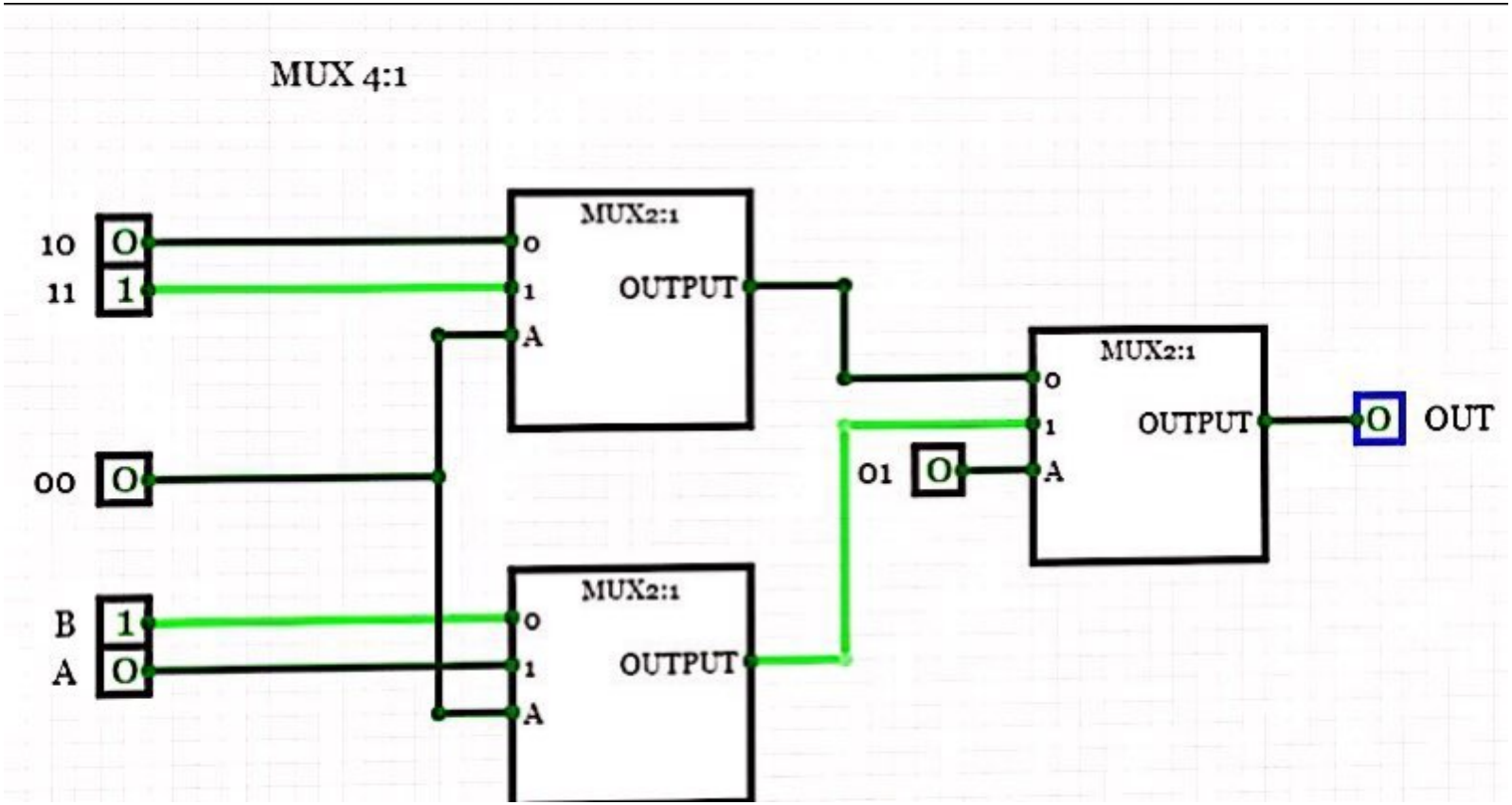
Propagation Delay
Es el máx delay que ocurre tras un cambio en su entrada

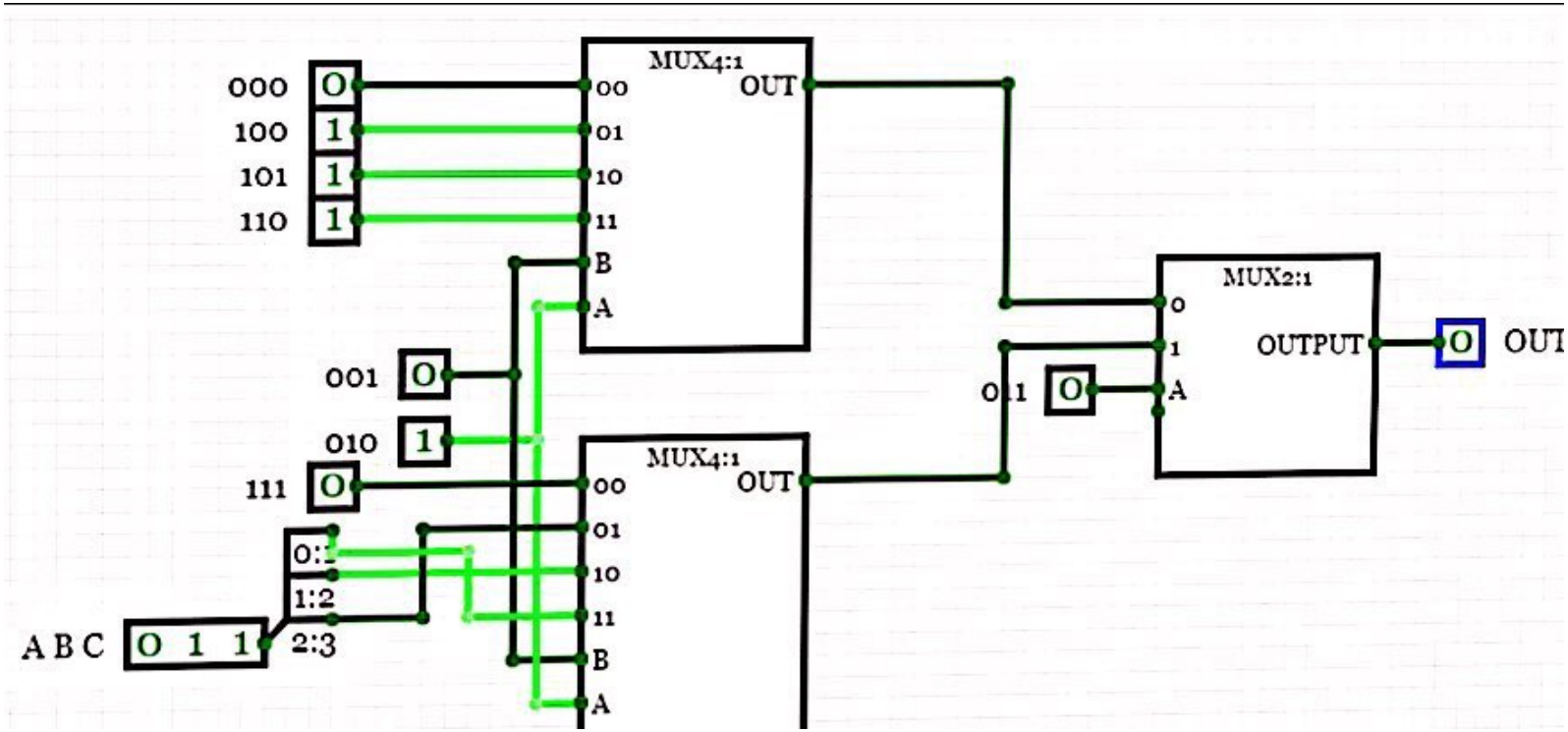
Contamination Delay
Es el delay mín causado por suciedad o imperfecciones en un circuito mostrado en su entrada.

Ruta Crítica La suma tpd de la ruta más alta.

Ruta Corta La suma tcd de la ruta es la mas baja.

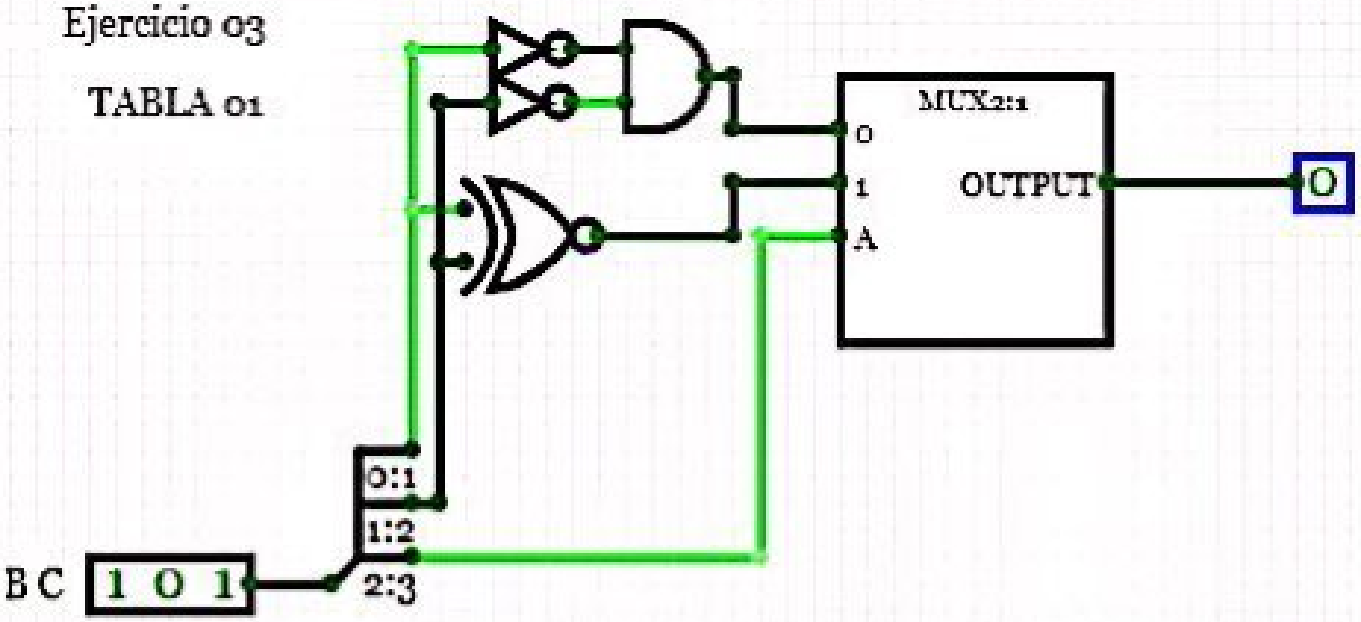


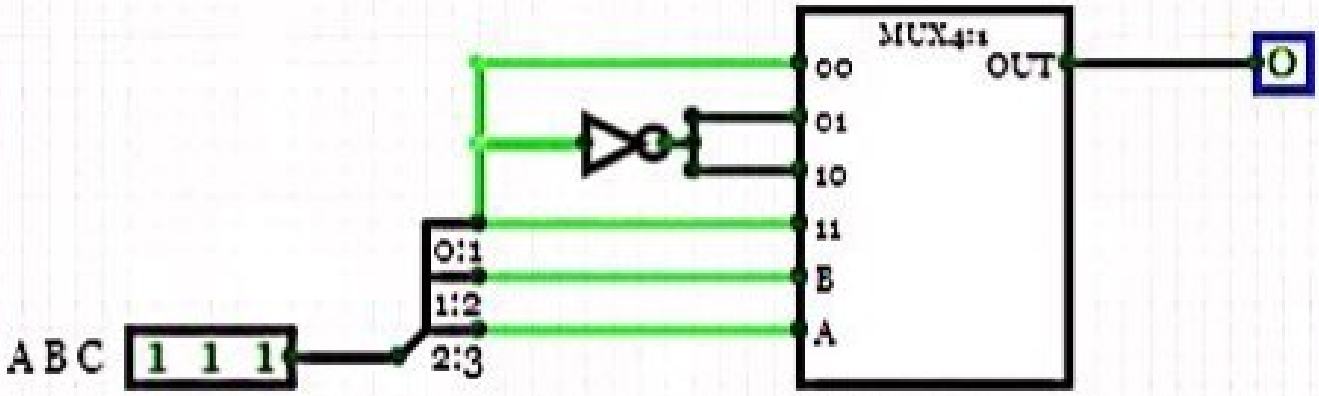




Ejercicio 03

TABLA 01





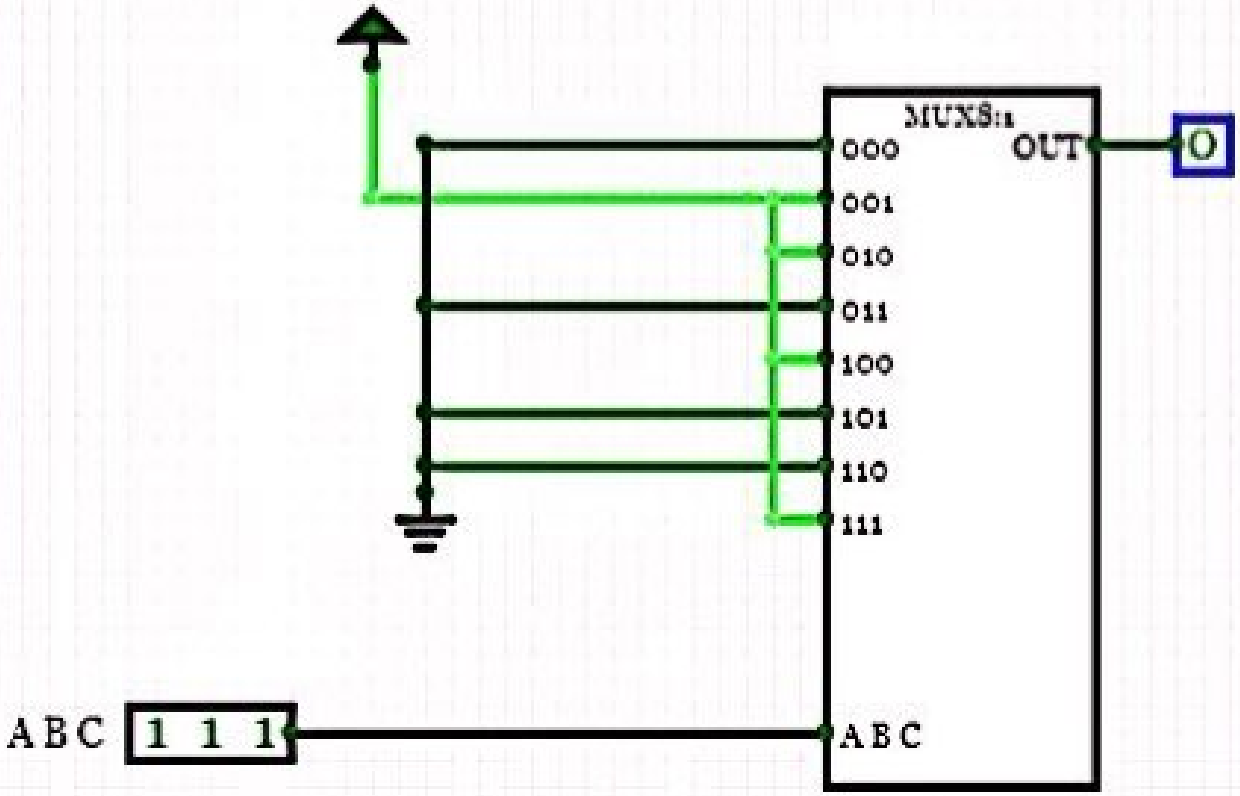
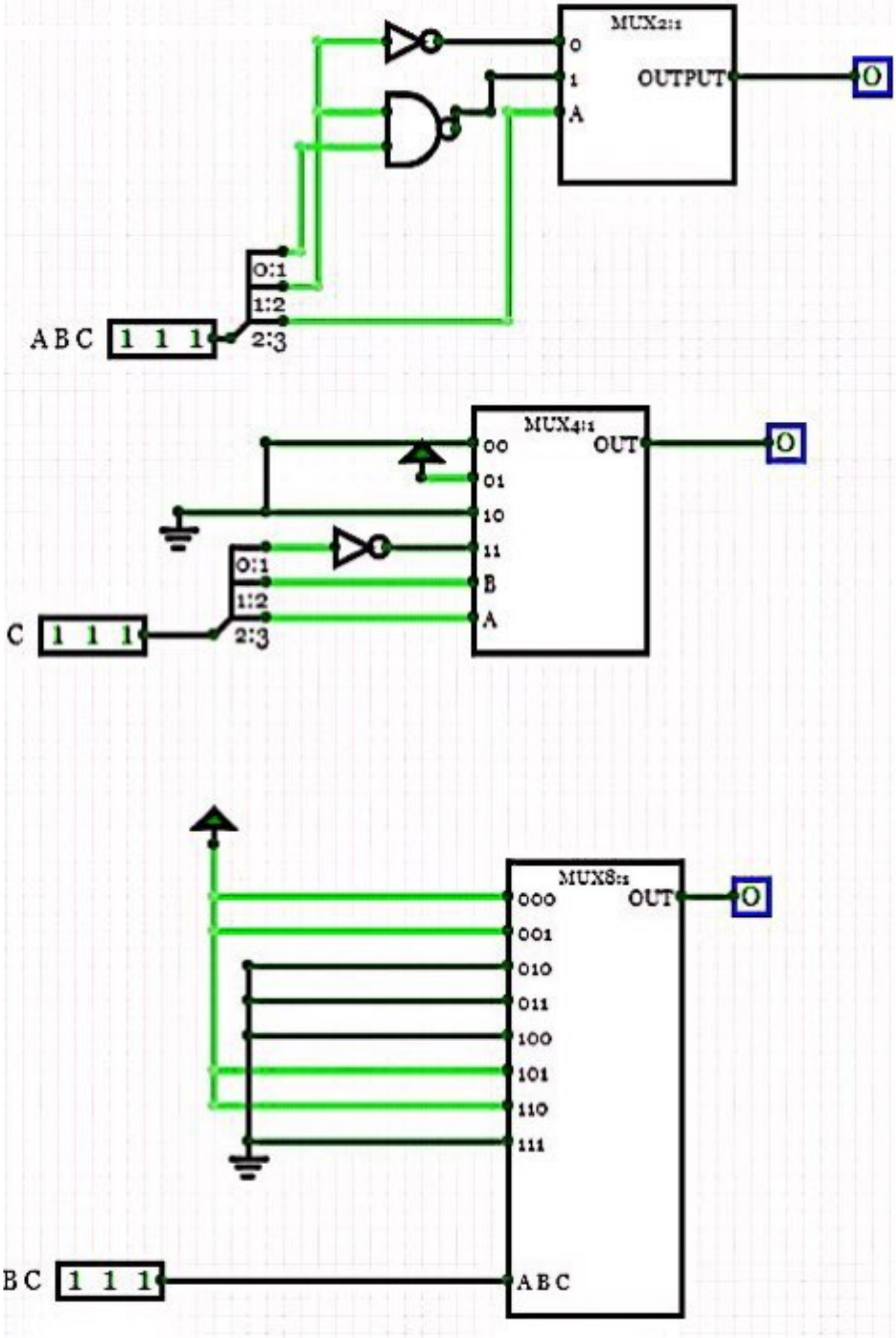


TABLA 02



Ejercicio 03

Tabla 01

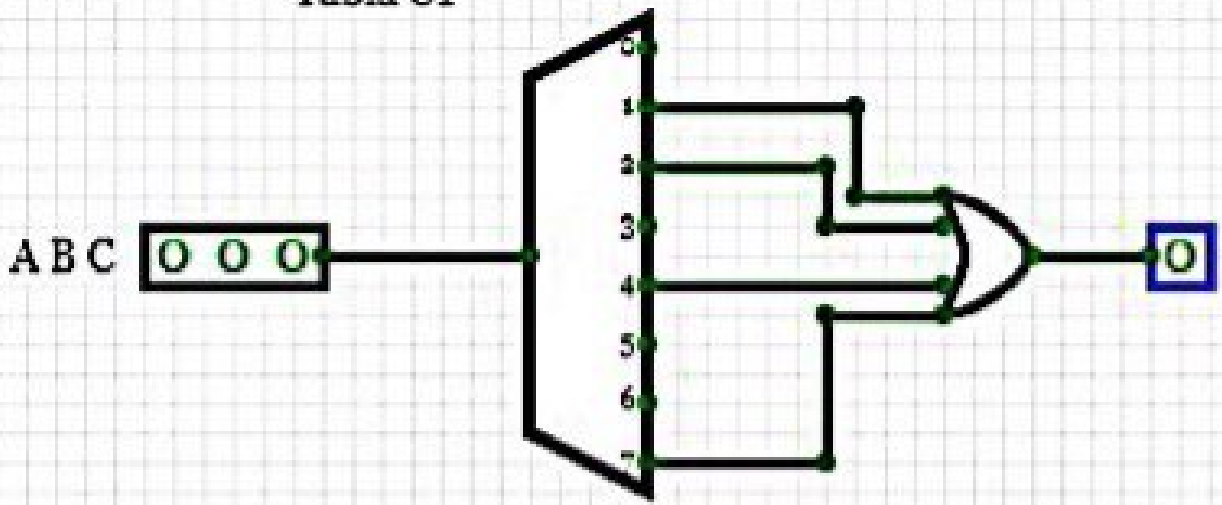
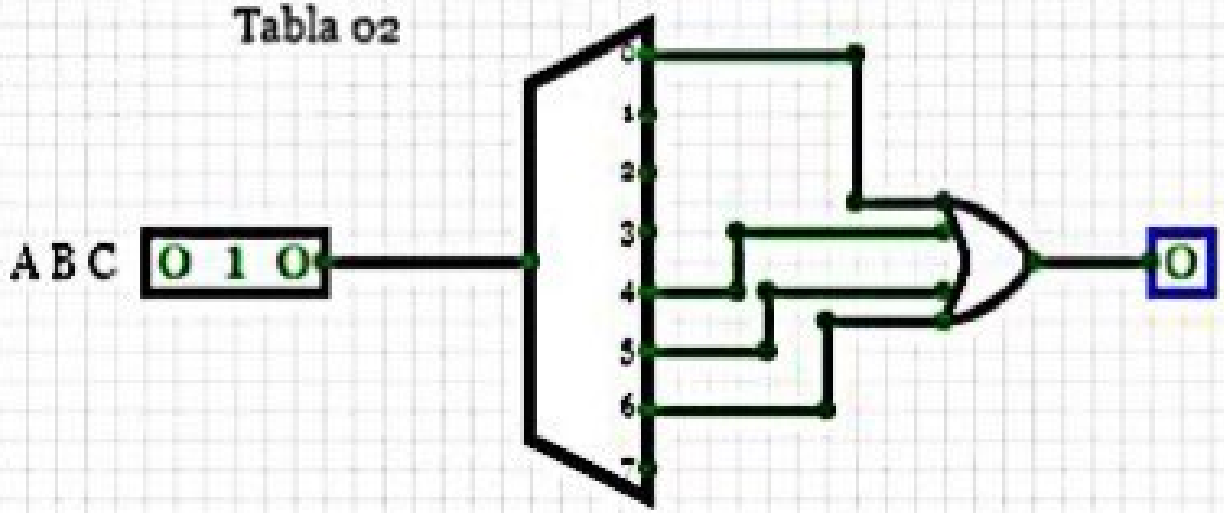
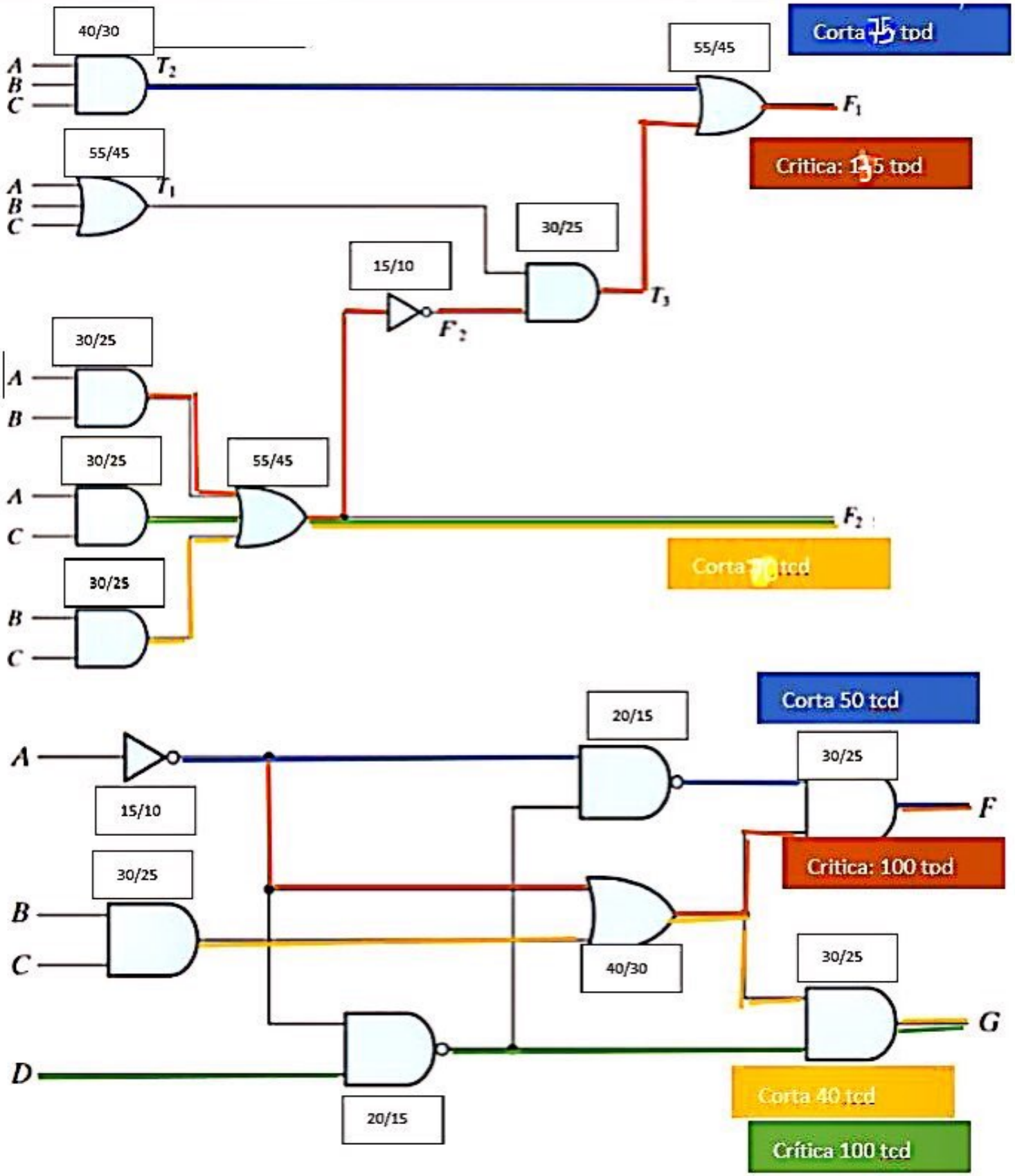


Tabla 02





Scanned with CamScanner