### Notes: Unless Otherwise Stated

### Scheme Spec:

FLASH: MLC, 3.3V

DRAM: DDR2/3 1.8V /1.5V

Key: NEXT, PREV, Vol+, Vol-, UP, DOWN, ENTER, UBOOT

Power: DCIN, 5V, 2A; BAT, 3.7V, 3600mAH

USBO: OTG USB1: HOST USB2: HOST WIFI: SDIO WIFI Card: TFcard\*2

Other: GPS, FM, Headphone, MIC, G-Sensor, camera

### Power Supply:

电源名称	输出电压	最大供电能力	预计谁在用
AXP209 DCDC2	1.25V	1600mA	CPU
AXP209 DCDC3	1.2V	1200mA	CORE
AXP209 LDO1	1.3V	30mA	RTC
AXP209 LDO2	3V	200mA	AVCC
AXP209 LDO3	2.8V	400mA	CSI0-IO
AXP209 LDO4	3.3V	200mA	CSI1-IO
	1.8V	1000mA	CSI-CORE
	1.5V/1.8V	1000mA	DRAM
	3.3V	1000mA	VCC/LCD/NAND//WIFI
	5V	2000mA	HDMI/USB
	2.8V	300mA	CSI0-AF-VCC
	1.2V	300mA	WIFI
RT9193-33PB	3.3V	300mA	GPS

#### Schematics Index:

P01: COVER P02: BLOCK

P03: PIO ASSIGNMENT

P04: POWER TREE

P05: CPU1 P06: CPU2 P07: POWER1 P08: POWER2 P09: BESIDE CPU

P10: HDMI-CSI P11: HP-FM-KEY-MIC-IR-TVOUT

P12: USB-CARD

P13: LCD P14: DRAM3 P15: NAND

P16: WIFI-GSENSOR

P17: GPS

HOENIXA20

Rev	Description	Date	Drawn	Checked	Approved
PAD_MAINCHIP_STD_V1.13	没有ACIN的时候与USBVBUS短接	2011-06-30	Leo		
PAD_MAINCHIP_STD_V1.14	增加LVDS和CTP的连接方式	2011-07-06	Leo		
PAD_MAINCHIP_STD_V1.15	更改了UBOOT按键电路,WIFI电源电路	2011-07-07	Leo		
PAD_MAINCHIP_STD_V1.15	更改了USB限流电路	2011-07-08	Leo		
PAD_MAINCHIP_STD_V1.15	更改了电源电路	2011-07-12	Leo		

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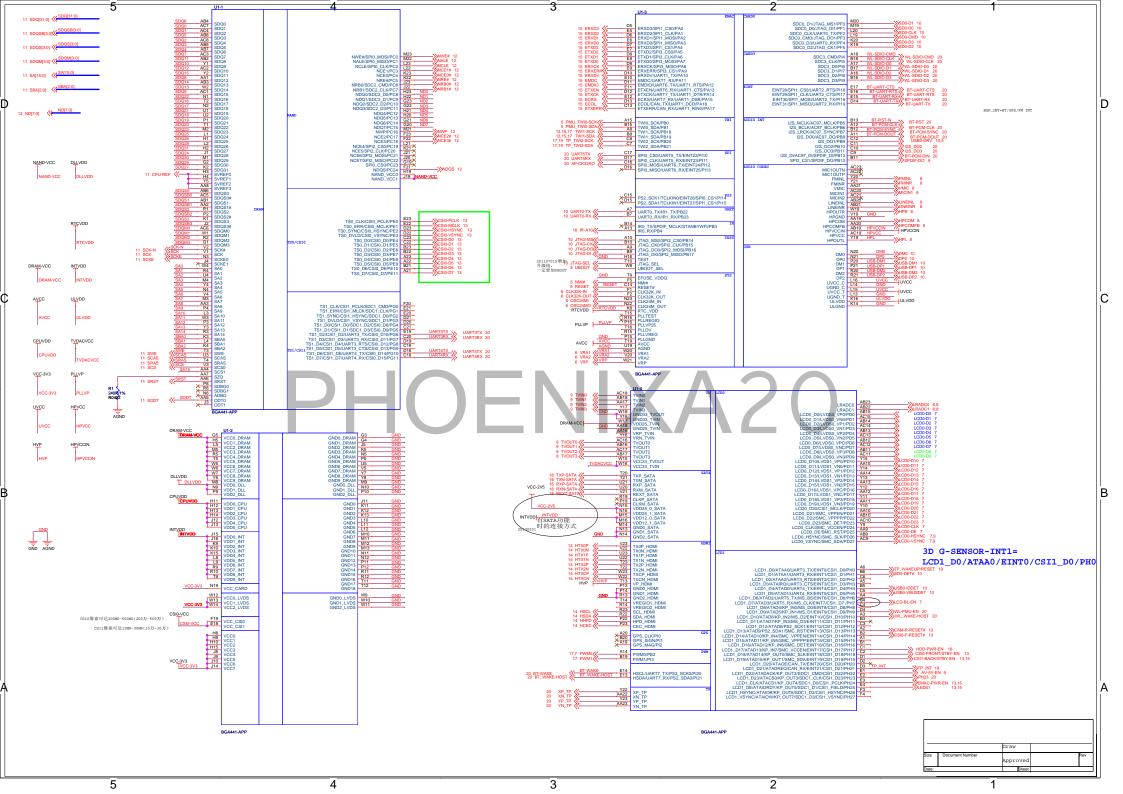
**BLOCK** PHOENIXA20 Approved PIO ASSIGNMENT

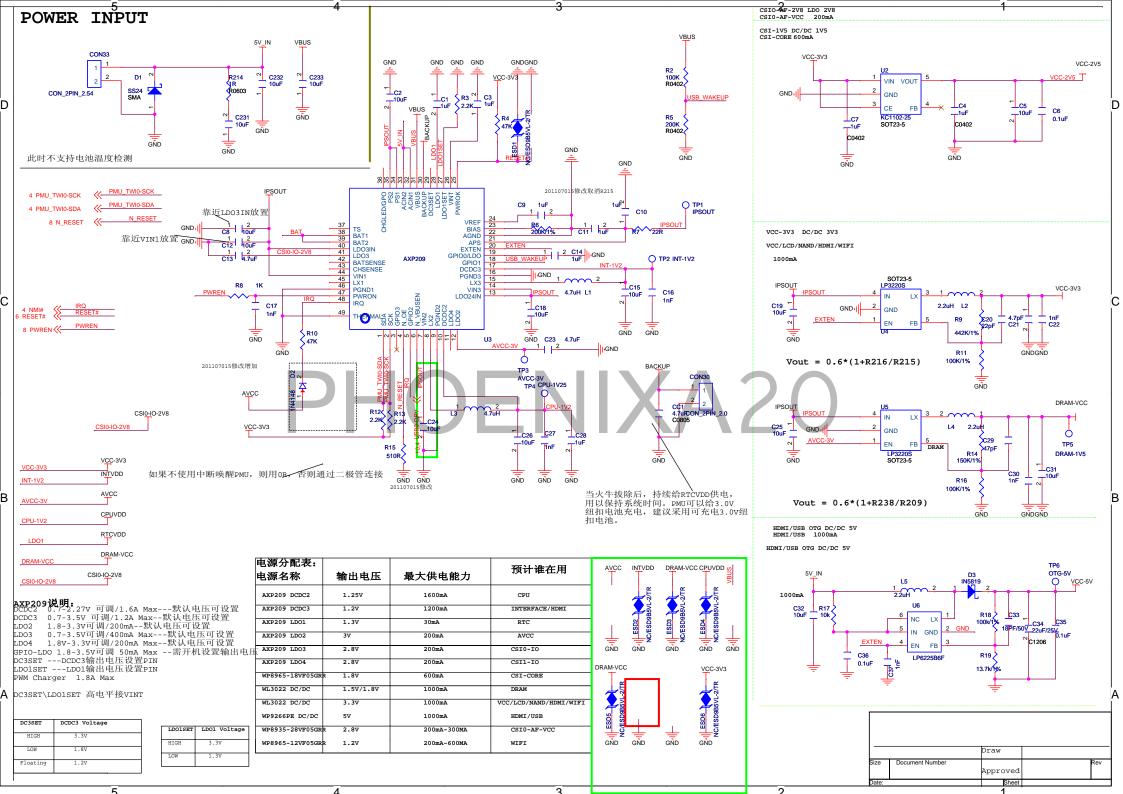
Pin Group	Pin Name	Define	Function	Î	Pin Group	Pin Name	Define	Function		Pin Group	Pin Name	Define	Function		Pin Group	Pin Name	Define	Function		Pin Group	Pin Name	Define	Function
	PA0	ERXD3				PC0	NWE#				PD18	LCD0_D18				PH0	EINT0	GS-INT1	1		PI15	GPIO_OUT	GPS-RX-EN
	PA1	ERXD2				PC1	NALE	1			PD19	LCD0_D19	İ		1	PH1	GPIO_IN	SD0-DET#			PI16	UART2_RTS	BT-UART-RTS
	PA2	ERXD1			İ	PC2	NCLE	1			PD20	LCD0_D20	İ		1	PH2	GPIO_IN	SD1-DET#			PI17	UART2_CTS	BT-UART-CTS
	PA3	ERXD0				PC3	NCE1	1			PD21	LCD0_D21	1		1	PH3	GPIO_OUT	USB2-DRV		PI(22)	PI18	UART2_TX	BT-UART-TX
	PA4	ETXD3			ĺ	PC4	NCE0			PD(28)	PD22	LCD0_D22	LCD		1	PH4	GPIO_IN	USB0-IDDET			PI19	UART2_RX	BT-UART-RX
	PA5	ETXD2				PC5	NRE#				PD23	LCD0_D23			1	PH5	GPIO_IN	USB0-VBUSDET			PI20	GPIO_OUT	BT-GPIO0
	PA6	ETXD1				PC6	NRB0				PD24	LCD0_CLK			1	PH6	GPIO_OUT	USB1-DRV			PI21	GPIO_OUT	BT-GPIO1
PA(18)	PA7	ETXD0	EMAC			PC7	NRB1				PD25	LCD0_DE			1	PH7	GPIO_OUT	LCD-BL-EN					
	PA8	ERXCK				PC8	NDQ0				PD26	LCD0_HSYN			1	PH8	GPIO_OUT	LCD-PWR					
	PA9	ERXERR				PC9	NDQ1				PD27	LCD0_VSYNO			1	PH9	GPIO_OUT	WIFI-SHDN#					
	PA10	ERXDV				PC10	NDQ2				PE0	CSIO_PCLK			1	PH10	GPIO_OUT	WIFI-HOST WAKEUP					
	PA11	EMDC				PC11	NDQ3				PE1	CSIO_MCLK			1	PH11	GPIO_OUT	WIFI-VDD-EN					
	PA12	EMDIO			PC(25)	PC12	NDQ4				PE2	CSIO_HSYNO			Day ( 0 (	PH12	GPIO_OUT	WIFI-VCC-EN					
	PA13	ETXEN			FC(23)	PC13	NDQ5	NAND			PE3	CSIO_VSYNO			PH(28	PH13	GPIO_OUT	CSIO-RESET#					
	PA14	ETXCK				PC14	NDQ6			PE(12)	PE4	CSIO_DO	CSI0		1	PH14	GPIO_OUT	CSI1-RESET#					
	PA15	ECRS				PC15	NDQ7			PE(12)	PE5	CSIO_D1	CSIU		1	PH15	GPIO_OUT	PA-SHDN#					
	PA16	ECOL				PC16	NWP				PE6	CSIO_D2			1	PH16	GPIO_OUT	CSI0-1V8-EN					
_	PA17	GPIO_OUT	E-RST			PC17	NCE2				PE7	CSIO_D3			1	PH17	GPIO_OUT	CSI1-1V8-EN					
	PB0	TWIO_SCK	PMU			PC18	NCE3	GPS-			PE8	CSIO_D4			1	PH18	EINT18	CSIO-STBY-E					
	PB1	TWI0_SDA				PC19	SPI2_CS	SCS GPS-			PE9	CSIO_D5			1	PH19	EINT19	CSI1-STBY-E					
	PB2	PWM0	PWM CP-RST			PC20	SPI2_SCLK	SCLK GPS-			PE10	CSIO_D6			1	PH20	EINT20	LS-INT					
	PB3 PB4	GPIO_OUT				PC21	SPI2_MOSI	MOSI GPS-			PE11				1	PH21	EINT21	TP-INT					
		IRO_RX GPIO_OUT	IR BT-RST			PC22 PC23	GPIO_OUT	VCC-EN			PF0	SDC0_D1		l		PH22	SDC1_CMD						
	PB5 PB6	I2S_BCLK	BT-PCM				NC NC				PF1	SDC0_D0			<b>V</b>	PH23 PH24	SDC1_CER						
		I2S_LRCK	-CLK BT-PCM			PC24 PD0	NDQS		)	PF(6)	PF2 PF3	SDC0_CLK	SDC0				SDC1_D1	SDC1					
	PB7 PB8	12S_ERCK 12S_D00	-SYNC BT-PCM			PD1	LCD0_D0		<b>.</b>		PF4	SDC0_CMI			$\mathcal{L}$	PH25	SDC1_D1						
	PB9	GPIO_OUT	-OUT USB0-DRV			_	LCD0_D1 LCD0_D2					SDC0_D3					SDC1_D3						
	PB10	GPIO_OUT	LCD0-SCK		Γ <b>Ι</b>	PD2 PD3	LCD0_D3	1		_	PF5 PG0	SDC0_D2 CSI1_PCLK			_	PH27 PI0	GPS_CLK						
	PB11	GPIO_OUT	LCD0-SDA			PD3	LCD0_D4	1			PG1	CSI1_MLCK			1	PII	GPS_SIGN	-					
PB(24)	PB12	I2S_DI	BT-PCM-I	N		PD5	LCD0_D5				PG2	CSI1_HSYN			1	PI2	GPS_MAG	GPS					
1	PB13	GPIO_OUT	TP-WAKEU	₽		PD5 PD6	LCD0_D3	1			PG2 PG3	CSI1_VSYNO				PI2	PWM1						
1	PB14	JTAG_MS0				PD7	LCD0_D7	1			PG4	CSI1_D0				PI4	SDC3_CMD						
	PB15	JTAG_CK0	1		PD(28)	PD8	LCD0 D8	LCD			PG5	CSI1_D0			1	PI5	SDC3_CLK	1					
	PB16	JTAG_DO0	JTAG			PD9	LCD0 D9	1		PG(12)	PG6	CSI1_D2	CSI1		PI(2		SDC3_D0	-					
1	PB17	JTAG_DI0	1			PD10	LCD0_D10	1				1 2211_02				PI7	SDC3_D1	WIFI					
	PB18	TWI1_SCK				PD11	LCD0_D11				PG7	CSI1_D3			1	PI8	SDC3_D2						
	PB19	TWI1_SDA	TWI1			PD12	LCD0_D12	1			PG8	CSI1_D4			1	PI9	SDC3_D3	1					
	PB20	TWI2_SCK				PD13	LCD0_D13	1			rgo	C311_D4			1	PI10	SPIO_CSO	GS-INT2					
	PB21	TWI2_SDA	TWI2			PD14	LCD0_D14	1			PG9	CSI1_D5			1	PI11	SPIO_CLK	CSIO-AF-EN					
	PB22	UARTO_TX				PD15	LCD0_D15	1			PG10	CSI1_D6	1			PI12	SPI0_MOSI	TV-EN					
		_	UART (DBUG)			PD16	LCD0_D16	1				5511_50			1	PI13	SPIO_MISO	CP-INT					
	PB23	UARTO_RX				PD17	LCD0_D17				PG11	CSI1_D7				PI14	GPIO_OUT	GPS-OSC-EN	1				

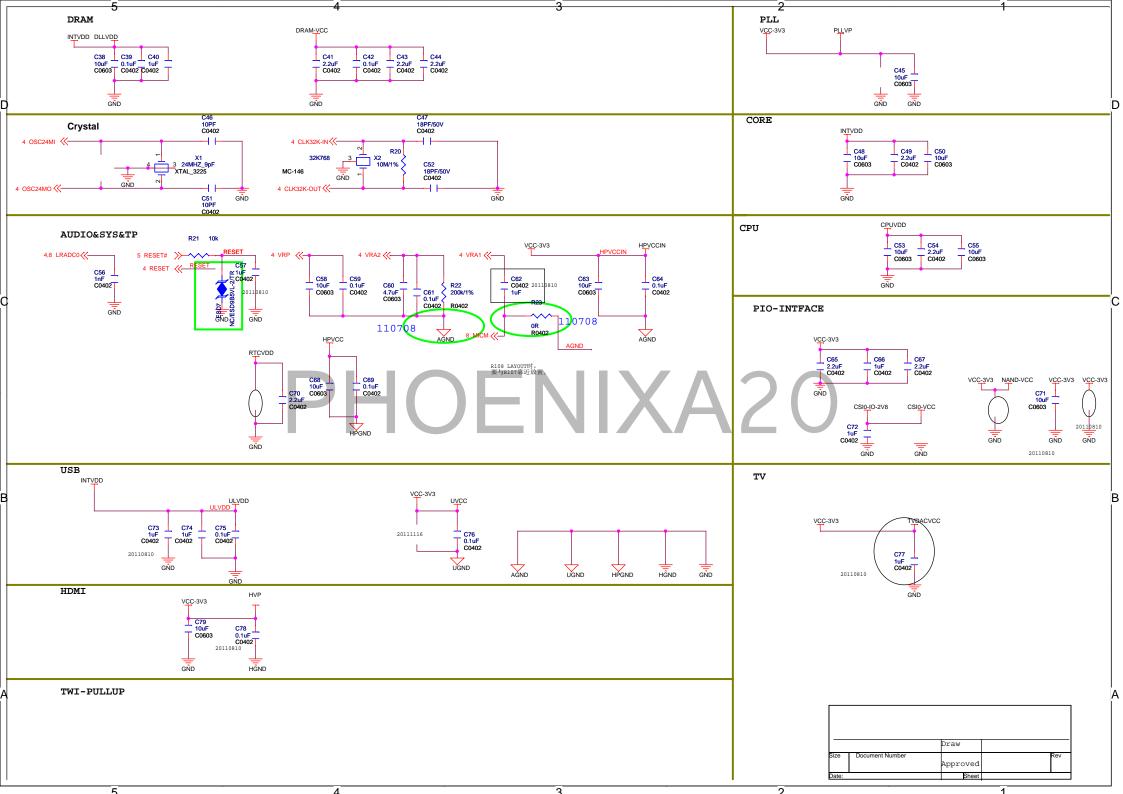
Draw Approved

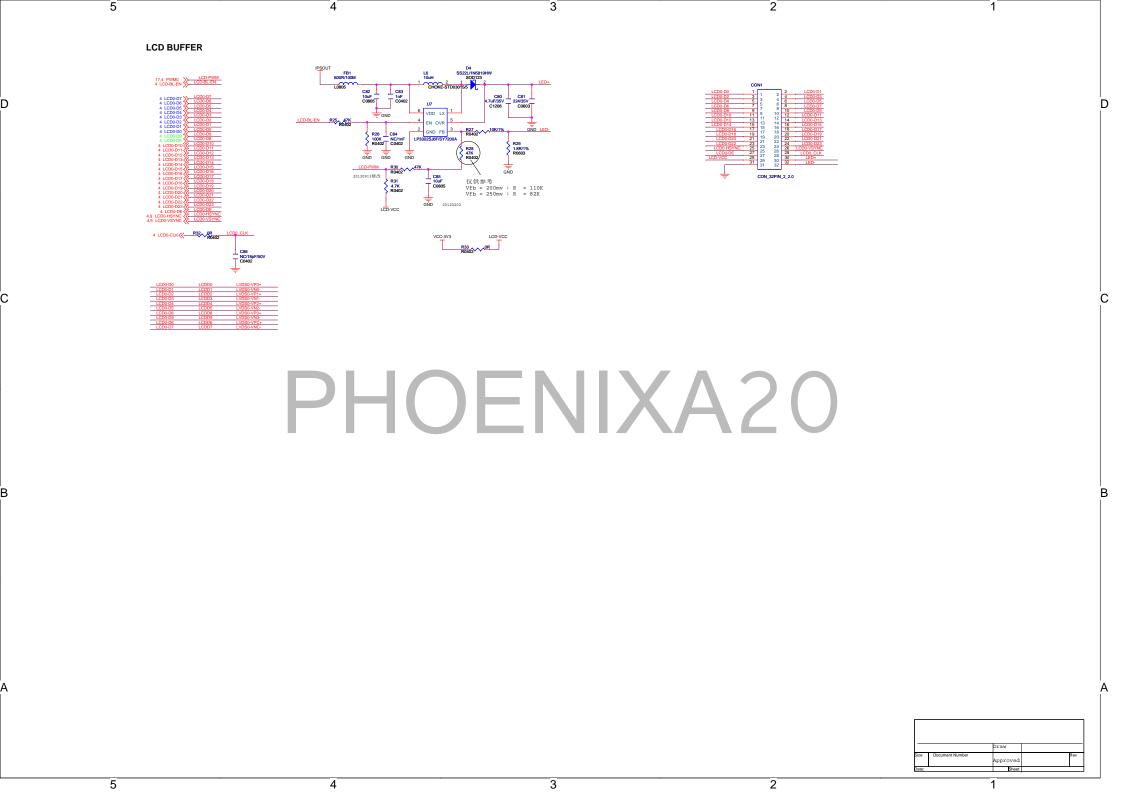
2

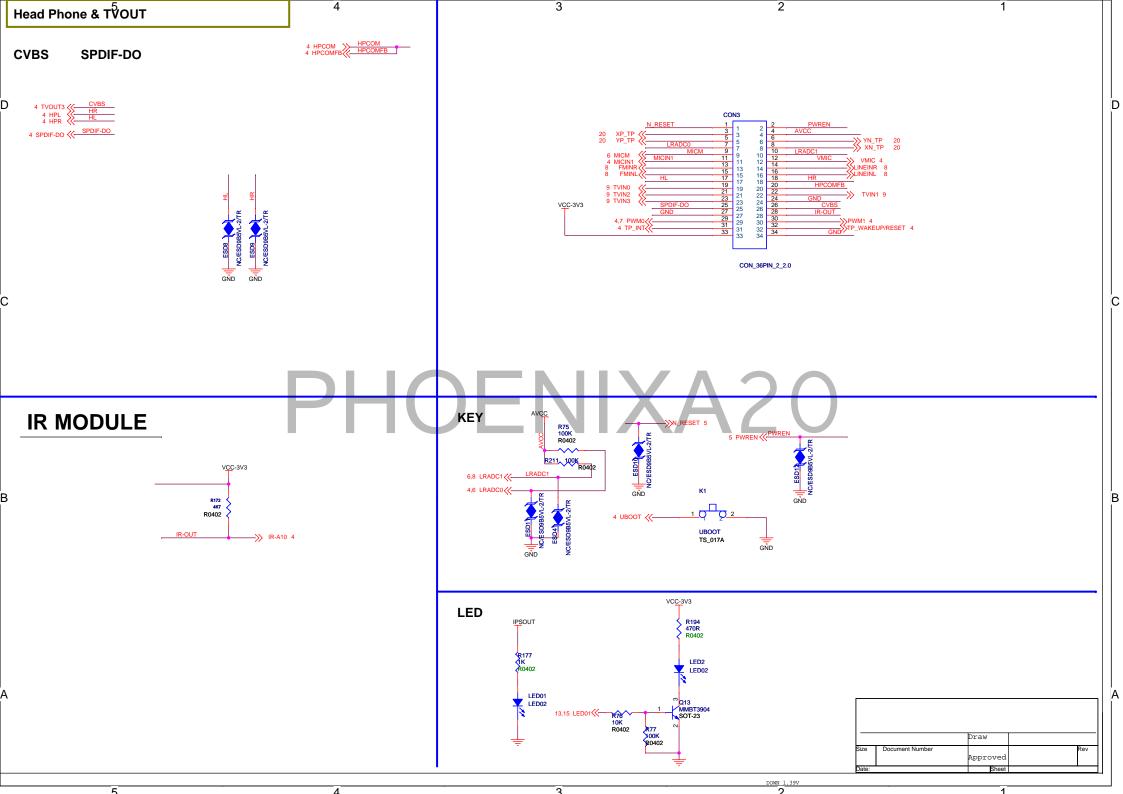
5

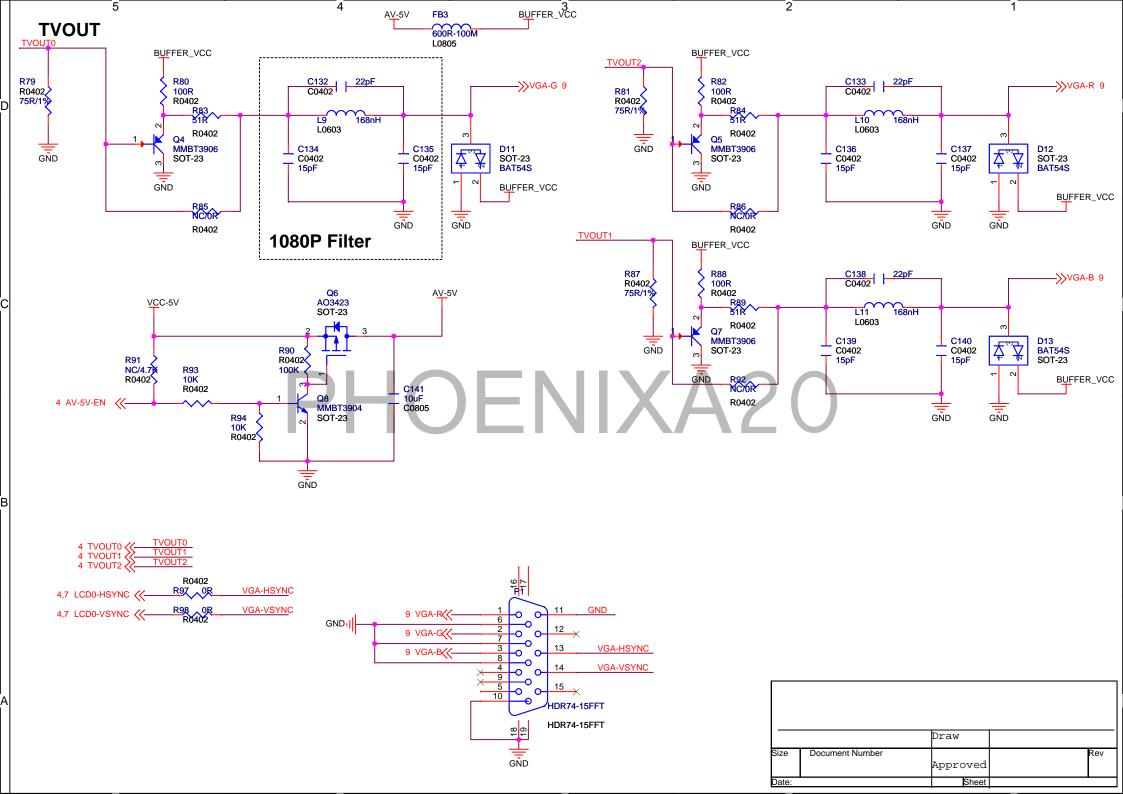


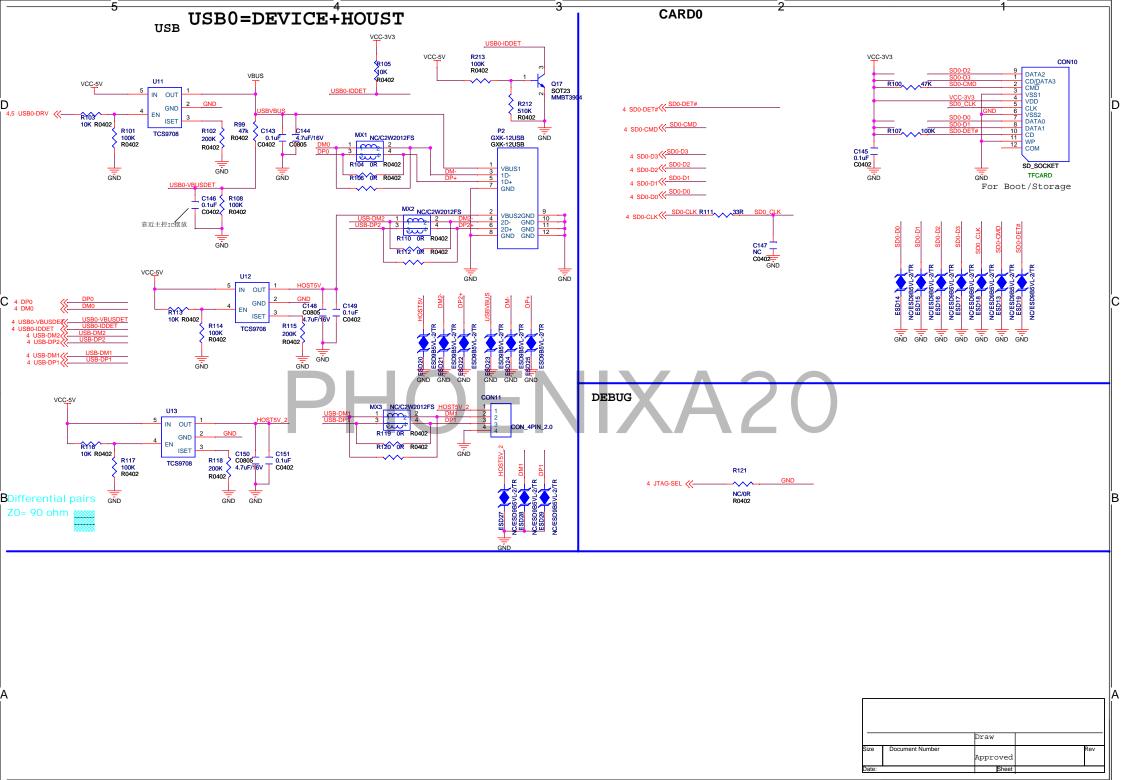




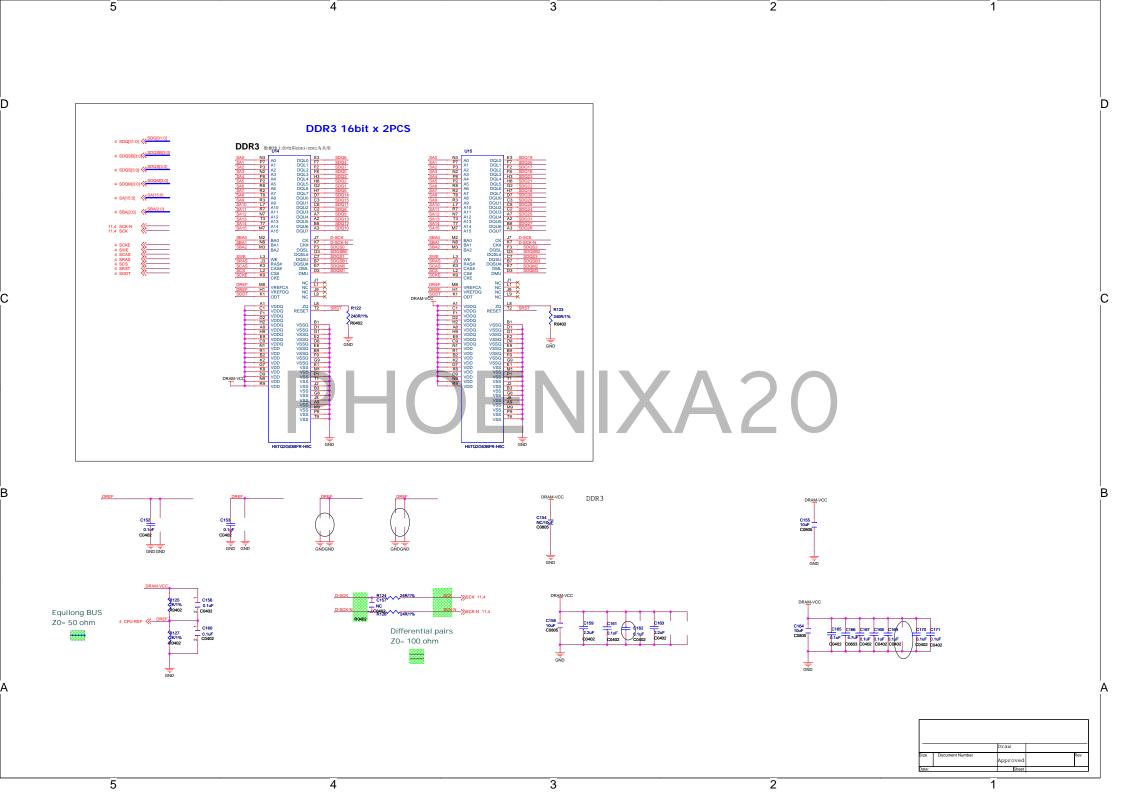


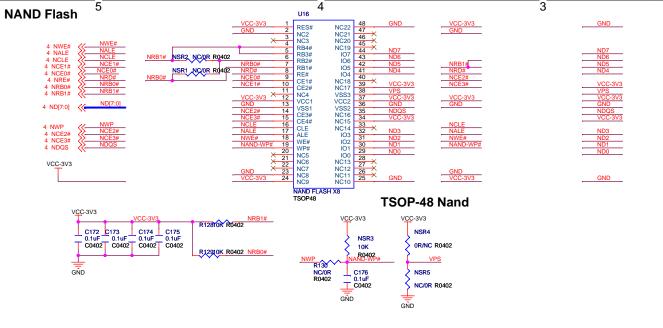




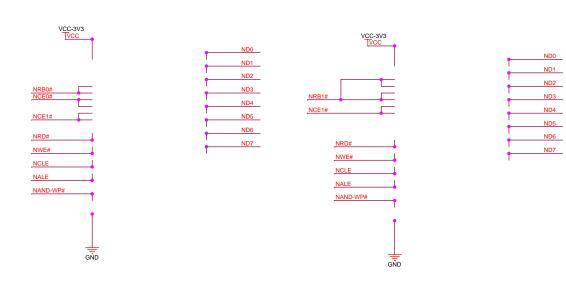


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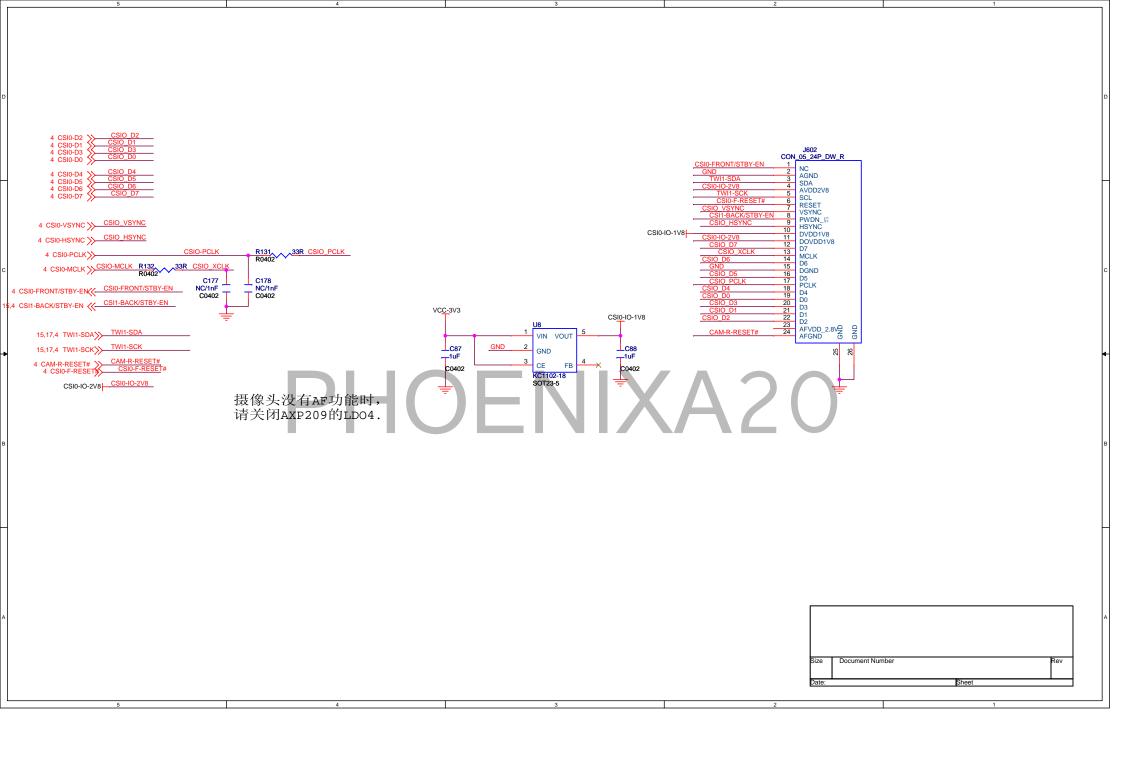


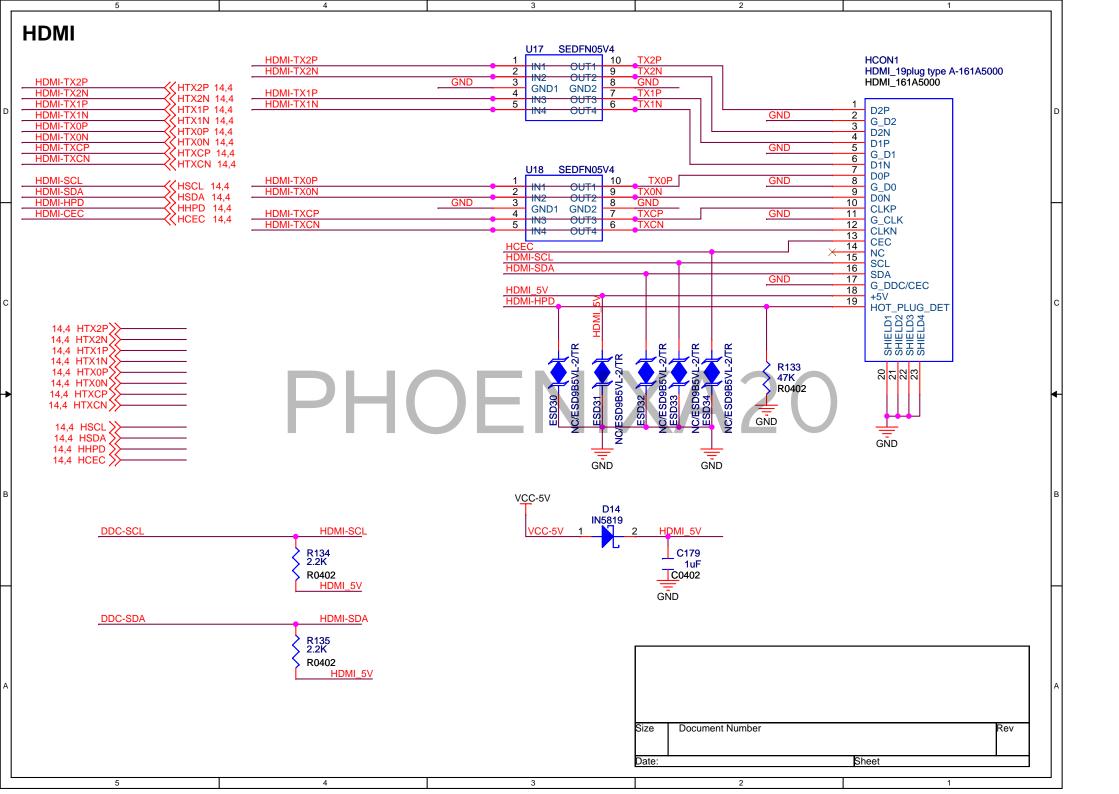
(1)接1片单片选Nand时,NSR2、NSR1断开 (2)接1片双片选Nand时,连接NSR2,断开NSR1 (3)接1片四片选Nand时,连接NSR1,断开NSR2 (4)接2片单片选或接2片双片选Nand时,连接NSR1,断开NSR2 (5)接Intel、Toshiba、Samsung 2xnm TSOP Nand时,NSR4连接,NSR5断开;其它的NSR4断开,NSR5连接

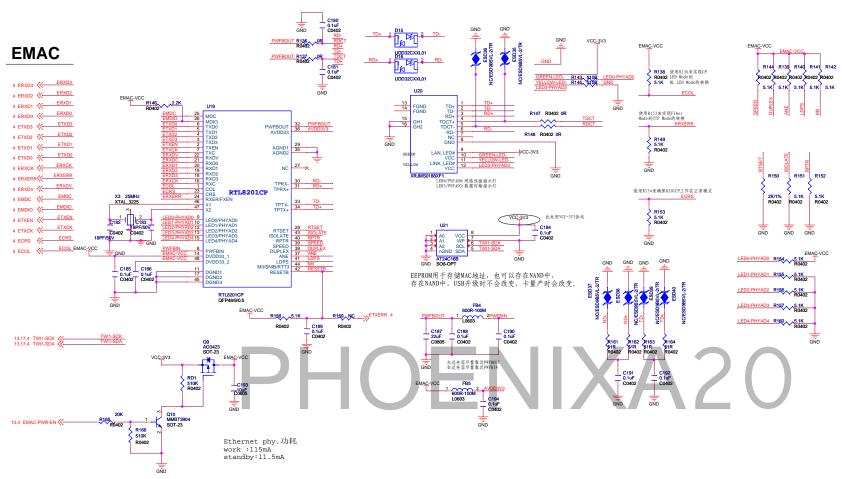


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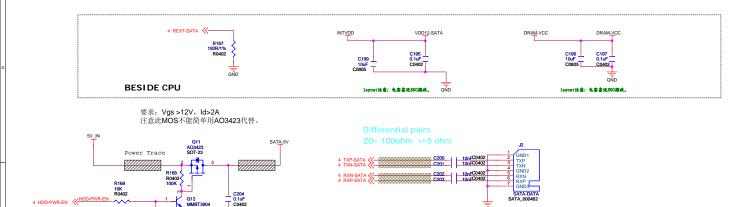


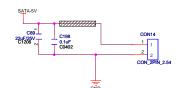
注意:此开关主要是为了在系统待机时,关斯HPY的电源,减少大概11.5mA的电流,对系统功耗有严格要求的产品才使用。对待机电流无特殊要求的产品,可以直接将VCC-3V3和EMAC-VCC连接起来,并把此部分电路删掉,而且软件不需更改。

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GND



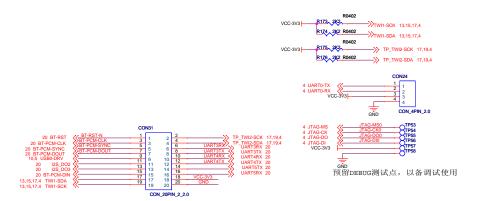


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4、开机操作:当A10处于关机状态时,MCU接收到遥控器的power健值。MCU先判断A10-POWER-DET#, 得判断A10-WOKA-DET,如果满足A10-POWER-DET#方低,A10-WOKR-DET为低,则MCU在PWR-ON#发出一个 1.5秒的低电平,PMU接收到之后,开始给A10供电、系统开始上电开机。

5. 其他操作:当软件出现异常需要复位系统时,一般采用的技院火牛的操作。如果在机器面板上定义了 POWR机械按键,或使用于开关机的办法与遥控器对PRA-ON的控制相同。机械按可以定义长按操作, MCU检测到长效时,编程使预见发出一个大700的低电平,PMU无条件接受机。

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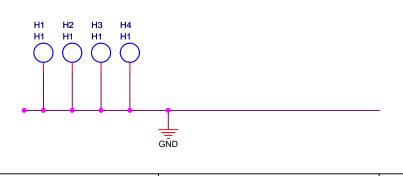


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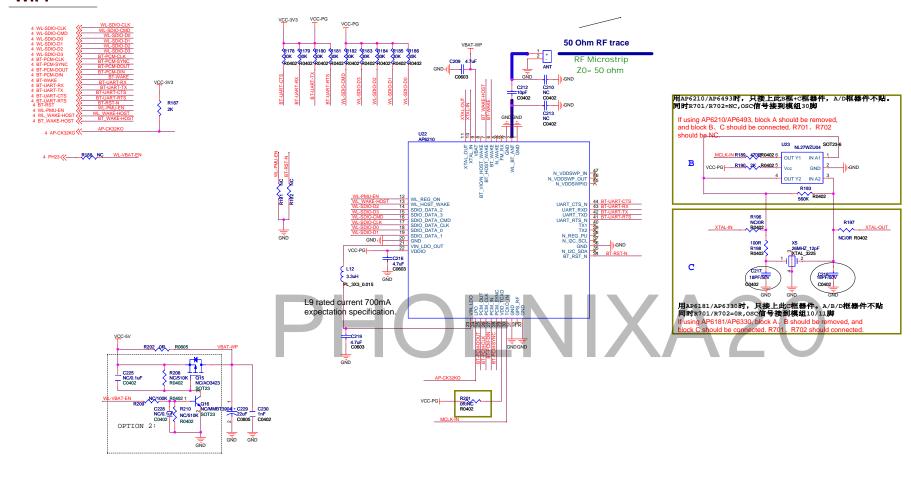
# PHOENIXA20

MASK1 MASK2 MASK3 MASK4
SMD SMD SMD SMD



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### WIFI



用AP6210/AP6476/AP6493时,接上此R705电阻 用AP6181/AP6330时,不用接上此R705电阻

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