

Instrução	Opcode	Sinais de controle															
		PcWE	Reg Write	Reg Dest	Branch	Jump	MemW	MemR	Select In	Resp		SrlCnt			AluCnt		
ADD	00000		1	1	0	0	X	X	0	0	0	X	X	X	0	0	0
ADDI	00001		1	0	0	0	X	X	1	0	0	X	X	X	0	0	1
SUB	00010		1	1	0	0	X	X	0	0	0	X	X	X	0	0	1
XOR	00011		1	1	0	0	X	X	0	0	0	X	X	X	1	0	0
XORI	00100		1	0	0	0	X	X	1	0	0	X	X	X	1	0	0
OR	00101		1	1	0	0	X	X	0	0	0	X	X	X	0	1	1
ORI	00110		1	0	0	0	X	X	1	0	0	X	X	X	0	1	1
AND	00111		1	1	0	0	X	X	0	0	0	X	X	X	0	1	0
ANDI	01000		1	0	0	0	X	X	1	0	0	X	X	X	0	1	0
BEQ	01001		0	X	1	0	X	X	0	X	X	X	X	X	1	0	1
BLT	01010		0	X	1	0	X	X	0	X	X	X	X	X	1	1	1
BGE	01011		0	X	1	0	X	X	0	X	X	X	X	X	1	1	0
LW	01100		1	0	0	0	0	1	1	0	1	X	X	X	0	0	0
SW	01101		0	0	0	0	1	0	1	0	0	X	X	X	0	0	0
JAL	01110		1	1	0	1	X	X	X	1	1	X	X	X	X	X	X
JUMP	01111		0	X	0	1	X	X	X	X	X	X	X	X	X	X	X
SLL	10010		1	1	0	0	X	X	0	1	0	0	0	0	X	X	X
SLR	10011		1	1	0	0	X	X	0	1	0	0	0	1	X	X	X
ROL	10100		1	1	0	0	X	X	0	1	0	0	1	0	X	X	X
ROR	10101		1	1	0	0	X	X	0	1	0	0	1	1	X	X	X
SRA	10110		1	1	0	0	X	X	0	1	0	1	0	0	X	X	X
ECALL	10000																
BREAK	10001																

ALUCNT	000	001	010	011	100	101	110	111
Function	A+B	A+EXT	A&B	A B	A XOR B	==	>=	<=

SrlCnt	000	001	010	011	100
Function	SLL	SLR	ROL	ROR	SRA