

Zynq Ultrascale pour KV260

Modifications apportées

DDR

- enable DDR controler
- Requested device frequency : 1200 MHz
- Memory type : DDR4
- Components : Components
- Effective DRAM bus width : 64 bits
- ECC : Disable
- Speed Bin : DDR4 2400P
- Cas latency : 16
- RAS to CAS : 16
- precharge time : 16
- Cas write latency : 16
- tRC : 47.06
- tRASmin : 33.0
- tFAW : 30.0
- additive latency : 0
- DRAM IC Bus width : 16 bits
- DRAM Device Capacity : 8192 MBits
- Bank group address count : 1
- Bank address count : 2
- Row address count : 16
- Column address count : 10
- uncheck Dual Rank

IO

GPIO

- GPIO0 MIO MIO 0..25
- GPIO1 MIO MIO 26..51

QSPI

- QSPI : single

SD/eMMC

- SD 1 MIO 39..51

SPI

- SPI 1 MIO 6..11

UART

- UART 1 36..37