

Rodolphe Thienard

HPC STUDENT

📞 (+33) 7 81 49 92 98 ✉ contact@rodolphethienard.fr 🌐 RodolpheThienard 🌐 Rodolphe Thienard

Experience

1st place at 2nd edition of Teratec Hackathon

Europe

HPC COMPETITION TO PORT AND OPTIMIZE APPLICATIONS ON AWS GRAVITON3E

2024

- Optimization of Riemann Zeta function
- Portage of EDF application, Telemac

End-of-studies internship

EMG2

TECHNOLOGICAL VALIDATION OF HARDWARE INTEGRATION OF A POSIT GPGPU CORE ON FPGA
FOR HPC/HPDA AND DEEP LEARNING APPLICATIONS

2024

- FPGA, RiscV architecture
- Posit & IEEE standard benchmarking
- HPC and AI applications

Master 1 Internship

The French Atomic Energy and Alternative Energies Commission (CEA)

DESIGNING A USER-SPACE THREAD SCHEDULER FOR MPC IN RUST

2023

- Low level programming
- Use of threads and libc
- Rust programming language

Bachelor Internship

David Laboritory, UVSQ

DEVELOPMENT OF A PROTOTYPE AS PART OF THE COD-PROF PROJECT WITH INSEE

2022

- Python scripting

Projects

Sudoku solver

Paris Saclay University

DEVELOPING AN APPLICATION FOR SOLVING SUDOKUS OF SIZES 2x2 TO 8x8. USING THE WAVE-FUNCTION COLLAPSE ALGORITHM AND THE CUDA LANGUAGE.

2024

- Cuda
- OpenMP Target
- Data sharing optimization

Programming project

Paris Saclay University

VALIDATION OF MODULAR ASSEMBLY QUALITY ANALYZER AND OPTIMIZER (MAQAO) METRICS

2022

- Micro-benchmark pour valider les mesures
- OpenMP
- Assembleur

Skills

Languages C/C++, Rust, Assembly, Cuda, Bash, Python, Zig

Spoken Languages French, English

Interests Volley-ball competition, Play music

Education

Master High Performance Computation and Simulation

Paris-Saclay University

2022 - 2024

Computer Science Bachelor's Degree

UVSQ University

2019 - 2022