

Model Name : GA-G41MT-ES2L

Revision 1.11

SHEET

TITLE

01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4 LGA775 A
05	P4 LGA775 B,D
06	P4 LGA775 C
07	P4 LGA775 E,F,G,H
08	G41 HOST
09	G41 DDRII
10	G41 PCI E, DMI
11	G41 VGA
12	G41 GND
13	G41 PWR
14	PCI EXPRESS*16 SLOT
15	DDR3 CHANNEL A
16	DDR3 CHANNEL B
17	DDR3 TERMINATION
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	CK505 CLOCK.
22	PCI SLOT 1,2,PCIE*1
23	IDE/FLOPPY
24	ITE 8718 GB
25	COM LPT
26	CI,HWM,KB/MS,DUALBIOS
27	ALC888B/888B-VD2

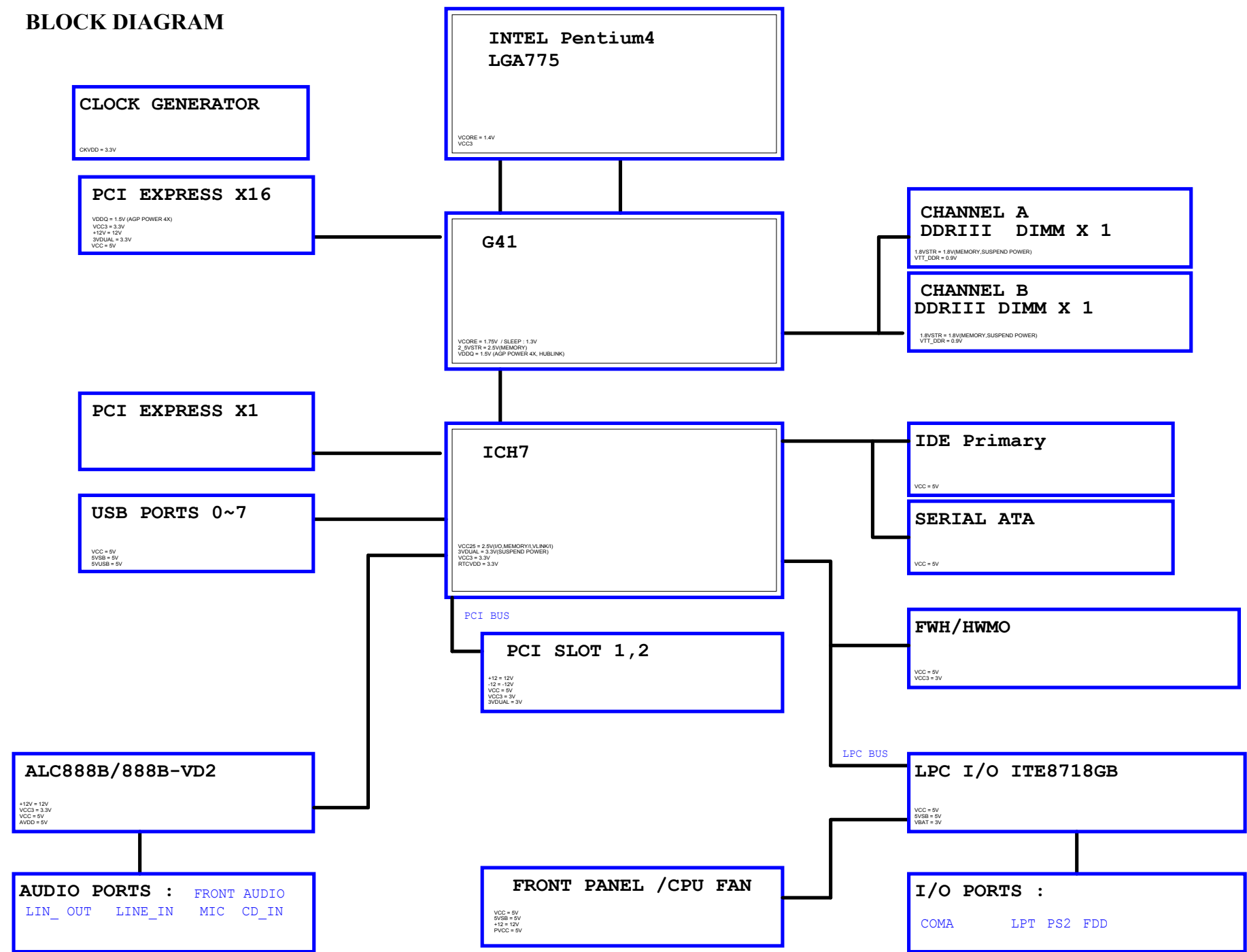
SHEET

TITLE

28	REAR AUDIO JACK
29	DISCRETE POWER
30	VCORE PWM ISL6312
31	ATX, OTHERS POWER
32	FRONT PANEL
33	REALTEK RTL8111E/8105E

<b>Gigabyte Technology</b>			
Title			
Cover Sheet			
Size	Document Number	Rev	
Custom	<b>GA-G41MT-ES2L</b>	<b>1.11</b>	
Date:	Tuesday, March 02, 2010	Sheet	1 of 33

BLOCK DIAGRAM

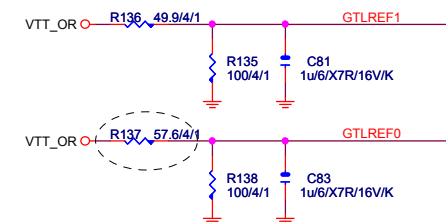
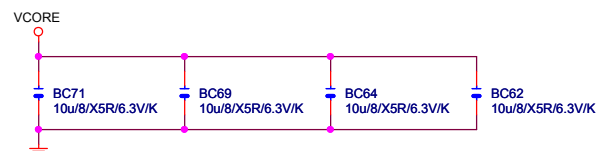
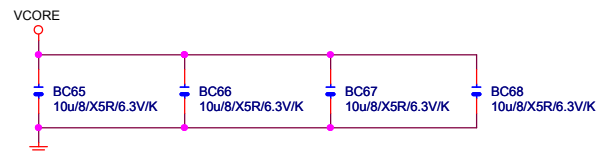


**Version: 1.11**

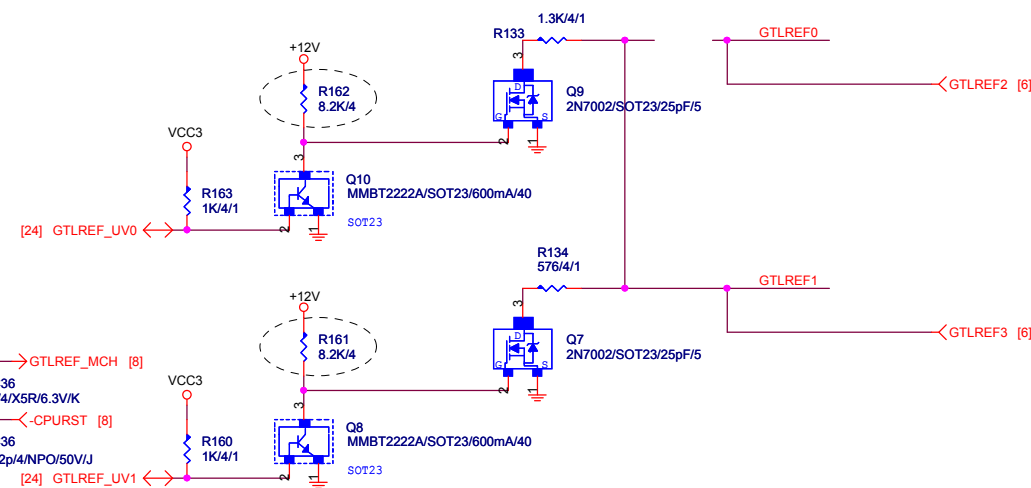
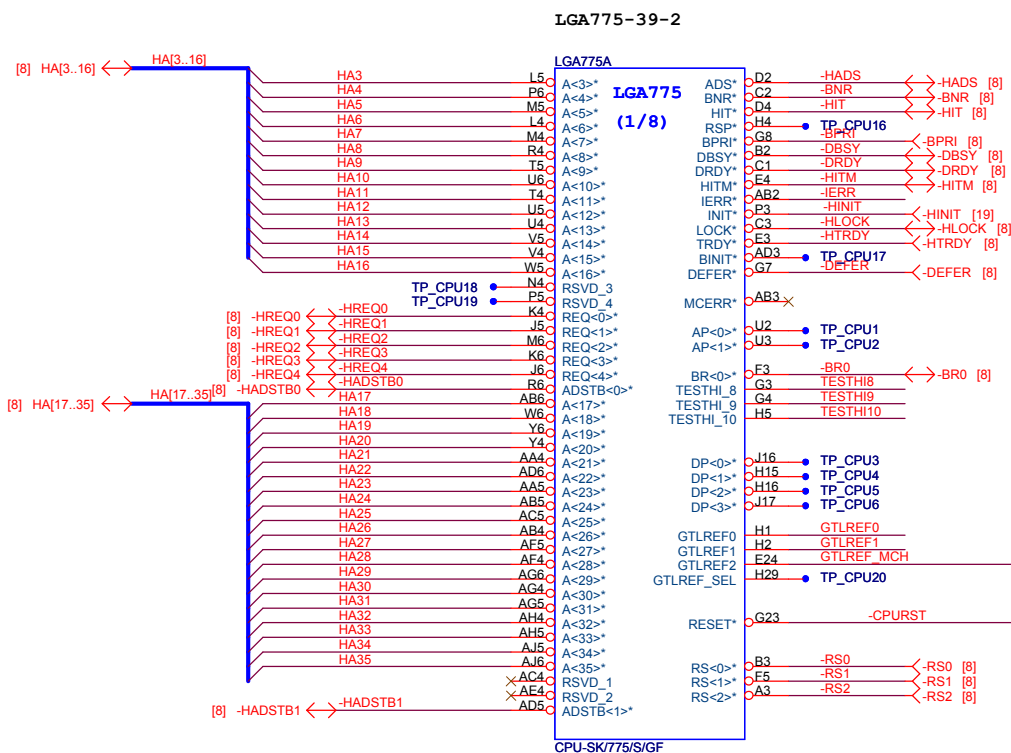
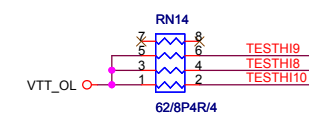
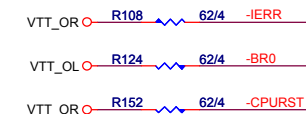
## 2010/03/02

[illegible][illegible]

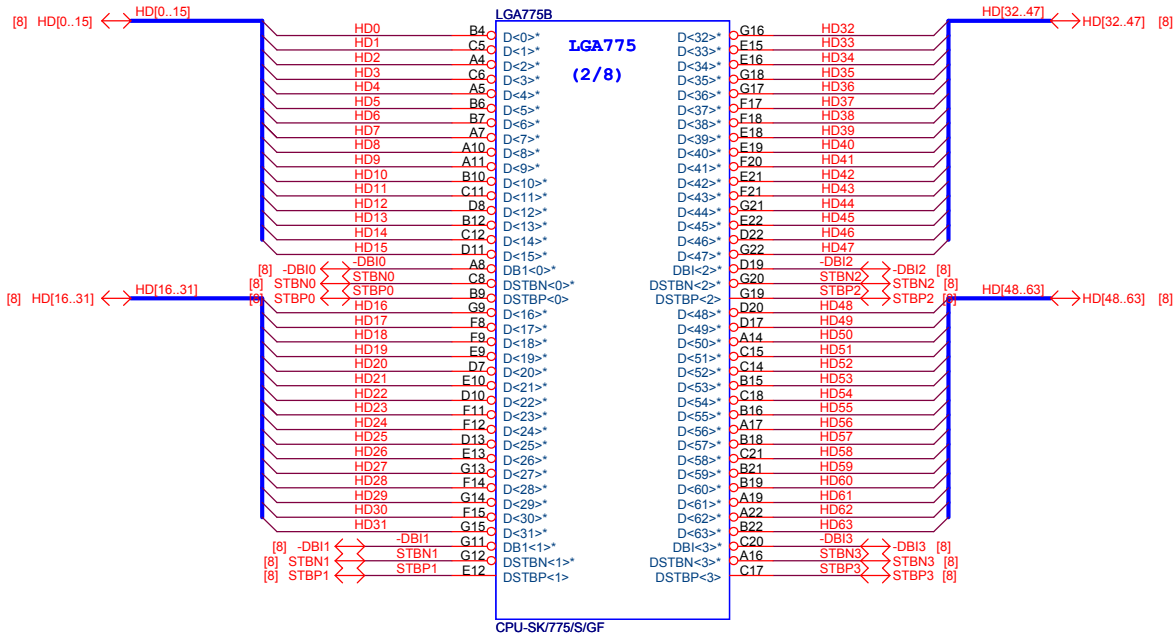
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<b>BOM &amp; PCB MODIFY HISTORY</b>			
Size Custom	Document Number	<b>GA-G41MT-ES2L</b>	Rev <b>1.1</b>
Date:	Tuesday, March 02, 2010	Sheet	3 of 33



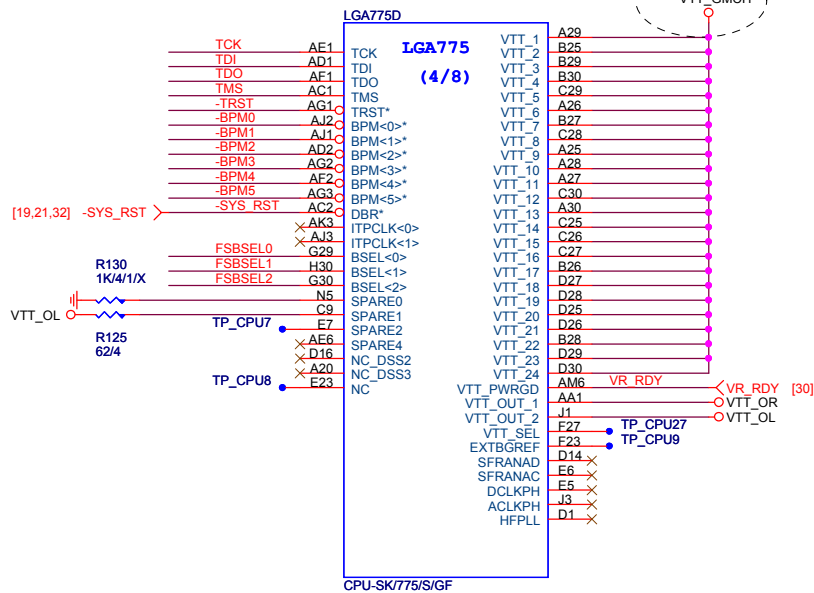
中間値0.9V



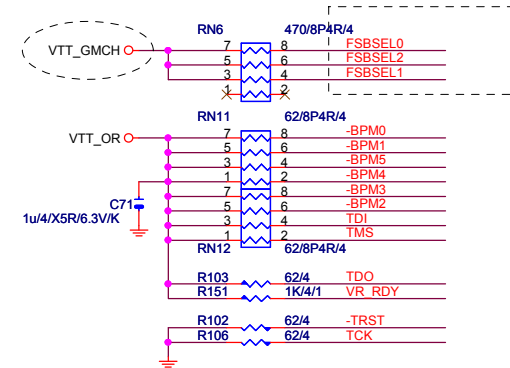
<p align="center"><b><i>Gigabyte Technology</i></b></p>				
<p align="center"><b>Title P4_LGA775-A</b></p>				
<p><b>Size B</b></p>	<p><b>Document Number GA-G41MT-ES2L</b></p>			<p><b>Rev 1.1</b></p>
<p><b>Date: Tuesday, March 02, 2010</b></p>		<p><b>Sheet 4 of 33</b></p>		



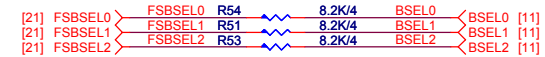
CPU-SK/775/S/GF



CPU-SK/775/S/GF



TO CLK GEN

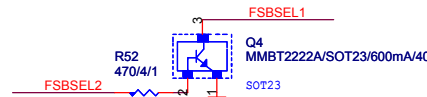


TO NB

## CPU

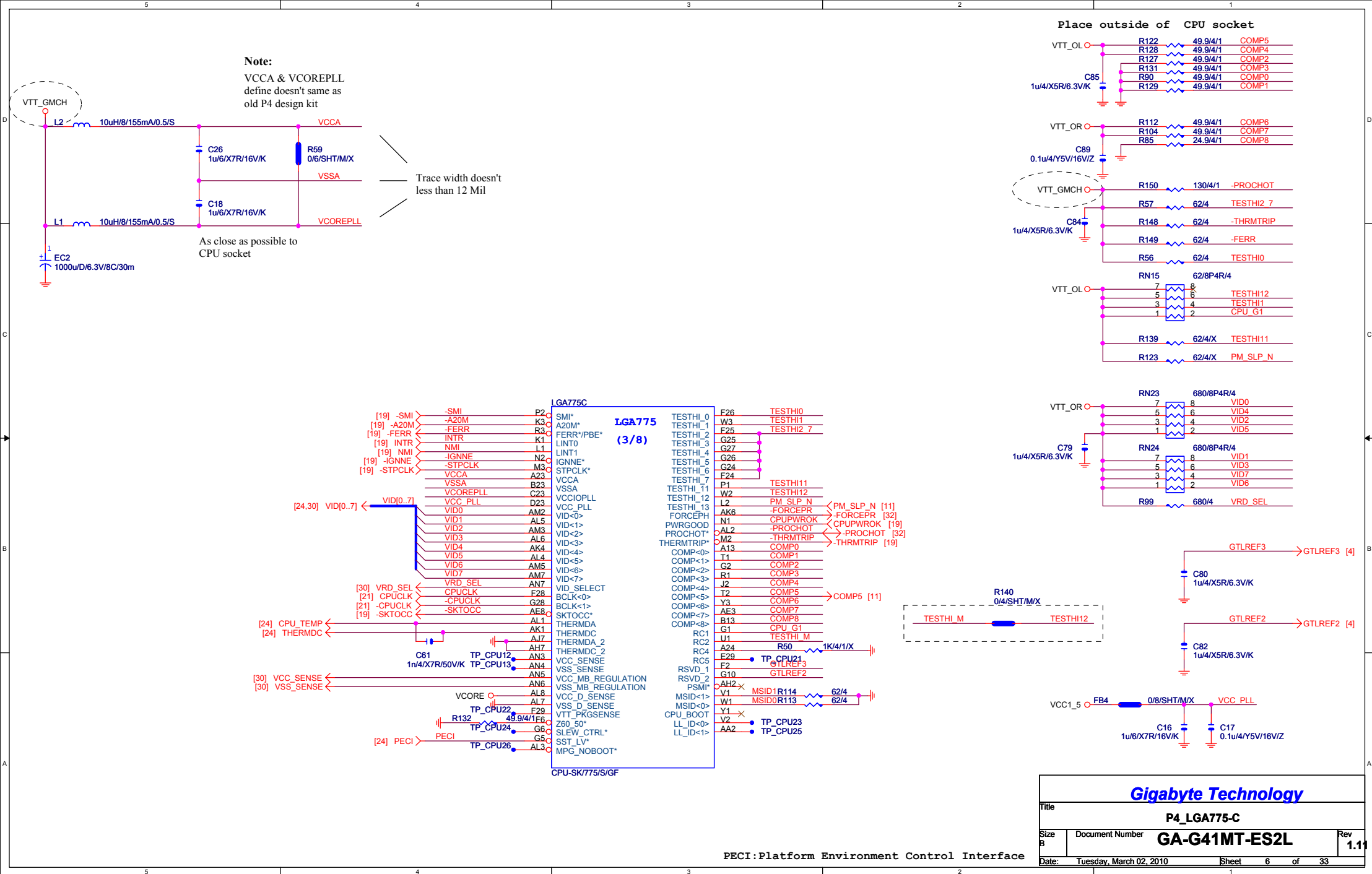
NA	FSB	FSA	
FSBSEL3	FSBSEL1	FSBSEL0	Clock
1	0	1	100MHz
0	0	1	133MHz
0	1	1	166MHz
0	1	0	200MHz
0	0	0	266MHz

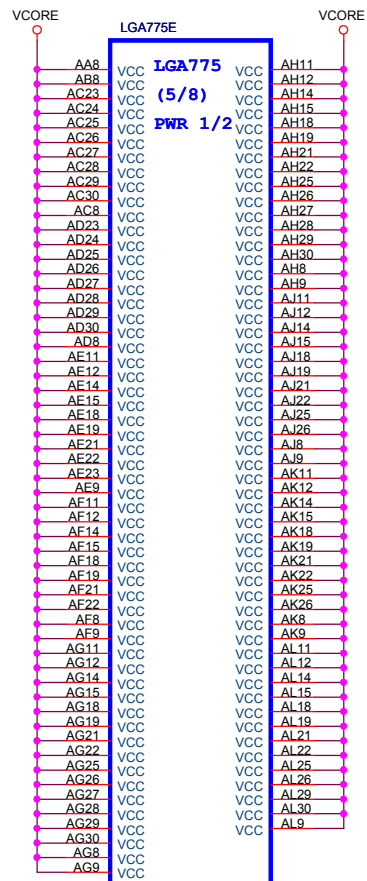
X



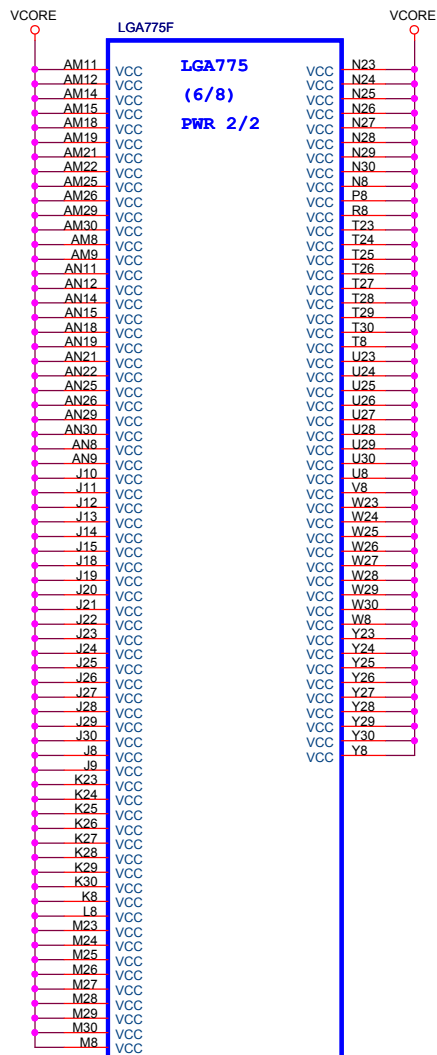
FIX FSB1600 LATCH FAIL

<b><i>Gigabyte Technology</i></b>			
Title			
P4_LGA775-B,D			
Size	Document Number	GA-G41MT-ES2L	Rev
B			1.11
Date:	Tuesday, March 02, 2010	Sheet	5 of 33

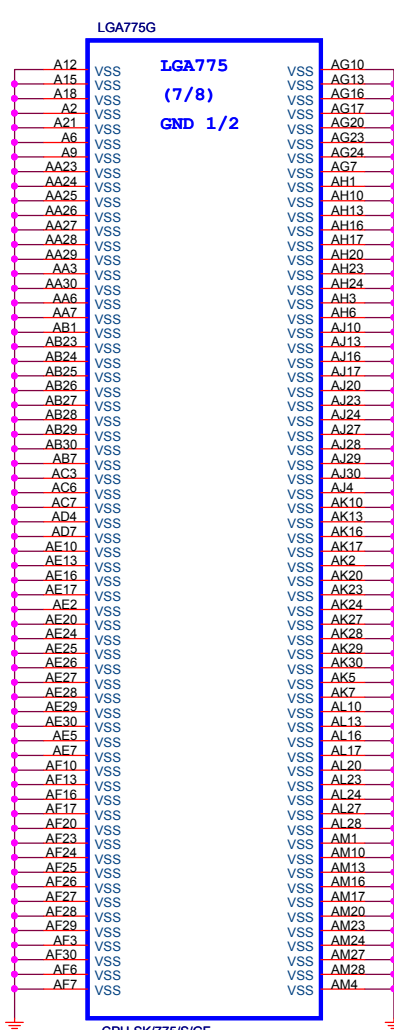




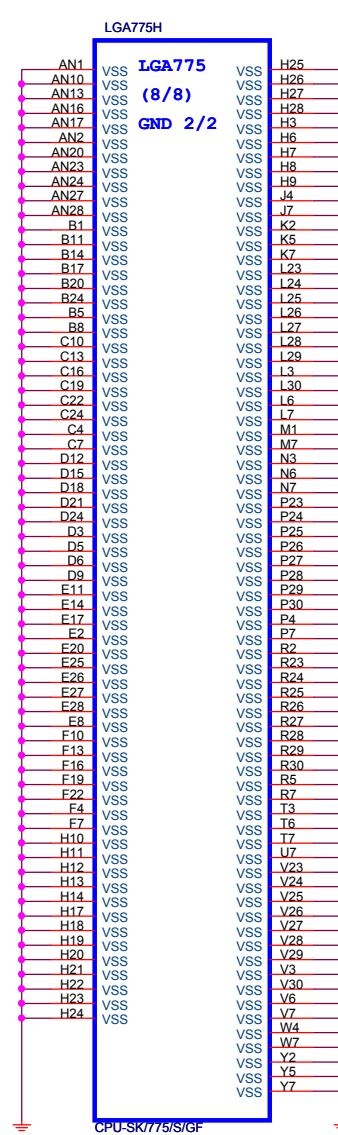
CPU-SK/775/S/GF



CPU-SK/775/S/GF



CPU-SK/775/S/GF



CPU-SK/775/S/GF





MCHC

**EL**  
(3/9)  
**DDR\_A**

BC41	DDR_A_MA_0	BC5	DQSA0
MAAA1	DDR_A_MA_1	BD4	DQSA0
MAAA2	DDR_A_MA_2	BC3	DMA1
MAAA3	DDR_A_MA_3		
MAAA4	DDR_A_MA_4	BC2	MDA1
MAAA5	DDR_A_MA_5	BD3	MDA1
MAAA6	DDR_A_MA_6	BD7	MDA2
MAAA7	DDR_A_MA_7	BD7	MDA3
MAAA8	DDR_A_MA_8	BD2	MDA4
MAAA9	DDR_A_MA_9	BA3	MDA5
MAAA10	DDR_A_MA_10	BE6	MDA6
MAAA11	DDR_A_MA_11		
MAAA12	DDR_A_MA_12	BD6	MDA7
MAAA13	DDR_A_MA_13	BD9	DQSA1
MAAA14	DDR_A_MA_14	BC9	DQSA1
AW42	DDR_A_WEB	BD9	DMA1
SCASA	DDR_A_CASB	BB8	MDA8
SRASA	DDR_A_RASB	BA8	MDA9
		BD11	MDA10
		BD11	MDA11
		BD11	MDA12
		BE8	MDA13
		BD10	MDA14
		AY11	MDA15
BD15	DDR_A_DQS_2	BD15	DQSA2
BD15	DDR_A_DQS_2	BD15	DQSA2
BD14	DDR_A_DM_2	BD14	DMA2
BB27	DDR_A_CKE_0	BB14	MDA16
CKEA1	DDR_A_CKE_1	BC14	MDA17
		BC16	MDA18
		BB16	MDA19
		BC15	MDA20
		BE12	MDA21
		BA15	MDA22
		BD16	MDA23
AR22	DDR_A_DQS_3	AR22	DQSA3
DA122	DDR_A_DQS_3	DA122	DQSA3
AV22	DDR_A_DM_3	AV22	DMA3
AW21	DDR_A_DQ_24	AW21	MDA24
AY22	DDR_A_DQ_25	AY22	MDA25
AY23	DDR_A_DQ_26	AY23	MDA26
AY24	DDR_A_DQ_27	AY24	MDA27
AY25	DDR_A_DQ_28	AY25	MDA28
AY26	DDR_A_DQ_29	AY26	MDA29
AY27	DDR_A_DQ_30	AY27	MDA30
AY28	DDR_A_DQ_31	AY28	MDA31
AH43	DDR_A_DQS_4	AH43	DQSA4
AK42	DDR_A_DM_4	AK42	DMA4
AL41	DDR_A_DQ_32	AL41	MDA32
AK43	DDR_A_DQ_33	AK43	MDA33
AG42	DDR_A_DQ_34	AG42	MDA34
AG44	DDR_A_DQ_35	AG44	MDA35
AL42	DDR_A_DQ_36	AL42	MDA36
AK44	DDR_A_DQ_37	AK44	MDA37
AG44	DDR_A_DQ_38	AG44	MDA38
AG41	DDR_A_DQ_39	AG41	MDA39
AD43	DDR_A_DQS_5	AD43	DQSA5
AE42	DDR_A_DQS_5	AE42	DQSA5
AE45	DDR_A_DM_5	AE45	DMA5
AF43	DDR_A_DQ_40	AF43	MDA40
AF42	DDR_A_DQ_41	AF42	MDA41
AC44	DDR_A_DQ_42	AC44	MDA42
AC42	DDR_A_DQ_43	AC42	MDA43
AF40	DDR_A_DQ_44	AF40	MDA44
AF44	DDR_A_DQ_45	AF44	MDA45
AD44	DDR_A_DQ_46	AD44	MDA46
AC41	DDR_A_DQ_47	AC41	MDA47
Y43	DDR_A_DQS_6	Y43	DQSA6
Y42	DDR_A_DQS_6	Y42	DQSA6
AM45	DDR_A_DM_6	AM45	DMA6
AB43	DDR_A_DQ_48	AB43	MDA48
AA42	DDR_A_DQ_49	AA42	MDA49
WA42	DDR_A_DQ_50	WA42	MDA50
WA41	DDR_A_DQ_51	WA41	MDA51
AB42	DDR_A_DQ_52	AB42	MDA52
AB44	DDR_A_DQ_53	AB44	MDA53
Y44	DDR_A_DQ_54	Y44	MDA54
Y40	DDR_A_DQ_55	Y40	MDA55
T44	DDR_A_DQS_7	T44	DQSA7
T43	DDR_A_DQS_7	T43	DQSA7
T42	DDR_A_DM_7	T42	DMA7
Y42	DDR_A_DQ_56	Y42	MDA56
U45	DDR_A_DQ_57	U45	MDA57
R40	DDR_A_DQ_58	R40	MDA58
P44	DDR_A_DQ_59	P44	MDA59
V44	DDR_A_DQ_60	V44	MDA60
V43	DDR_A_DQ_61	V43	MDA61
R41	DDR_A_DQ_62	R41	MDA62
R44	DDR_A_DQ_63	R44	MDA63

G41A3[10H81-030G41-10R\_10H81-030G41-20R]

MDA0.63	MDA0.63	[15]
MAAA0.14	MAAA0.14	[15]
MODT_A0.1	MODT_A0.1	[15]
DQSA0.7	DQSA0.7	[15]
DQSA0.7	DQSA0.7	[15]
DMA0.7	DMA0.7	[15]

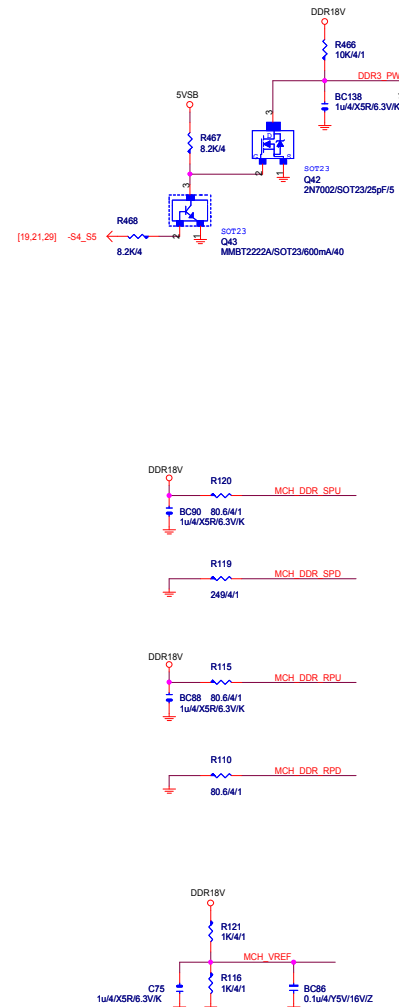
MCHD

**EL**  
(4/9)  
**DDR\_B**

MAA80	DDR_B_MA_0	MAA80	DDR_B_MA_0
MAA81	DDR_B_MA_1	MAA81	DDR_B_MA_1
MAA82	DDR_B_MA_2	MAA82	DDR_B_MA_2
MAA83	DDR_B_MA_3	MAA83	DDR_B_MA_3
MAA84	DDR_B_MA_4	MAA84	DDR_B_MA_4
MAA85	DDR_B_MA_5	MAA85	DDR_B_MA_5
MAA86	DDR_B_MA_6	MAA86	DDR_B_MA_6
MAA87	DDR_B_MA_7	MAA87	DDR_B_MA_7
MAA88	DDR_B_MA_8	MAA88	DDR_B_MA_8
MAA89	DDR_B_MA_9	MAA89	DDR_B_MA_9
MAA90	DDR_B_MA_10	MAA90	DDR_B_MA_10
MAA91	DDR_B_MA_11	MAA91	DDR_B_MA_11
MAA92	DDR_B_MA_12	MAA92	DDR_B_MA_12
MAA93	DDR_B_MA_13	MAA93	DDR_B_MA_13
MAA94	DDR_B_MA_14	MAA94	DDR_B_MA_14
-SWEB	DDR_B_WEB	-SWEB	DDR_B_WEB
-SCASB	DDR_B_CASB	-SCASB	DDR_B_CASB
-SRASB	DDR_B_RASB	-SRASB	DDR_B_RASB
SBAB0	DDR_B_BS_0	SBAB0	DDR_B_BS_0
SBAB1	DDR_B_BS_1	SBAB1	DDR_B_BS_1
SBAB2	DDR_B_BS_2	SBAB2	DDR_B_BS_2
CSB0	DDR_B_CSB_0	CSB0	DDR_B_CSB_0
CSB1	DDR_B_CSB_1	CSB1	DDR_B_CSB_1
CKEB0	DDR_B_CKE_0	CKEB0	DDR_B_CKE_0
CKEB1	DDR_B_CKE_1	CKEB1	DDR_B_CKE_1
MODT_B0	DDR_B_ODT_0	MODT_B0	DDR_B_ODT_0
MODT_B1	DDR_B_ODT_1	MODT_B1	DDR_B_ODT_1
DCLKB0	DDR_B_CK_0	DCLKB0	DDR_B_CK_0
DCLKB1	DDR_B_CK_1	DCLKB1	DDR_B_CK_1
DCLKB2	DDR_B_CK_2	DCLKB2	DDR_B_CK_2
AR38	DDR_B_DQS_4	AR38	DDR_B_DQS_4
AL37	DDR_B_DM_4	AL37	DMA4
AR36	DDR_B_DQ_32	AR36	MDB32
AL38	DDR_B_DQ_33	AL38	MDB33
AN37	DDR_B_DQ_34	AN37	MDB34
AV39	DDR_B_DQ_35	AV39	MDB35
AN39	DDR_B_DQ_36	AN39	MDB36
AN40	DDR_B_DQ_37	AN40	MDB37
AN41	DDR_B_DQ_38	AN41	MDB38
AK34	DDR_B_DQS_5	AK34	DQSB5
AL34	DDR_B_DM_5	AL34	DMA5
AL35	DDR_B_DQ_40	AL35	MDB40
AL36	DDR_B_DQ_41	AL36	MDB41
AK36	DDR_B_DQ_42	AK36	MDB42
AL34	DDR_B_DQ_43	AL34	MDB43
AN39	DDR_B_DQ_44	AN39	MDB44
AN40	DDR_B_DQ_45	AN40	MDB45
AK37	DDR_B_DQ_46	AK37	MDB46
AL39	DDR_B_DQ_47	AL39	MDB47
AE37	DDR_B_DQS_6	AE37	DQSB6
AE36	DDR_B_DQS_6	AE36	DQSB6
AL35	DDR_B_DM_6	AL35	DMA6
AL38	DDR_B_DQ_48	AL38	MDB48
AL37	DDR_B_DQ_49	AL37	MDB49
AE38	DDR_B_DQ_50	AE38	MDB50
AE37	DDR_B_DQ_51	AE37	MDB51
AK40	DDR_B_DQ_52	AK40	MDB52
AK40	DDR_B_DQ_53	AK40	MDB53
AE34	DDR_B_DQ_54	AE34	MDB54
AE35	DDR_B_DQS_55	AE35	DQSB55
AB35	DDR_B_DQS_55	AB35	DQSB55
AD35	DDR_B_DM_55	AD35	DMA55
AD37	DDR_B_DM_7	AD37	DMA7
AD40	DDR_B_DQ_56	AD40	MDB56
AD38	DDR_B_DQ_57	AD38	MDB57
AB40	DDR_B_DQ_58	AB40	MDB58
AE39	DDR_B_DQ_59	AE39	MDB59
AE36	DDR_B_DQ_60	AE36	MDB60
AE39	DDR_B_DQ_61	AE39	MDB61
AB37	DDR_B_DQ_62	AB37	MDB62
AB38	DDR_B_DQ_63	AB38	MDB63

G41A3[10H81-030G41-10R\_10H81-030G41-20R]

MDB0.63	MDB0.63	[16]
MAAB0.14	MAAB0.14	[16]
MODT_B0.1	MODT_B0.1	[16]
DQSB0.7	DQSB0.7	[16]
DQSB0.7	DQSB0.7	[16]
DMA0.7	DMA0.7	[16]



MCHB

EXP\_A\_RXP0 F6  
EXP\_A\_RXN0 G7  
EXP\_A\_RXP1 H6  
EXP\_A\_RXN1 G4  
EXP\_A\_RXP2 J6  
EXP\_A\_RXN2 J7  
EXP\_A\_RXP3 L6

PEG\_RXP\_0  
PEG\_RXN\_0  
PEG\_RXP\_1  
PEG\_RXN\_1  
PEG\_RXP\_2  
PEG\_RXN\_2  
PEG\_RXP\_3(HB\_HPD)

HDMI  
/DVI  
(B)

(HBTX2)PEG\_TXP\_0  
(HBTX2#)PEG\_TXN\_0  
(HBTX1)PEG\_TXP\_1  
(HBTX1#)PEG\_TXN\_1  
(HBTX0)PEG\_TXP\_2  
(HBTX0#)PEG\_TXN\_2  
(HBTXC)PEG\_TXP\_3  
(HBTXC#)PEG\_TXN\_3

C11 EXP\_A\_TXP0  
B11 EXP\_A\_TXN0  
A10 EXP\_A\_TXP1  
B9 EXP\_A\_TXN1  
C9 EXP\_A\_TXP2  
D8 EXP\_A\_TXN2  
B8 EXP\_A\_TXP3  
C7 EXP\_A\_TXN3

EXP\_A\_TXP[0..15] >>> EXP\_A\_TXP[0..15] [14]  
EXP\_A\_TXN[0..15] >>> EXP\_A\_TXN[0..15] [14]  
EXP\_A\_RXP[0..15] >>> EXP\_A\_RXP[0..15] [14]  
EXP\_A\_RXN[0..15] >>> EXP\_A\_RXN[0..15] [14]

EXP\_A\_RXN3 L7  
EXP\_A\_RXP4 N9  
EXP\_A\_RXN4 N10  
EXP\_A\_RXP5 N7  
EXP\_A\_RXN5 N6  
EXP\_A\_RXP6 R7  
EXP\_A\_RXN6 R6  
EXP\_A\_RXP7 R9

PEG\_RXN\_3  
PEG\_RXP\_4  
PEG\_RXN\_4  
PEG\_RXP\_5  
PEG\_RXN\_5  
PEG\_RXP\_6  
PEG\_RXN\_6  
PEG\_RXP\_7(HC\_HPD)

HDMI  
/DVI  
(C)

(HCTX2)PEG\_TXP\_4  
(HCTX2#)PEG\_TXN\_4  
(HCTX1)PEG\_TXP\_5  
(HCTX1#)PEG\_TXN\_5  
(HCTX0)PEG\_TXP\_6  
(HCTX0#)PEG\_TXN\_6  
(HCTXC)PEG\_TXP\_7  
(HCTXC#)PEG\_TXN\_7

B7 EXP\_A\_TXP4  
B6 EXP\_A\_TXN4  
B3 EXP\_A\_TXP5  
B4 EXP\_A\_TXN5  
D2 EXP\_A\_TXP6  
C2 EXP\_A\_TXN6  
H2 EXP\_A\_TXP7  
G2 EXP\_A\_TXN7

EXP\_A\_RXN7 R10  
EXP\_A\_RXP8 U10  
EXP\_A\_RXN8 U9  
EXP\_A\_RXP9 U6  
EXP\_A\_RXN9 U7  
EXP\_A\_RXP10 AA9  
EXP\_A\_RXN10 AA10  
EXP\_A\_RXP11 R4  
EXP\_A\_RXN11 P4  
EXP\_A\_RXP12 AA7  
EXP\_A\_RXN12 AA6  
EXP\_A\_RXP13 AB10  
EXP\_A\_RXN13 AB9  
EXP\_A\_RXP14 AB3  
EXP\_A\_RXN14 AA2  
EXP\_A\_RXP15 AD10  
EXP\_A\_RXN15 AD11

PEG\_RXN\_7  
PEG\_RXP\_8  
PEG\_RXN\_8  
PEG\_RXP\_9  
PEG\_RXN\_9  
PEG\_RXP\_10  
PEG\_RXN\_10  
PEG\_RXP\_11  
PEG\_RXN\_11  
PEG\_RXP\_12  
PEG\_RXN\_12  
PEG\_RXP\_13  
PEG\_RXN\_13  
PEG\_RXP\_14  
PEG\_RXN\_14  
PEG\_RXP\_15  
PEG\_RXN\_15

EL  
(2/9)

PCIE

PEG\_TXP\_8  
PEG\_TXN\_8  
PEG\_TXP\_9  
PEG\_TXN\_9  
PEG\_TXP\_10  
PEG\_TXN\_10  
PEG\_TXP\_11  
PEG\_TXN\_11  
PEG\_TXP\_12  
PEG\_TXN\_12  
PEG\_TXP\_13  
PEG\_TXN\_13  
PEG\_TXP\_14  
PEG\_TXN\_14  
PEG\_TXP\_15  
PEG\_TXN\_15

J2 EXP\_A\_TXP8  
K2 EXP\_A\_TXN8  
K1 EXP\_A\_TXP9  
L2 EXP\_A\_TXN9  
P2 EXP\_A\_TXP10  
M2 EXP\_A\_TXN10  
T2 EXP\_A\_TXP11  
R1 EXP\_A\_TXN11  
U2 EXP\_A\_TXP12  
V2 EXP\_A\_TXN12  
W4 EXP\_A\_TXP13  
V3 EXP\_A\_TXN13  
AA4 EXP\_A\_TXP14  
Y4 EXP\_A\_TXN14  
AC1 EXP\_A\_TXP15  
AB2 EXP\_A\_TXN15

Close to MCH

[18] DMI\_MCH\_IT\_MR\_0\_DP >>> DMI\_MCH\_IT\_MR\_0\_DP AD7  
[18] DMI\_MCH\_IT\_MR\_0\_DN >>> DMI\_MCH\_IT\_MR\_0\_DN AD8  
[18] DMI\_MCH\_IT\_MR\_1\_DP >>> DMI\_MCH\_IT\_MR\_1\_DP AE9  
[18] DMI\_MCH\_IT\_MR\_1\_DN >>> DMI\_MCH\_IT\_MR\_1\_DN AE10  
[18] DMI\_MCH\_IT\_MR\_2\_DP >>> DMI\_MCH\_IT\_MR\_2\_DP AE6  
[18] DMI\_MCH\_IT\_MR\_2\_DN >>> DMI\_MCH\_IT\_MR\_2\_DN AE7  
[18] DMI\_MCH\_IT\_MR\_3\_DP >>> DMI\_MCH\_IT\_MR\_3\_DP AF9  
[18] DMI\_MCH\_IT\_MR\_3\_DN >>> DMI\_MCH\_IT\_MR\_3\_DN AF8

DMI\_RXP\_0  
DMI\_RXN\_0  
DMI\_RXP\_1  
DMI\_RXN\_1  
DMI\_RXP\_2  
DMI\_RXN\_2  
DMI\_RXP\_3  
DMI\_RXN\_3

DMI

DMI\_TXP\_0  
DMI\_TXN\_0  
DMI\_TXP\_1  
DMI\_TXN\_1  
DMI\_TXP\_2  
DMI\_TXN\_2  
DMI\_TXP\_3  
DMI\_TXN\_3

AC2 DMI\_MCH\_MT\_IR\_0\_DP C50  
AD2 DMI\_MCH\_MT\_IR\_0\_DN C52  
AD4 DMI\_MCH\_MT\_IR\_1\_DP C51  
AE4 DMI\_MCH\_MT\_IR\_1\_DN C53  
AE2 DMI\_MCH\_MT\_IR\_2\_DP C54  
AE2 DMI\_MCH\_MT\_IR\_2\_DN C57  
AF4 DMI\_MCH\_MT\_IR\_3\_DP C55  
AG4 DMI\_MCH\_MT\_IR\_3\_DN C58

0.1u/4/X7R/16V/K >>> DMI\_ICH\_MT\_IR\_0\_DP [18]  
0.1u/4/X7R/16V/K >>> DMI\_ICH\_MT\_IR\_0\_DN [18]  
0.1u/4/X7R/16V/K >>> DMI\_ICH\_MT\_IR\_1\_DP [18]  
0.1u/4/X7R/16V/K >>> DMI\_ICH\_MT\_IR\_1\_DN [18]  
0.1u/4/X7R/16V/K >>> DMI\_ICH\_MT\_IR\_2\_DP [18]  
0.1u/4/X7R/16V/K >>> DMI\_ICH\_MT\_IR\_2\_DN [18]  
0.1u/4/X7R/16V/K >>> DMI\_ICH\_MT\_IR\_3\_DP [18]  
0.1u/4/X7R/16V/K >>> DMI\_ICH\_MT\_IR\_3\_DN [18]

[21] SRCCLK\_MCH >>> SRCCLK\_MCH D9  
[21] -SRCCLK\_MCH >>> -SRCCLK\_MCH E9

EXP\_CLKP  
EXP\_CLKN  
SDVO\_CTRLDATA  
SDVO\_CTRLCLK

EXP\_RCOMP  
EXP\_COMPI  
EXP\_ICOMP

Y7  
Y8  
Y6

GRCOMP  
VCC1\_1  
R93  
49.9/4/1

[14] SDVO\_CTRL\_DATA >>> SDVO\_CTRL\_DATA J13  
[14] SDVO\_CTRL\_CLK >>> SDVO\_CTRL\_CLK G13

PORTB

AB13  
AD13

EXP\_RBIAS

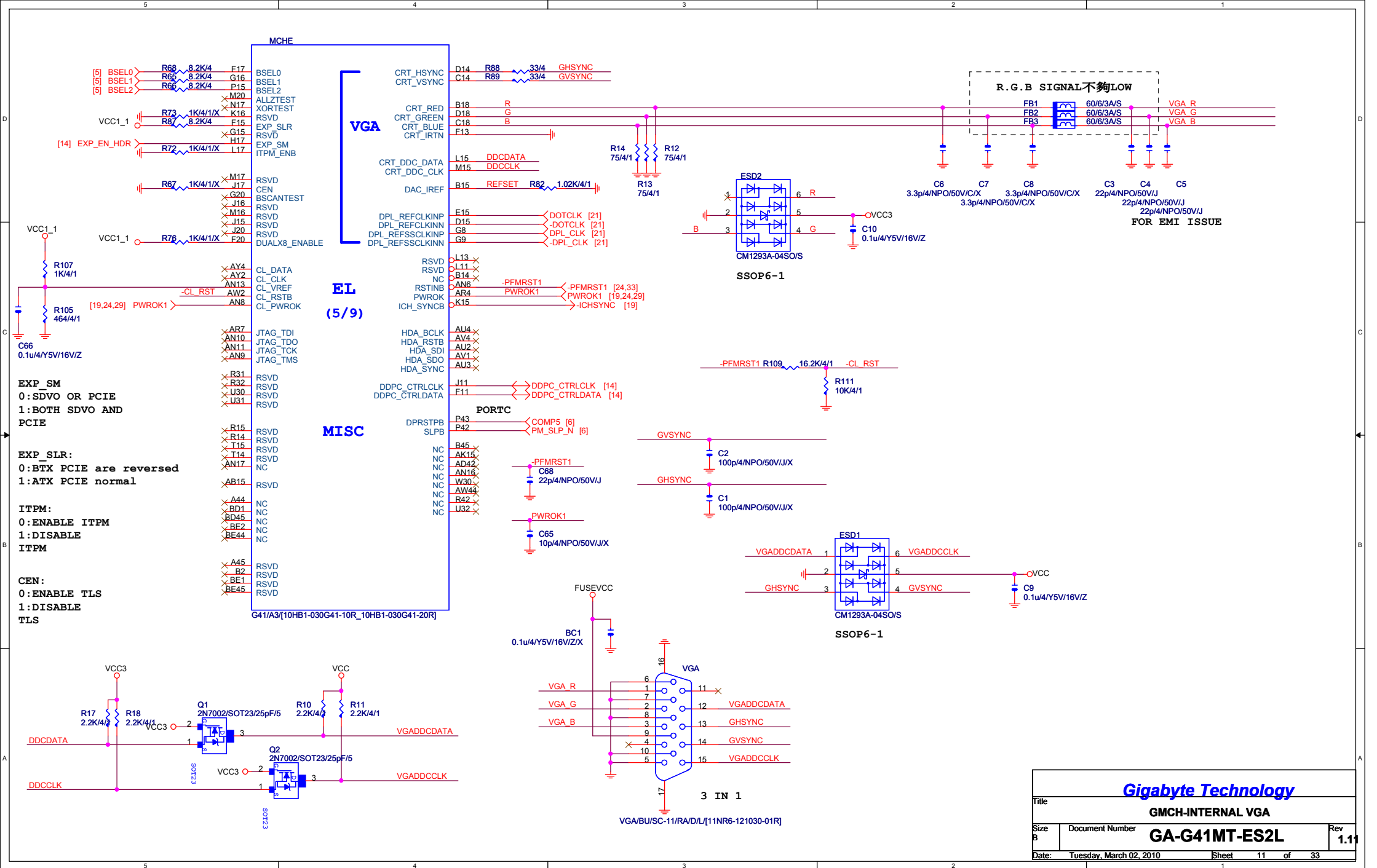
AG1

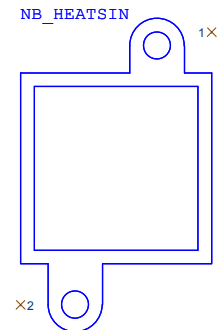
GRBIAS  
R96  
750/4/1

G41/A3(10HB1-030G41-10R\_10HB1-030G41-20R)

Gigabyte Technology

Title		
GMCH-PCI E & DMI		
Size	Document Number	Rev
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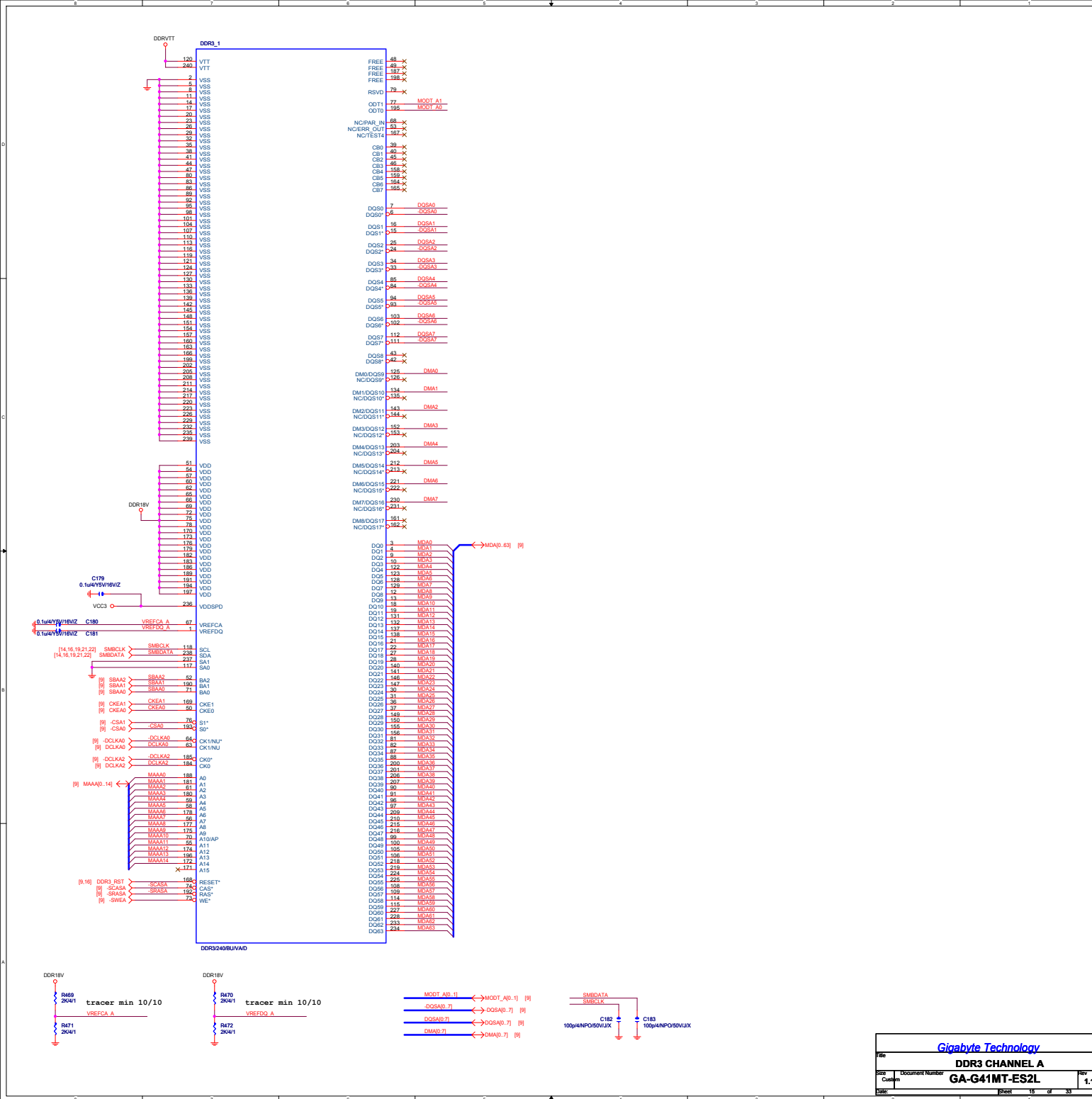


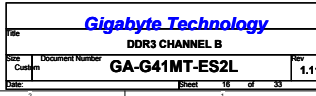
BGASINK445A-L GRAY 20MM

<b><i>Gigabyte Technology</i></b>			
Title <b>GMCH-GND</b>			
Size Custom	Document Number	<b>GA-G41MT-ES2L</b>	Rev <b>1.11</b>
Date:	<b>Tuesday, March 02, 2010</b>	Sheet	<b>12 of 33</b>



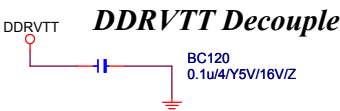
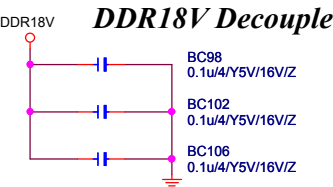
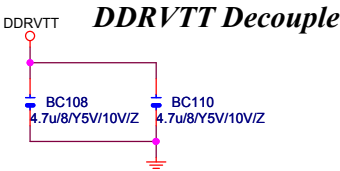
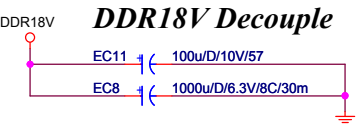




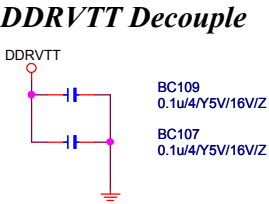
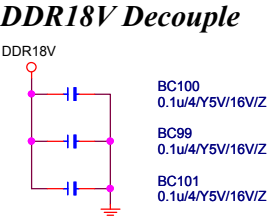




DDR TERMINATION  
CHANNEL A



DDR TERMINATION  
CHANNEL B





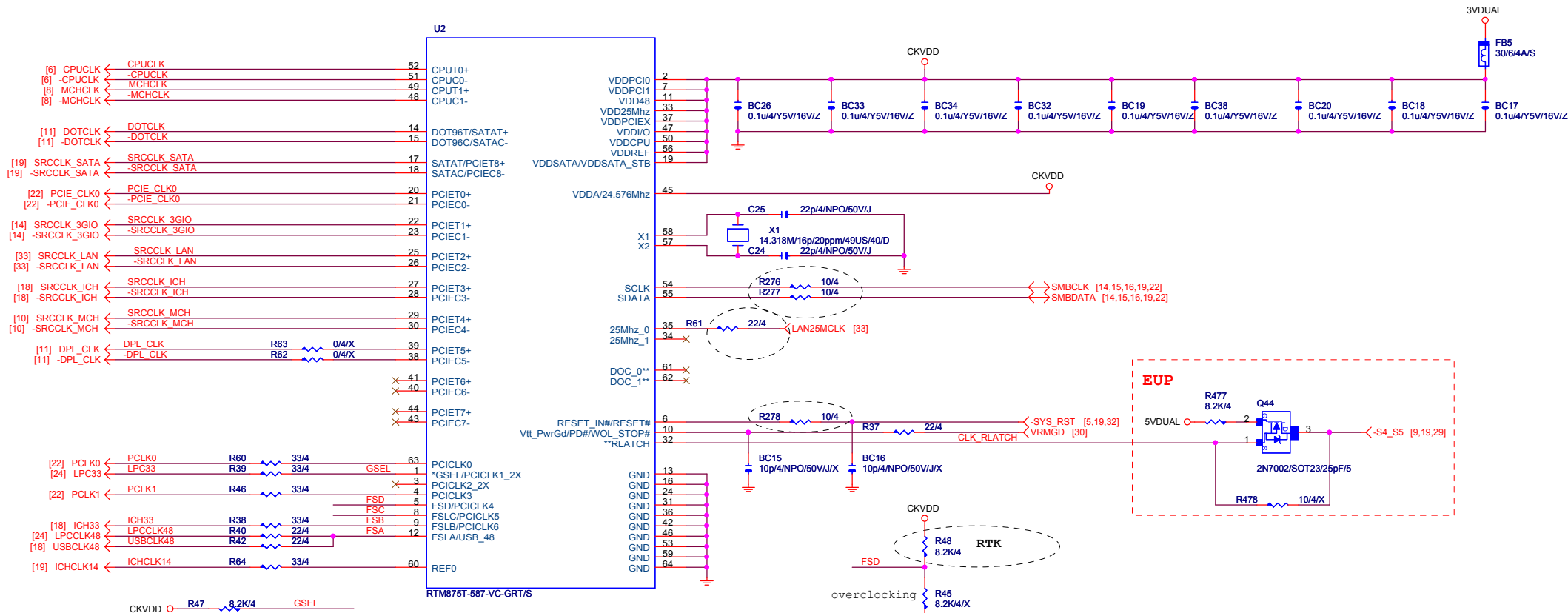




**EUP**

Title				ICH7-PWR & GND			
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B		GA-G41MT-ES2L				1.1	
Date: Tuesday, March 02, 2010				Sheet 20 of 33			

## CLK GEN CK505



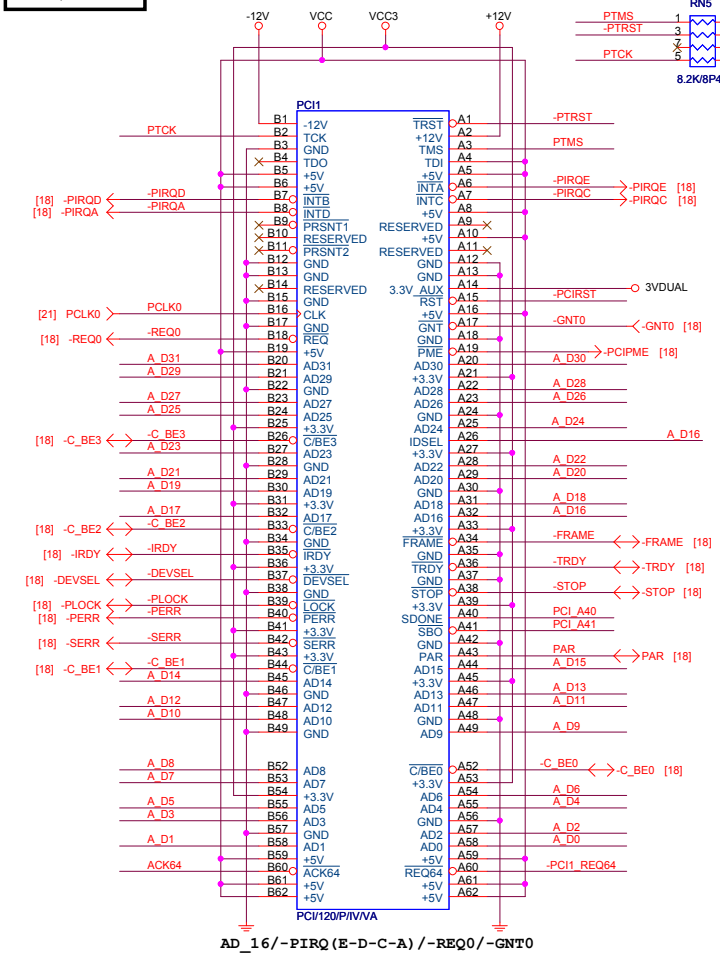
```
GSEL=1,96Mhz from 14/15,SATACLK from 17/18
GSEL=0,SATACLK from 14/15,PCIECLK from
17/18
```

[5] FSBSEL0  R36  1K/4/1 FSA

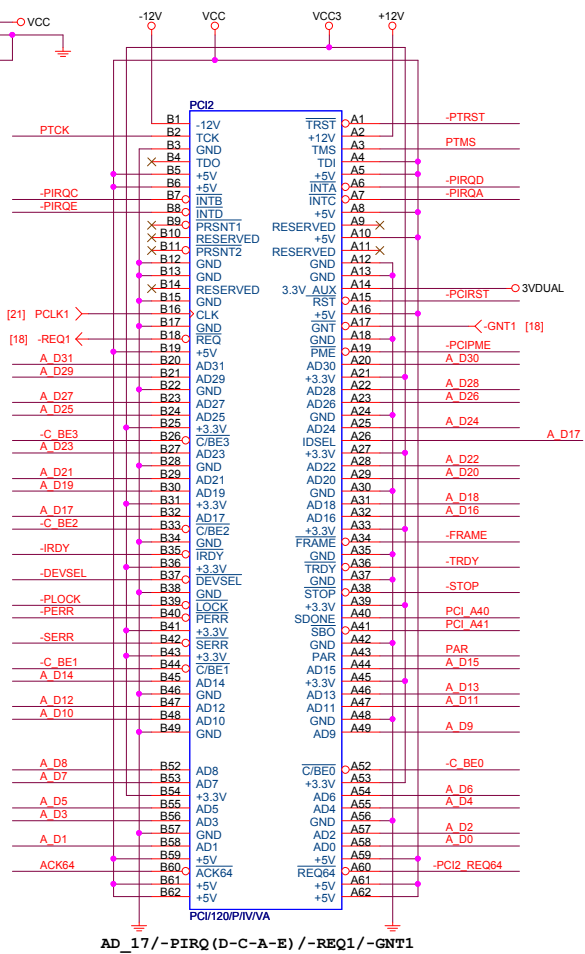
[5] FSBSEL1  R43  1K/4/1 FSB

[5] FSBSEL2  R44  1K/4/1 FSC

PCI1, 2 SLOT

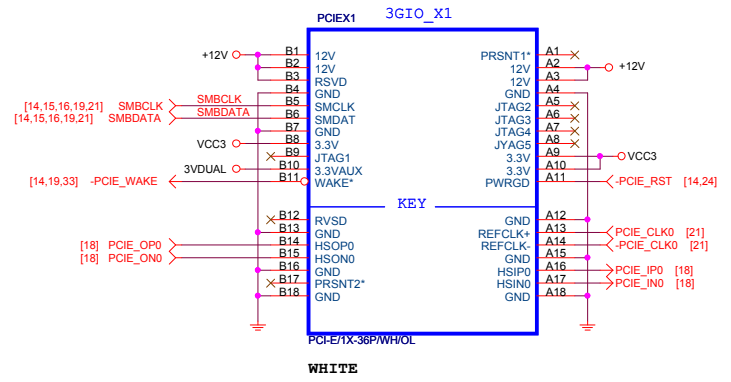


AD\_16/-PIRQ (E-D-C-A) /-REQ0/-GNT0

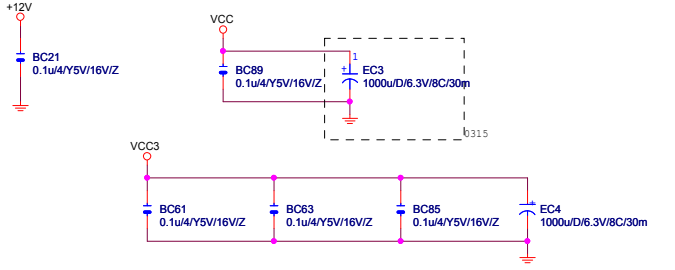
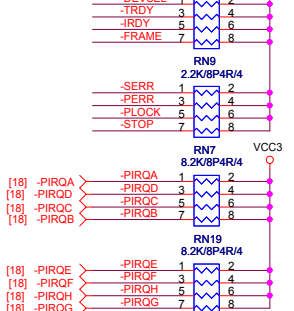
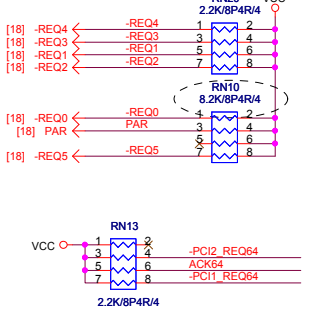
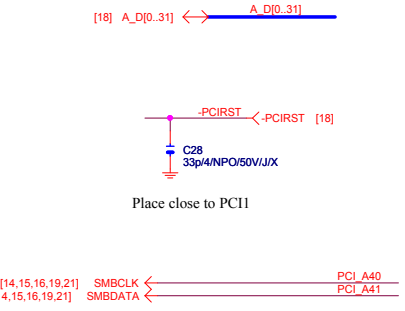


AD\_17/-PIRQ (D-C-A-E) /-REQ1/-GNT1

PCI-E\*1

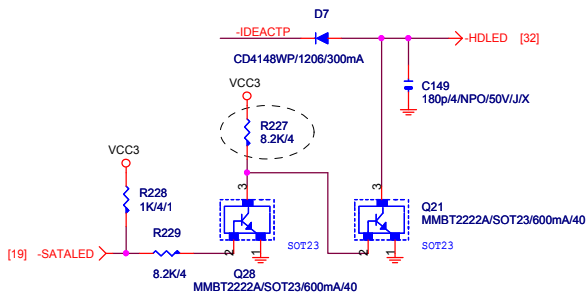


WHITE

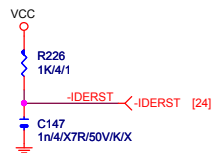


Gigabyte Technology			
PCI SLOT 1, 2/PCI-E*1			
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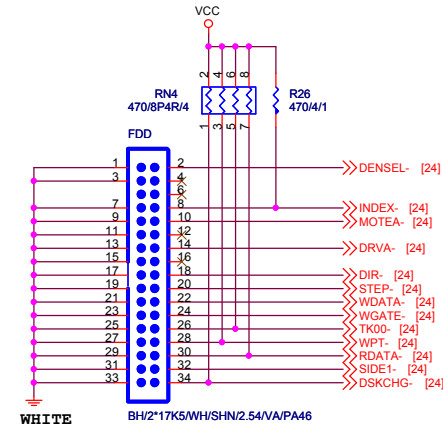
# IDE/SATA LED



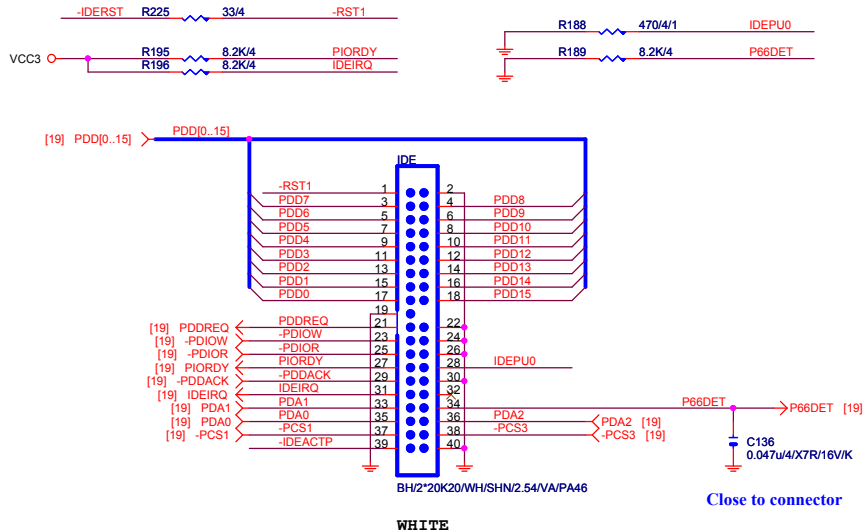
# IDE RESET



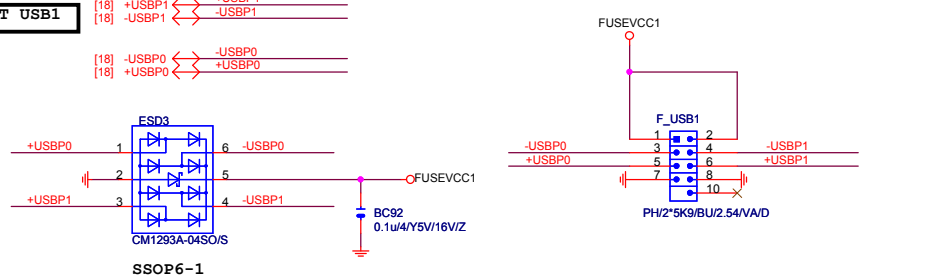
# FLOPPY



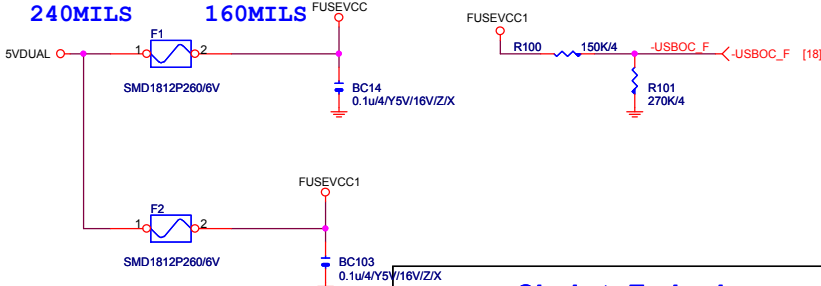
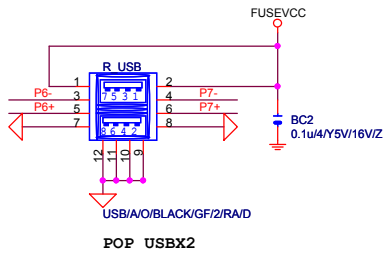
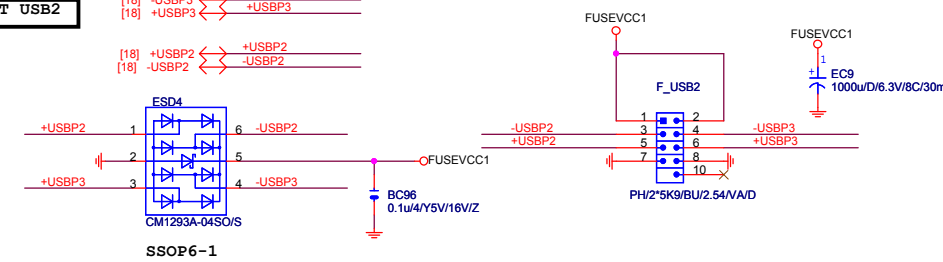
# IDE



# FRONT USB1



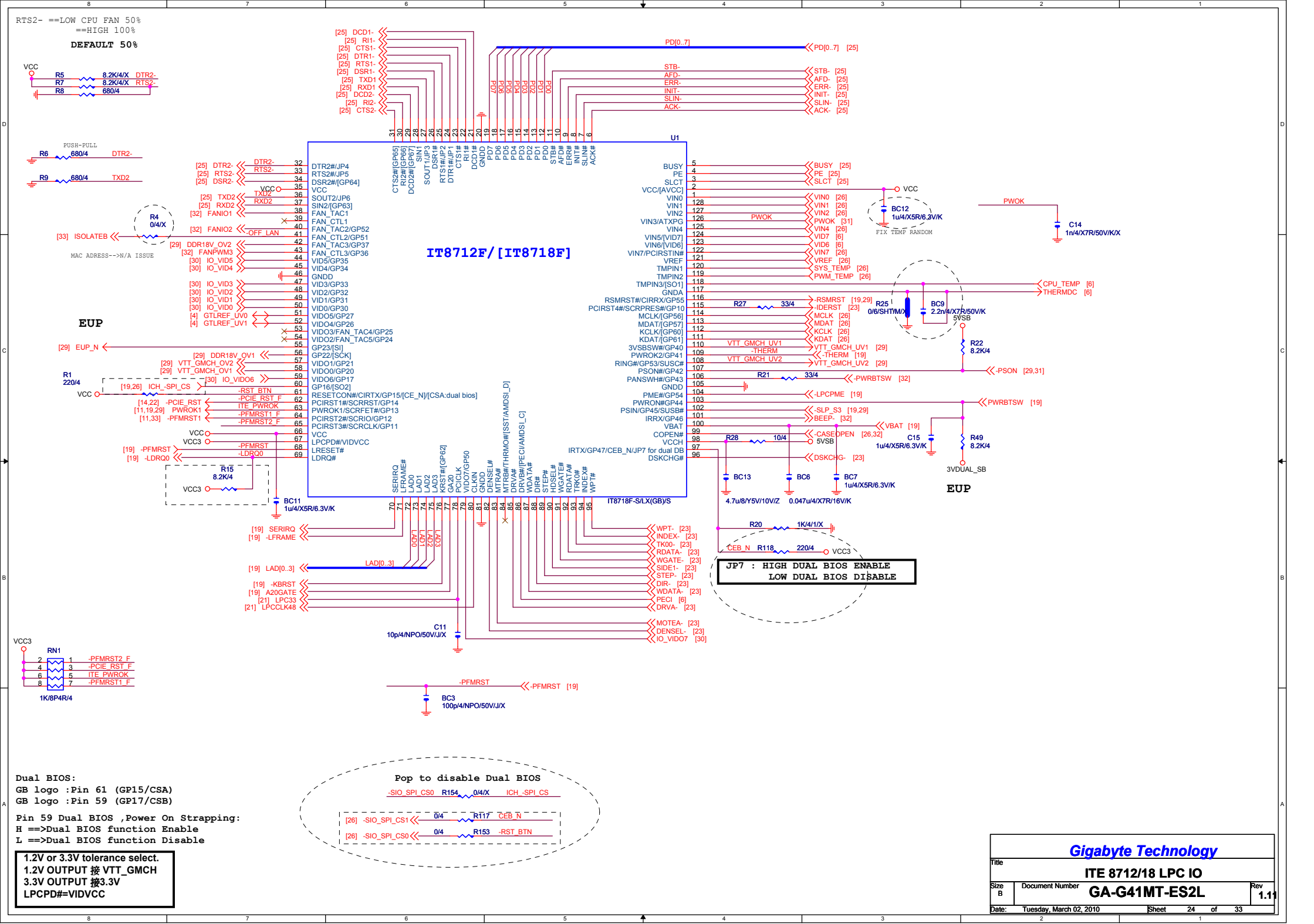
# FRONT USB2



Gigabyte Technology

Title			IDE,FDD,F_USB,R_USB
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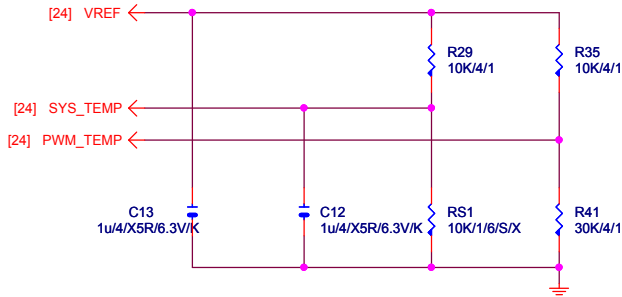




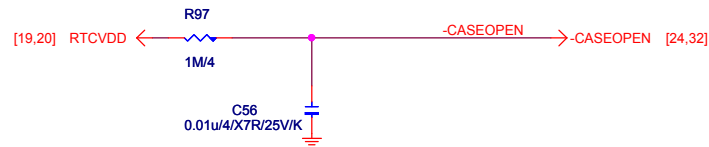




## TEMP H/W MONITOR

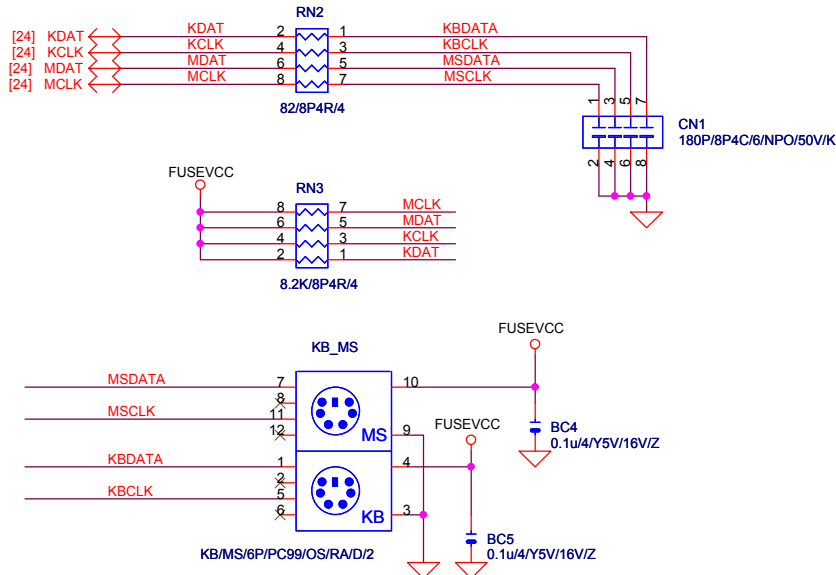


## CASE OPEN

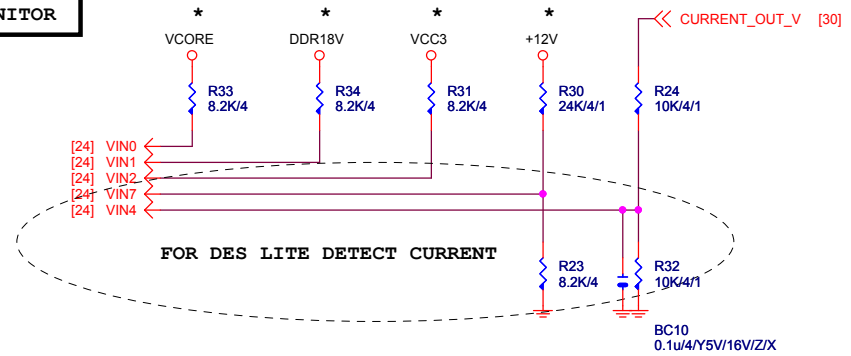


Case Open Circuits

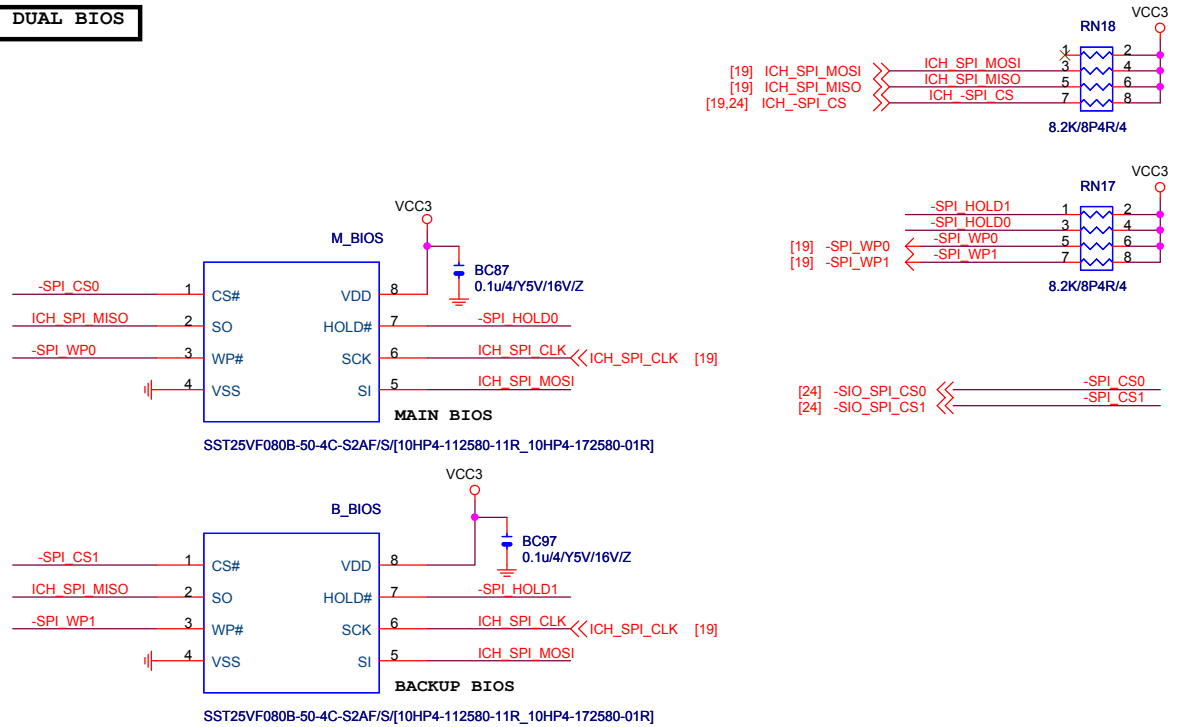
## KB/MS



## VOLTAGE-- H/W MONITOR

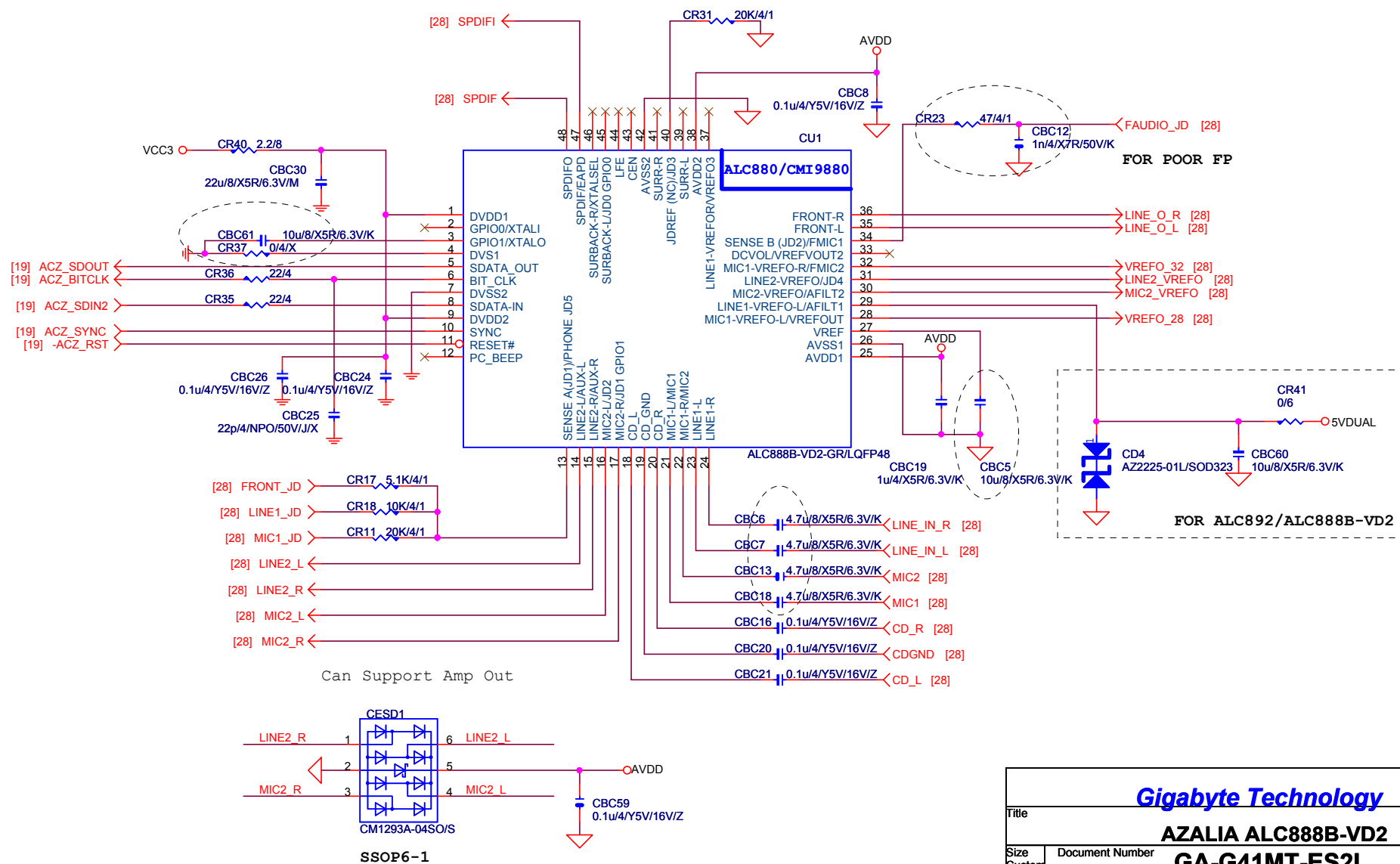


## DUAL BIOS



**Gigabyte Technology**

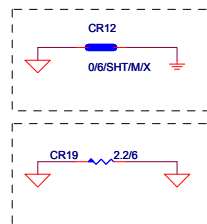
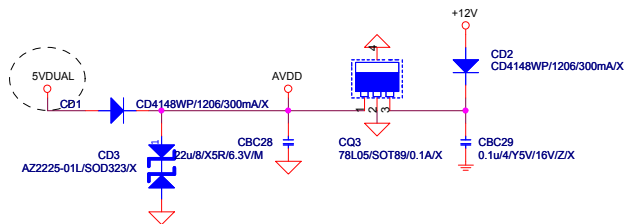
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Size	Custom	Document Number	GA-G41MT-ES2L	
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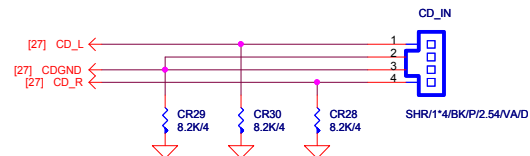
Gigabyte Technology

Title		
AZALIA ALC888B-VD2		
Size	Document Number	Rev
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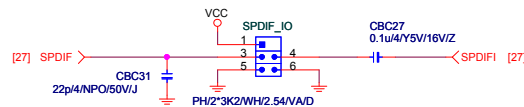
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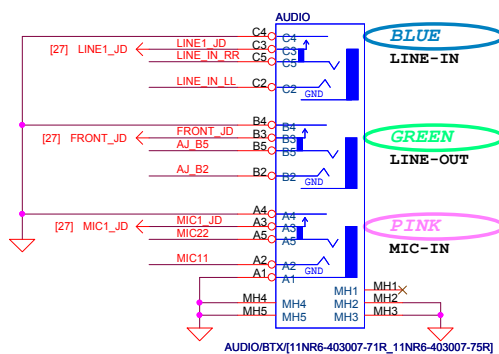
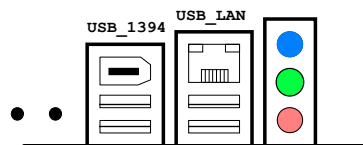
CD IN



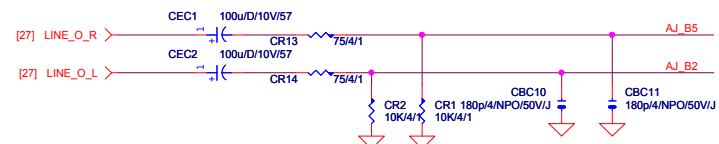
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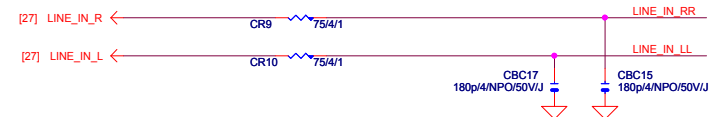
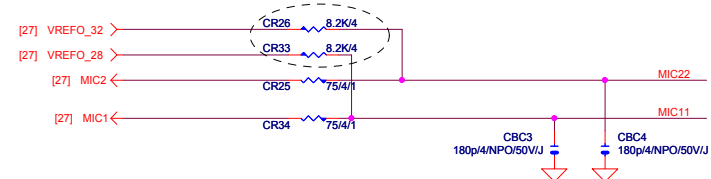
**AZALIA JACK**



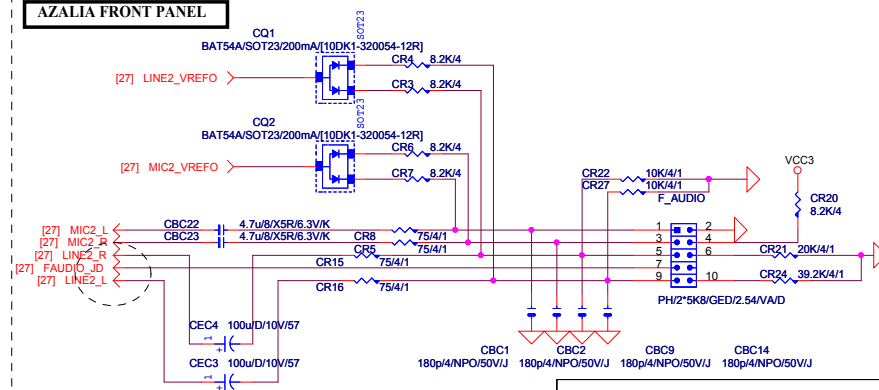
LINE-OUT

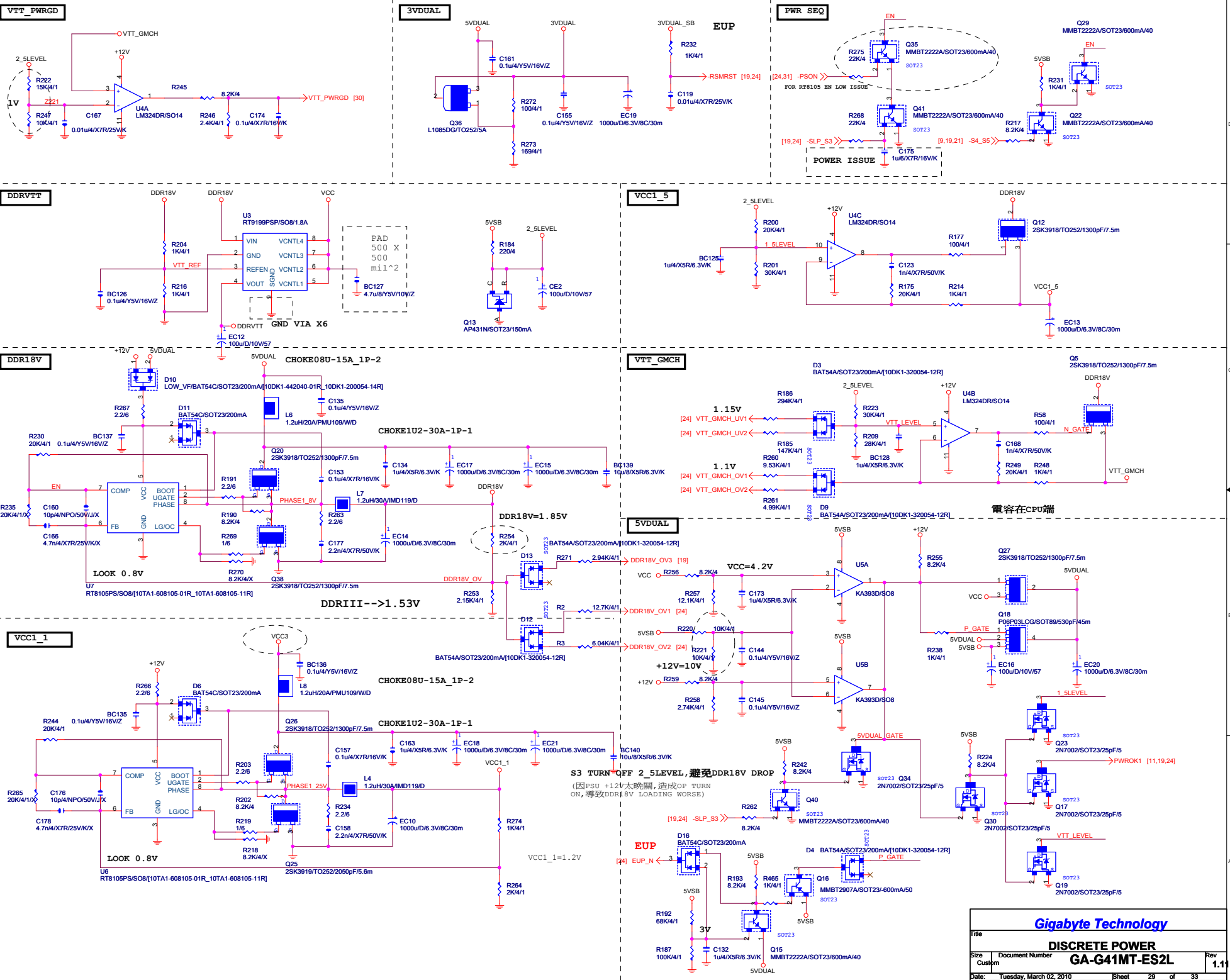


**LINE-IN**

**MIC-IN**

### AZALIA FRONT PANEL



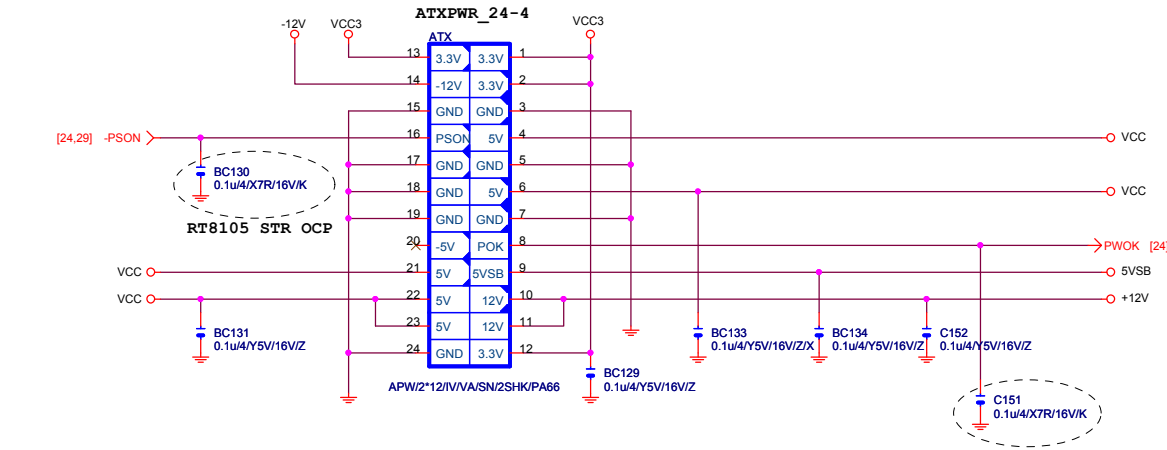


Gigabyte Technology

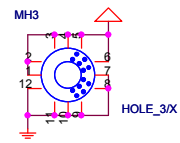
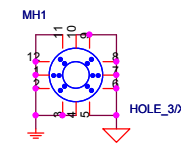
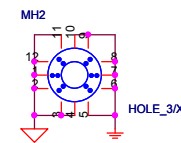
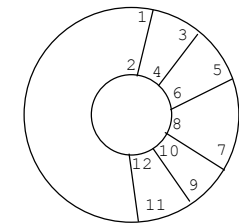
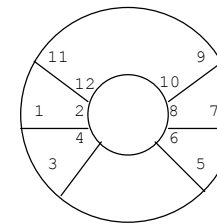
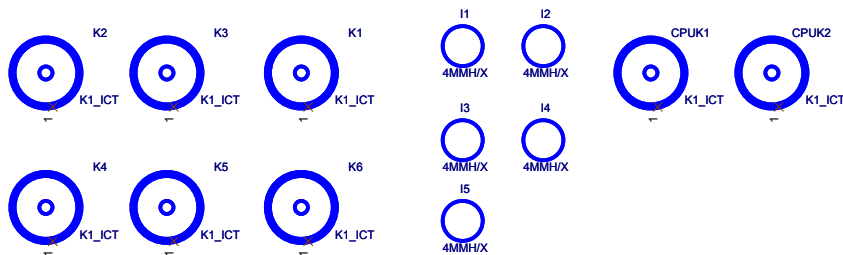
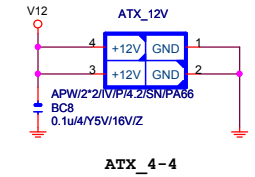
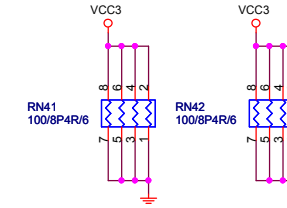
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Title				
DISCRETE POWER				
Size	Document Number			Rev
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# ATX POWER CONNECTOR

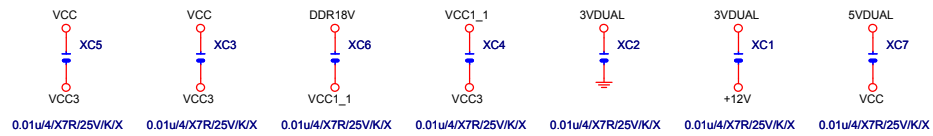
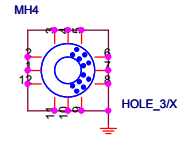
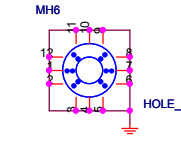
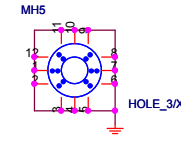


## FIX PWR AcBel (ATX-400C-A2ADB)



HOLE\_4-RH-1

HOLE\_4-RH-5MM-1



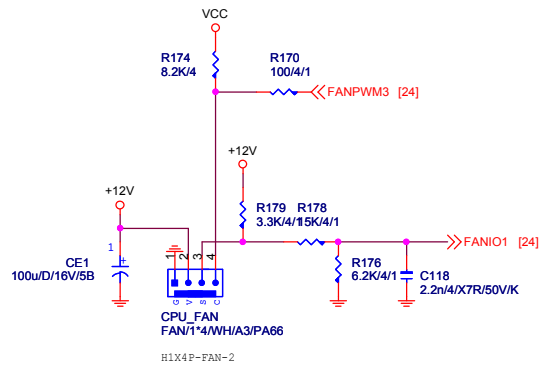
**Gigabyte Technology**

**ATX POWER CONNECTOR**

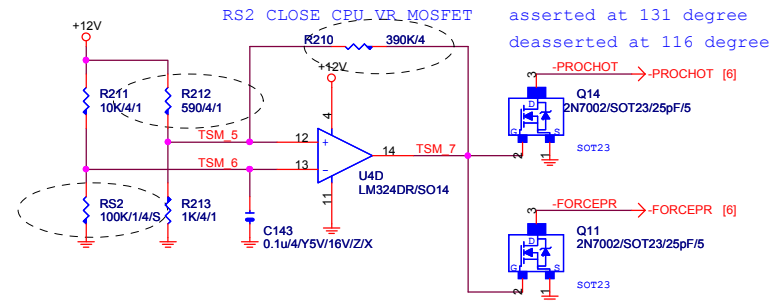
**GA-G41MT-ES2L**

Rev 1.11

# CPU SMART FAN SMART FAN

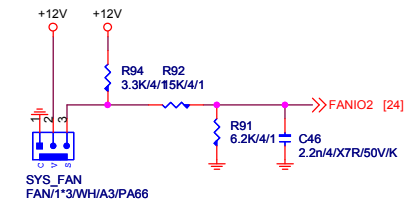
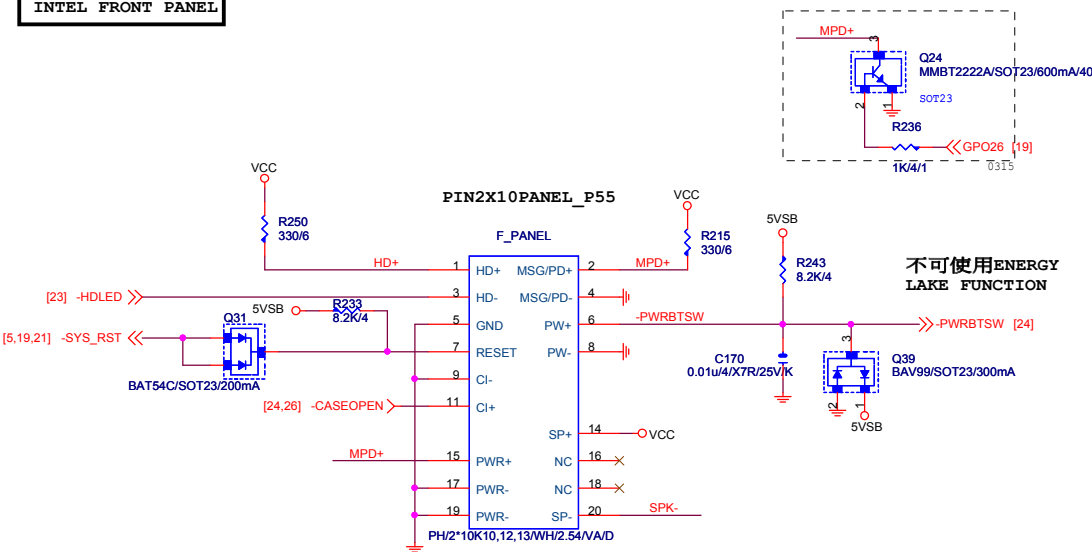


# PROCESSOR HOT

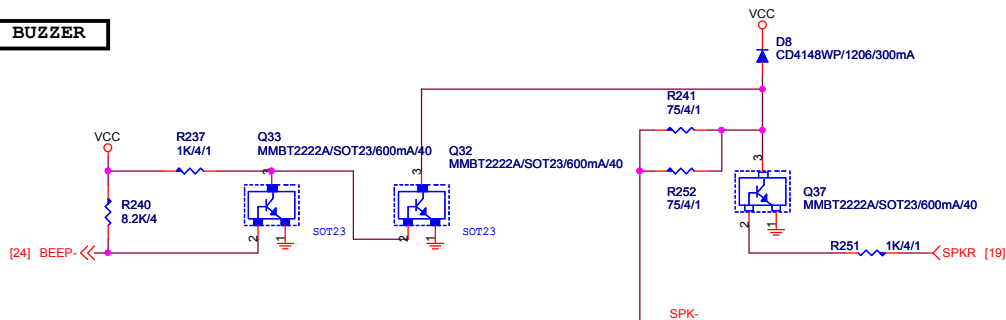


# INTEL FRONT PANEL

# SYS\_FAN



# BUZZER

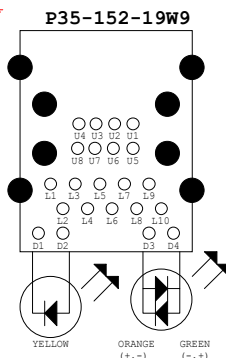
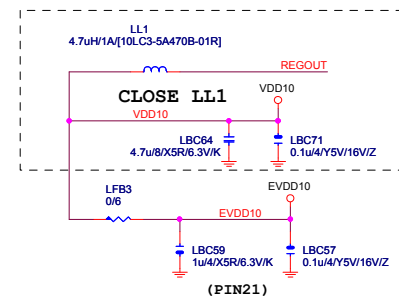
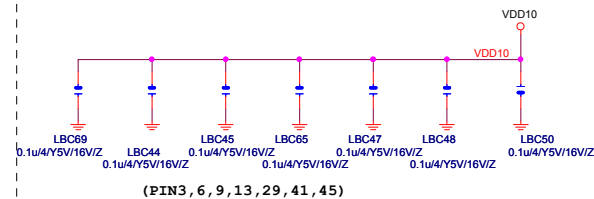
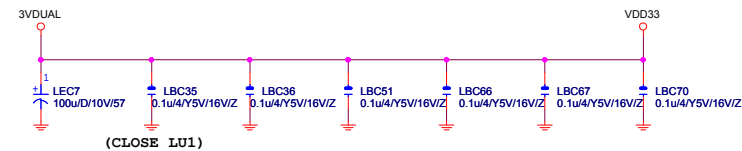
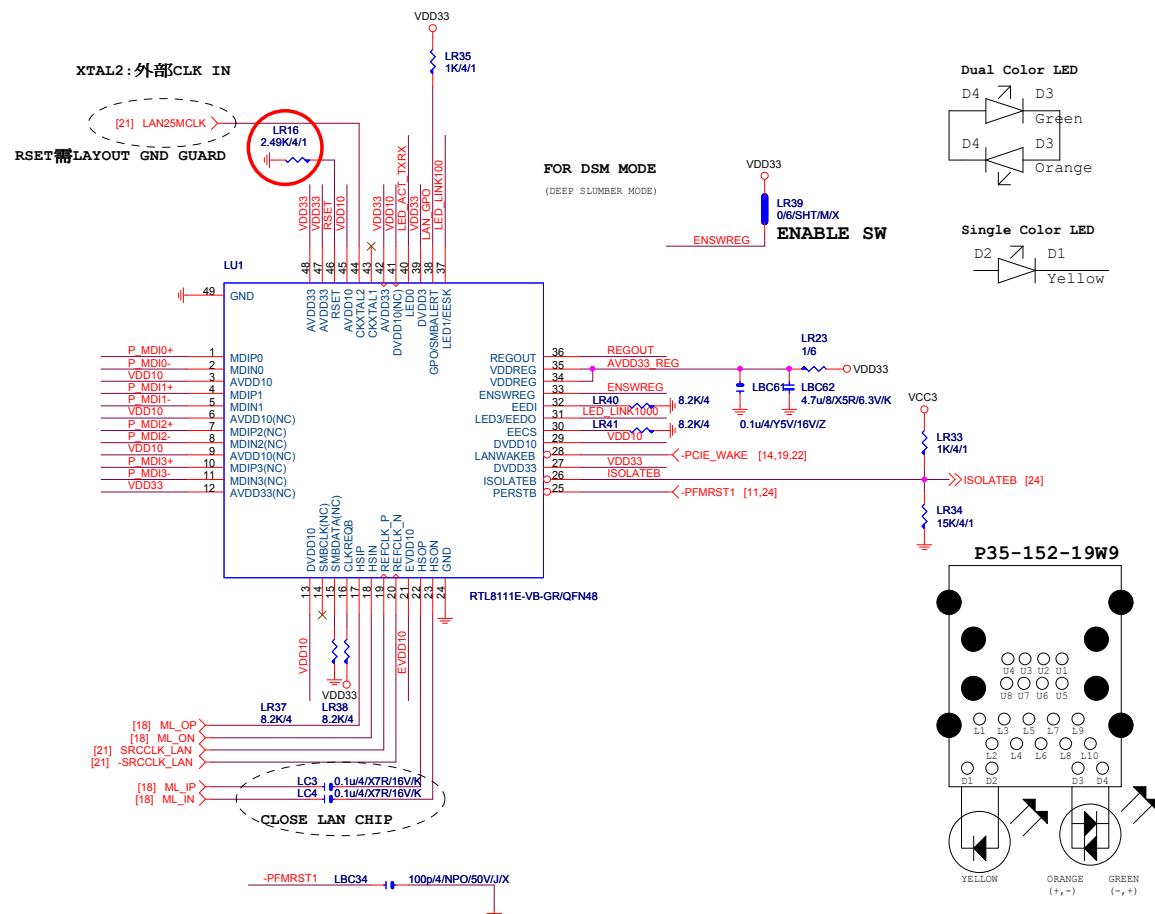


Gigabyte Technology

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PCIE-1G LAN



USB LAN CONNECTOR

USB\_LAN

