Rodrigue de Guerre

Profile

Passionate about innovation and eager to learn new technologies. Curiosity drives me to discover new challenges, which I seek to overcome through a thriving persistence.

Contact details

@ rodrigue.de.guerre@gmail.com
+33 6 48 73 45 80
LinkedIn
Github: Rodrigue2g
91 Route Neuve
1024 - Lausanne (CH)

Personal informations

Born on: **09.13.2002**
Citizenship: **French**
Languages:

- **French** - C2
- **English** - C2
- **Spanish** - B1
- **Dutch** - A2

Skills

- Swift, C++, C, Shell, Assembly
- JavaScript, Matlab, Python
- Git, Docker, k8s, MongoDB, Node.js
- Altium, Plecs, Comsol, Simulink
- Communication and team work

Other Activities

- Sports, Photography, Music
- DIY (Electrical projects)
- Baden Powell Belgian Lonescouts **2010-2020**

Education

Ecole Polytechnique Fédérale de Lausanne (EPFL). **2020-onwards**
Master in Electrical and Electronics Engineering (EEE) **2024-2026**
◇ *Energy Storage systems, Semiconductor devices, Wireless receivers, Analog VLSI design, Applied data analysis, Data visualisation, Audio engineering.*
◇ *Minor in cyber-security: Cryptography, Software Security, Information Security and Privacy, Data-intensive systems.*

Bachelor in Electrical and Electronics Engineering (EEE). **2020-2024**
◇ *Power Electonics, Electrical machines, Energy conversion, Signal Processing, Electromagnetism, Digital IC design, Control systems.*

Lycée Français Jean Monnet, Brussels (LFB). **2013–2020**
Scientific baccalaureate obtained with highest honour in June 2020.

Academic projects

Master Semester Project in Acoustics **Sept 2025 - Jan 2026**
Development of a stand-alone controller solution for Electroacoustic Resonators, in collaboration with LG Electronics.
Sup. by Dr. Hervé Lissek, EPFL Laboratory of Wave Engineering.

Wireless reciever Project **Sept 2025 - Jan 2026**
Acoustic Orthogonal Frequency Division Multiplexing (OFDM) Transmission System Project.

Bachelor Project on 6G wireless **Feb 2024 - July 2024**
Decoder with bit flipping post-processing for 6G wireless
Sup. by Prof. Andreas Burg, EPFL Telecomunication Circuits Laboratory.

Power Electonics Project **Feb 2024 - July 2024**
Designed and assembled a 45/24 V, 50 W DC-DC converter.

Additional education

Oxford Royale Academy (ORA) **July 2019**
Summer School at Imperial College London.
◇ *'Broadening Horizons' diploma obtained with distinction, after following courses in Mathematics, Economics and foundations in Business.*

Mount St Mary's College (UK) **March - July 2017**
◇ *Pupil during the end of Rudiments year in a British boarding school.*

Experience

Melødia **2023 - onwards**
AI music streaming platform - Co-founder
◇ *Developed an AI music streaming platform, available on* iOS.
◇ *Built an authentification service based on passkeys, removing passwords altogether.*

Tech Spark Academy, Villars (CH) **July 2024**
La Garenne International School - Teacher
◇ *Taught to teenagers about python, AI and Cyber-security.*

EPFL **2022–2023**
Mentor for first year students

Festival Balélec (EPFL student association) **2021–2022**
Chief of Transport and Circulation
◇ *Managed the logistics behind the transportation of artists and attendees for a 15'000 people music festival.*