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Sequence-Dropout Block for Reducing Overfitting Problem in Image Classification

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 **ABSTRACT** Over tting is a common problem for computer vision applications It is a problem that whentraining convolution neural networks and is caused by lack of training data or network complexity. The novel sequence-dropout (SD) method is proposed in this paper to alleviate the problem of over tting when training networks. The SD method works by dropping out units (channels of feature) from the network in a sequence, replacing the traditional operation of random omitting. Sophisticated aggregation strategies are used to obtain the global information of feature channels, and channel-wise weights are produced by gating mechanism. The SD method then selectively drops out the feature channels according to the channel-wise weights that represent the importance degree of each channel. The proposed SD block can be plugged into state-of-the-art backbone CNN models such as VGGNet and ResNet. The SD block is then evaluated on these models, demonstrating consistent performance gains over the baseline model on widely-used benchmark image classi cation datasets including MNIST, CIFAR-10, CIFAR-100, and ImageNet2012. Experimental results demonstrate that the superior performance of the SD block compared to other modern methods.



 **INDEX TERMS** Convolutional networks, image classi cation, over tting, sequence-dropout.



**I. INTRODUCTION**

Over tting is a typical problem for computer vision appli-cations [1] [5]. It is a problem that arti cial neural networks with non-linear hidden layers produce complex co-adaptations on the training data. It leads to the predictions on the testing set are worse than on the training sets. A variety of techniques [6] [11] have been developed to tackle over tting problem. However, over tting still remains a major challenge when training large neural networks or having very small amounts of data, and tackling this challenging well can ben-e t many computer vision tasks, such as classi cation [12], [13], denoising [14] and tracking [49], [50].

Over past decades, several researches focus on functional solutions [15], trying to extend convolutional neural networks (CNNs) for application on smaller datasets to reduce over-tting. For example, a typical technique is Dropout [16], [17], which improves the generalization ability of networks by stopping the feature detectors with random probability

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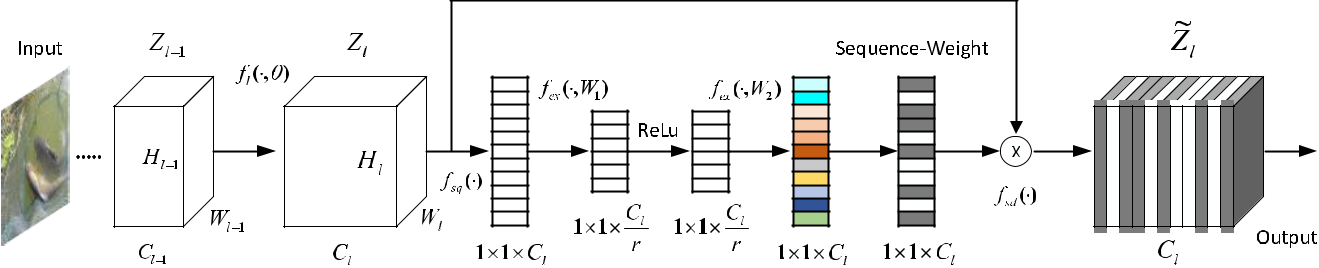


*p* at each training epoch. Batch normalization (BN) [18]is another technique, it has been applied to modern CNNs to enhance the generalization ability by normalizing the set of activations in a layer. In addition, many other strategies for reducing over tting concentrate on the network architec-ture. Therefore, a sequence of progressively complex archi-tectures are proposed, such as AlexNet [2], VGG-16 [3], ResNet [19], Inception-V3 [5], and DenseNet [13]. CNNs with more complex architectures can learn various feature representations of the input image by using different lters in a convolution layer, and the layer output (feature map) indicates the importance (measured by *matching degree*) of the learned feature to identify the input image. For instance, SENet [20] just exploits the characteristic of convolution to selectively enhance and suppress features according to channel-wise weights acquired by *Global Average Pooling* (GAP) [21], embedded in CNNs to improve the generaliza-tion performance.

Through investigating the characteristics of Dropout [17] and SENet [20], we construct a novel structure unit named as ‘‘*sequence-dropout*’’ (SD) block. It is introduced into CNNs

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**FIGURE 1.** The SD building block is placed between the convolution layers. **Zl** is the feature map of layer **l** with spatial dimensions **Wl Hl** and **Cl** channels. **r** is a positive integer. The section of **1 1** **Cl** with different colors symbolizes the different importance degree of feature map channels, closely following is **sequence-weight**. Black denotes the reassignment to 1 and white symbolizes reassignment to 0. In the output **Z**e**l** , black features show that the feature map channels are preserved and white features show that the feature map channels are temporarily removed.

to reduce the problem of over tting on image classi cation task. The proposed SD block establishes a novel mechanism of feature recalibration which highlights the most critical aspect of the proposed method, to generate the values repre-senting the *matching degree* of feature maps, selectively drop-ping the feature map channels or neurons with low *matching* *degree* during training to improve the model generalizationperformance. The method inherits the advantages of Dropout, simplifying the model with residual architecture [13], [19], and improves the robustness of the model in cooperation with other regularization methods (*e.g.* L2, BN).

The architecture of the proposed SD block is illustrated in Fig. 1. For any given transformation *fl* ( ; ): *Zl* 1 !

*Zl* , *Zl* 1 2 R*Wl* 1 *Hl* 1 *Cl* 1 , *Zl* 2 R*Wl* *Hl* *Cl* , (e.g. a

convolution or a set of convolutions), the constructed SD block includes three calculation steps (see Section [III](#page3) for further details): *squeeze fsq*( ), *excitation fex* ( ) and *sequence-dropping fsd* ( ), which are elaborated as follows. Firstly,using *fsq*( ) calculation (*squeeze* operation), the feature map *Zl* of *l* layer is polymerized into a 1 1 *Cl* channeldescriptor across spatial dimensions *Wl* *Hl* . The obtained descriptor contains the information of global distribution for channel-wise feature responses, which makes the infor-mation from the global receptive domain of the network to be utilized by its lower layers. Then, the descriptor is passed through *excitation* operation including *fex* ( ; *W*1), *fex* (; *W*2) and *Sequence-Weight*, which utilizes a gatingmechanism to capture channel-wise dependencies, gener-ating the weights of importance for each feature channel. Finally, through *fsd* ( ) calculation, the weights are reassigned to get the *matching degree* and the feature map channels are dropped out by channel-wise multiplication of the *matching* *degree*.

The proposed SD block abandons randomness used in Dropout and preserves important features to the greatest extent, signi cantly reducing over tting on image classi - cation (the experiment results are detailed in Section [V)](#page6). Under the same training condition, comparing with L2, BN and Dropout, our method achieves the lower test classi cation error 0.64%. Further more, combining our method with L2 and BN further reduces the error to 0.56%.

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In the following sections, related work is rst introduced, focusing on the evolution of solutions to reduce over tting in Section [II](#page2) followed by a detailed description of the proposed SD block in Section [III.](#page3) The architecture and computational complexity of network embedded the SD block is analyzed in Section [IV](#page5) and our experiments are reported in Section [V.](#page6) Finally, the Section [VI](#page10) concludes this paper.

**II. RELATED WORK**

Over tting is a common problem in neural networks. To solve this issue, a wide range of works have been carried out in recent years. Several classical approaches are approximately summarized as follows.

**A. DATA AUGMENTATION**

Increasing the amount of data is an effective way to prevent over tting for networks. Because more training data means that a deeper network can be used for training to obtain superior results [22]. Thus, a lot of datasets are built such as ImageNet [23], COCO [24], and Objects365 [25]. However, it is laborious to substantially increase the data, and it is unable to estimate how much data is enough. Data augmentation

1. can augment data with certain rules on the existing data. It is based on basic image manipulation including Color Jittering [2], Random Crop [2], Scale Jittering [19], Flip [2], Random Erasing [26], Noise injection [27], Kernel lters [28], Mixing images [29], etc. In addition, neural networks are being utilized to further the enhancement of data augmen-tation techniques. It is divided into Feature space augmenta-tion, Adversarial training, GAN-based, Neural Style Transfer, Meta learning Data Augmentations [15].

**B. MODEL MODIFICATION AND SELECTION**

Selecting an appropriate complexity network architecture can also reduce the over tting problem. Weight sharing [30] is an effective way to reduce the weights that need to be learned. In addition, choosing the right activation function also improves the generalization ability of the model. For example, Recti ed linear units (ReLUs) mentioned in [31] helps the model avoid the vanishing gradient problem and makes it has a better generalization. Maxout units [32] and Stochastic pooling [33] (a noisy version of max-pooling)

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are designed for regularization. Batch normalization [18] and Ghost batch normalization [34] are other paradigms of regularization technique, which x the activation input dis-tribution to avoid the problem of ‘‘Internal Covariate Shift’’ in a layer.

In addition to modifying the model architecture, the gener-alization can also be improved by the selection of learning mode, loss function and optimization method. Multi-task learning, meta-learning and transfer learning make the model share representation information between related tasks, and achieve better generalization performance. The cost function can also have a regularizing effect. For instance, numerous research works [35], [36] introduce regularizer [8], [10], [11] into the cost function to achieve regularization. Essentially, over tting is caused by over training.

**C. MODEL COMBINATION**

Model combination is also a way to reduce over tting prob-lem. It assembles several models, weakens the in uence of outliers and the characteristics of each model, and main-tains commonality between models. It trains several models and uses the average output of each model as a result to improve the model performance. For example, the approach of Bagging (bootstrap aggregating) [37] trains several dif-ferent models separately, and then all the models vote on the output for test examples. Boosting [38] is an another example of model combination. It is achieved by training a series of simple neural networks to acquire weights and get the nal results by averaging these weights. Different from above two techniques, The technique of Stacking [39] outputs a nal prediction result according to the prediction results from different weak models (heterogeneous weak learner).

Although model combination can alleviate the problem of over tting to some extent, it is usually hard to train the model with large amounts of parameters. Dropout [16] is proposed to solve this problem. It provides a method of approximately combining exponentially various neural network architec-tures ef ciently to prevent over tting. It randomly shields a certain proportion of neurons, makes the output uncertain about which features it is combining, and may mask the features that play an important role in the output results. Numerous research works [40] [42] have carried out on basis of Dropout.

**III. APPROACH**

The constructed SD block can be applied to existing state-of-the-art deep architecture to reduce model over tting. The calculation of SD block is divided into three parts: chan-nel compression calculation, channel weight calculation, and sorting and dropping calculation. Similar to dropout involv-ing modi cations to the model architecture, the SD block can be placed in two locations in the model architecture: between the convolution layers and behind the FC layer. There is a small distinction between the calculation methods of the two locations, being that the features must be compressed on the

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channel dimension in the FC layer. A diagram of the SD building block is shown in Fig. [1.](#page2) The calculation of the SD block embedded between convolution layers are discussed as follows.

The SD block is added after the convolution layer of ‘ in any given network model. The convolutional transformation *fl* of‘layer in the model architecture (‘[1; *L*]): *fl* V

*Zl* 1! *Zl* , *Zl* 12R*Wl* 1 *Hl* 1 *Cl* 1, *Zl* 2R*WI Hl Cl* . Right

here we set the convolutional lter involved in convolutional calculation to be *Kl* D [*k*1; *k*2; : : : ; *kC* ]*l* , in which *kC* is the convolutional lter of *c* channel. Through the convolutional transformation *fl* , we will get *Zl* D [*z*1; *z*2; : : : ; *zC* ]*l* , *zC* is

h i

calculated by convolution of *kC* D *kC*1; *kC*2; : : : ; *kCC*0 and

* i

*Zl* 1D *z*1; *z*2; : : : ; *zC*0 (the bias term is omitted in the

* 1

formula):

*C*0

X

|  |  |  |  |
| --- | --- | --- | --- |
| *zC* D *kC Zl* 1D | *kcn Zln* | 1 | (1) |
|  | *n*D1 |  |  |

As illustrated in the above formula, the value of the current layer is generated through the sum of all channels in the upper layer, and the interdependencies between channels are implicitly embedded in it. The purpose is to obtain the values representing the importance of each channel, which serve as a basis for judgment of which low-importance feature map channels (or neurons) can be dropped in order to obtain network generalization. To achieve this, the interdependen-cies are explicitly modeled between channels before entering the next convolution and the output value is considered as the importance degree of each feature channel after feature selection. According to the output values, the weights of the non-important channels are set to the value of 0, which means that the channels are temporarily removed from the network. Important channels that must remain are given the value of 1, then an elimination matrix composed of 0 and 1 is achieved. Finally, the SD of the original feature on the channel dimension is achieved by using channel-wise mul-tiplication between the elimination matrix and the previous feature map.

To summarize, the method is implemented in three steps: *squeeze* (see Section [III-](#page3)A), excitation (see Section [III-](#page4)B), and *sequence-dropping* (see Section [III-](#page4)C), as detailed below.

1. **SQUEEZE: EXTRACTING GLOBAL INFORMATION OF EACH CHANNEL**

The characteristics of CNNs show that each unit in output *Z* only perceives the local receptive elds, and cannot makeuse of relevant information outside the locality. In order to calculate the weights of each channel, the channel global information must rst be obtained, which can be achieved by combining the information of local receptive eld at higher layers. For this purpose, the output *Z* is minimized through the spatial dimension direction and the two-dimensional fea-ture map of each channel to a real number by using global average pooling (or other more sophisticated aggregation

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strategies). Formally, a statistic *S* 2 R*c* is generater by shrinking *Z* through spatial dimensions *W H* , where the *c*-th element of *S* is calculated by:

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  |  | 1 |  |  | *W* | | *H* | |  |  |
|  |  |  | XX | | | | |  |  |
| *sc* D *fsq* .*zc*/D |  |  |  |  |  |  |  | *zc*(*i*; *j*) | (2) |  |
| *W* |  | *H* | |  | 1 *j* |  |  |
|  |  |  | *i* | D | D | 1 |  |  |
|  |  |  |  |  |  |  |  |  |

The dimension of output *S* is consistent with the number of feature channels of input. To some extent, the real num-ber *S* has a certain global receptive eld, which represents the global distribution of response on feature channels, and enables the layer close to the input to obtain the global recep-tive eld. This process is useful in many feature engineering works [3].

1. **EXCITATION: CAPTURE CHANNEL-WISE DEPENDENCIES**

The global information of each channel is generated by the *squeeze* operation, which can be used to explicitly modelchannel-wise dependencies. To achieve this, a gating mecha-nism similar to RNN [43] is utilized to generate the weights for each feature map by parameter learning. The formula is as follows:

channels need to be dropped out, while assigning the weights of important feature channels to 1 indicates that no dropout is required. The pseudocode for the calculation process is as Algorithm [1.](#page4)

**Algorithm 1** Implementation of Sequence-Weight in SDBlock

**Input**: The set of channel sequence weight,*E*; thefeatures (or neurons) dropout ratio, *p*

**Output**: The nal output is reassigned *E*. The order of the sequence remains invariant, while the value of its elements is set to 0 or 1 according to the lters by using *for* loop

* Sort the values of each element in sequence *E*,

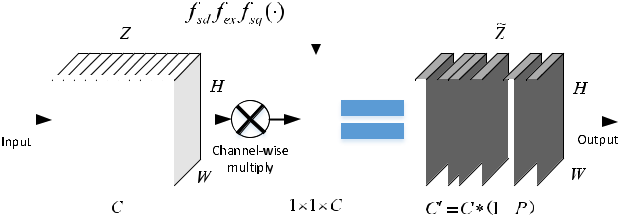
*Esorted* D *E*:*sort*();

* Obtain the index *c* of each element of the *Esorted* by

using *argsort*(), *c* D *Esorted* :*argsort*();

* **for** *i*D1I*i c*:*size*()I **do**
* **if** *i int*(*len*(*E*)*p*) **then**
* set *E*(*c*) to 0;
* **else**
* set *E*(*c*) to 1;

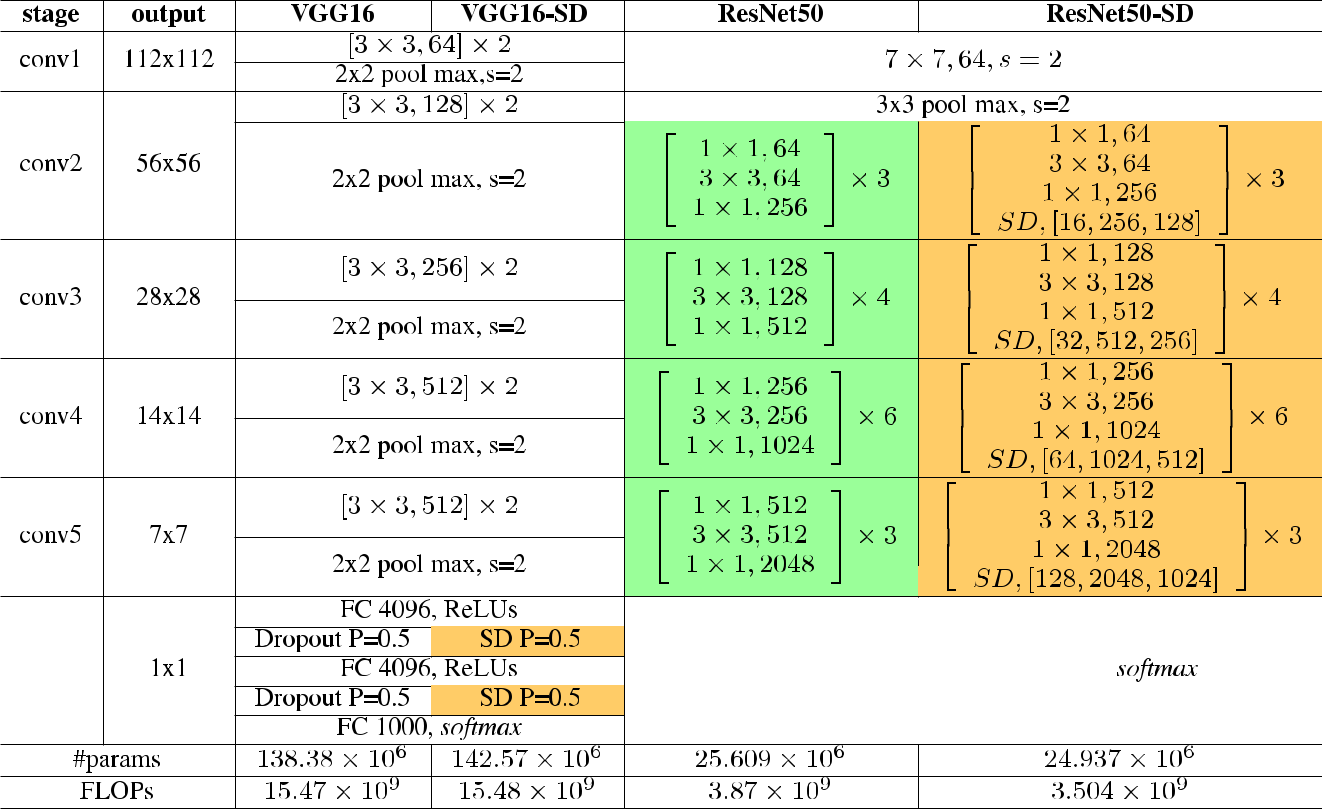
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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|  | *E* D *fex* (*S*; *W* )D *g*(*S*; *W* )D *W*2.*W*1*S*/ | | | | | | (3) | **8** | | **Final** ; | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  | **9** | | **Return** *E*; | | | | | | | | | | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| where is ReLUs [31] function, and the parameters of *W*1 | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| and *W*2 are learned to model the channel-wise dependencies, | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| *W*12R | *C* | C,*W*2 2RC | *C* | . To reduce the parameter for com- | | | |  |  | After the *sequence-weight* calculation, the weight | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |  |
| r | r |  |  |  |
| putation complexity, and make the model block more non- | | | | | | | | sequence *E* of the feature channels consists of 0 and 1. The | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |  |
| linear to better t the complex correlation between channels, | | | | | | | | nal output of the SD block is then obtained by using reas- | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |  |
| two FC layers are adopted to form a bottleneck architecture. | | | | | | | | signed *E* to recalibrate the feature of the original convolution | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |  |
| This process is realized in three steps. Firstly, the feature | | | | | | | | layer: | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| dimension is reduced to 1=*r* through the rst FC layer with | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | *zc* D *fsd* .*zc*; *Ec*/D *Ec zc* | | | | | | | | | | | (4) | | | | | | |  |
| parameter *W*1. It is then activated by the ReLUs function, and | | | | | | | | Here *fsdW* | | | | | | | |  | *H* | |  |  |  |  |  |
| nally, the second FC layer with parameter *W* | | | | | 2 | is adopted to | |  |  |  |  | e | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  | ( | | | | |  | ) donates channel-wise multiply, feature mapping | | | | | | | | | | | | | | | | | | | | | | | | |  |
| return the dimension back to the original number. The nal | | | | | | | | *zc* | | 2 R | | | | | | | | | , the block output *z* | | | | | | | | | | | | D [*z*1; *z*2; ; *zc*]. Fig. [2](#page4) | | | | | | | | | |  |
| output *E* is the weight of importance for each feature channel | | | | | | | | depicts the output of channel-wise multiply. | | | | | | | | | | | | | | | | | | | | | | | | | |  |  |  |  |  |  |  |  |
| whose dimension is consistent with the number of feature | | | | | | | |  |  | The spatial dimension of | | | | | | | | | | | | | | | | | the nal output in SD block | | | | | | | | | | | | | |  |
| channels. | | | | |  |  |  |  |  |  | e | | | e e | | |  |  | e | | | | |  |
|  |  |  | remains invariant *W H* . However, for spatial channel, | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |  |
|  |  |  |  |  |  |  |  | the proposed mechanism extracts the global information of | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |  |
| **C. SEQUENCE-DROPPING: SEQUENCE WEIGHT** | | | | | | |  | spatial channel dimension to judge the importance degree | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |  |
| **AND DROPPING OUT FEATURES** | | | | |  |  |  | of each channel, which removes the *C p* relatively non- | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |  |
| The output *E* of Eq. (3) is the weight of feature channels, | | | | | | | | important channels, and retains the *C* (1 | | | | | | | | | | | | | | | | | | | | | | | | | |  | *p*) relatively | | | | | |  |
| which re ects the importance degree of each channel. Judg- | | | | | | | | important channels. | | | | | | | | | | | | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| ing by this value, in the last step of SD block, the fea- | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| ture map channels are dropped by setting a dropout ratio | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| *p*. This thins the network to carry out the generalization. | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| The dropout ratio is most commonly set to 0.5 [16], [17], | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| meaning that half the feature channels (neurons) are dropped. | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| In this stage, the weights of each feature channel must be | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| sequenced (termed as ‘‘sequence-weight’’). However, the | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| *sequence-weight* does not actually change the location of | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| each feature channel, it only obtains the index value of the | | | | | | | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| sorted channels. Following this, the weights of each feature | | | | | | | | **FIGURE 2.** The final output of SD block. The black and white in **1 1 C** | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |  |
| channel are recalibrated. Reassigning the weights of non- | | | | | | | | represent the value 1 and 0 respectively. Through **Channel-wise multiply** | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |  |
| with input, the output channels corresponding to 0 are temporarily | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |  |
| important feature channels to 0 indicates that the feature | | | | | | | |  |
| removed, and are a hidden in **z**e. | | | | | | | | | | | | | | | | | | |  |  |  |  |  |  |  | 62833 | | | | | | |  |
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**TABLE 1.** Model architectures of VGG16 and ResNet50 with SD. Here VGG16 with SD is referred to as VGG16-SD, and ResNet50 with SD is ResNet50-SD.

Both VGG16-SD and ResNet50-SD embody two different applications of SD in CNNs (as mentioned in Section [III)](#page3).



**IV. MODEL AND COMPUTATIONAL COMPLEXITY**

Previous studies have shown that dropout unit is generally placed behind the FC layer of model architecture to randomly drop out the neurons (such as VGGNet [3]). In contrast, the proposed SD can not only be placed behind FC layer, but between blocks of convolution layer to drop out the convo-lution feature channels. Table [1](#page5) respectively describes the VGG16 and ResNet50 module architecture of dropout and sequence-dropout with 224 224 pixel images as input.

In this section, the different model architecture embedding the SD block are rst introduced in [IV-A](#page5) followed by the interpretation of the model complexity in [IV-B,](#page6) and then the computational cost of the networks are analyzed in [IV-C.](#page6) Finally, this section ends in the discussion.

**A. MODEL ARCHITECTURE**

As illustrated in Table [1,](#page5) two dropout units exist in the VGG16 network, which are placed behind the 6th and 7th FC layer, respectively. Additionally, the number of neurons changes from 4096 to 2048 when using the dropout unit (the probability *p* of random elimination is set at 0.5). The third column in Table [1](#page5) shows that using the SD scheme in VGG16 network replaces the dropout units with SD blocks, and also shows that the input of SD block is FC layer neurons (1 1 4096) in VGG16-SD architecture, which means it is not required to aggregate the feature maps across spatial

dimension in SD block. With elimination ratio 0.5, the neuron number changes from 4096 to 2048 by SD calculation, which is the same as dropout.

The ResNet50 network deepens the network through the residual module, which to a certain extent solves the problem that the deep network cannot be trained due to gradient disap-pearance or gradient explosion. The ResNet50 architecture is implemented by stacking residual modules together, and the input dimension (number of channels) of its stack block is expanded four times by 1 1 convolution kernel for output. Detailed description of residual module is provided in the row conv2 of column ResNet50 in Table [1](#page5) (green background) as follows. Firstly, a 1 1 convolution lter kernel is utilized, followed by a 3 3 (*c* D 64) convolution operation. The dimension for output is then expanded with 1 1(*c* D 256) convolution. The concept of applying SD block to ResNet50 network is to imbed SD block in the original residual module to form a new stack block. In the column of ResNet50-SD, the numbers in the brackets after SD of each residual module respectively refer to the rst FC layer output dimension, the second FC layer output dimension and the nal SD block output dimension. Compared with the original ResNet50, the output dimension of stack block in ResNet50-SD network is reduced to half (elimination ratio is set at 0.5). The orange background area of Table [1](#page5) shows that the conv2 output dimension of ResNet50-SD is reduced from 256 to 128, the

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**C. COMPUTATIONAL COMPLEXITY**

**A 224 224 pixel image is used as the input of network, and the computational cost of the network is evaluated by using oating-point operations per second (FLOPs). The VGG16 network requires 15.470 GFLOPs for a single forward propagation calculation. By comparison, the VGG16-SD net-work increases two FC layer oating-point operations of SD block, slightly increasing by 0.027% (0.00419 GFLOPs).**

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**where *r* denotes the reduction ratio, *L* refers to the num-ber of stages (*e.g.* conv2, , conv5 in Table 1), *Nl* is the number of repetitions of SD block at the *l* stage, and *Cl* denotes the dimension of the output channels for *l* stage. The calculation in Eq.** [**(5)**](#page6) **shows that the number of parameters in the VGG16-SD network rises 3.028% (4.19M) compared to the VGG16 network. In the ResNet50-SD network, the parameters of the network increase with the addition of SD block, while the number of parameters introduced by the connection between stack blocks decreases. Thus, the nal result calculated by Eq.** [**(5)**](#page6) **shows that the parameters of ResNet50-SD are reduced by 2.623% (0.672M) compared with ResNet50.**

***l*D1**

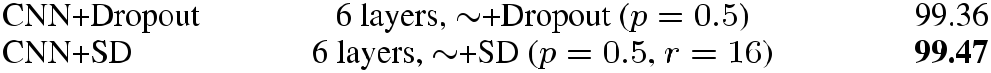
***conv3 output dimension is reduced from 512 to 256, the conv4 output dimension is reduced from 1024 to 512, and the conv5 output dimension is reduced from 2048 to 1024.***

***B. MODEL COMPLEXITY***

***The complexity of the model is evaluated by calculating the number of parameters of the neural network. In the VGG16 network, the dropout unit only introduces probability p to remove the neurons without generating learning parameters in the calculation process. Therefore, in the VGG16-SD architecture, SD block is used to take the place of dropout module, and the additional network parameters are intro-duced on the SD block. The parameters of SD block are contained in two FC of gate mechanism [44], and the calcu-lation of the number of parameters is given by the following formula:***

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**TABLE 2.** Comparison of MLP and CNN on MNIST dataset. indicates the model architecture of CNN.

******

*L*

2X*Nl* *Cl*2

*r*

This illustrates that the proposed SD block used in the VGG16 network is completely acceptable in terms of computational complexity. The original ResNet50 requires 3.87 GFLOPs for a single forward propagation calculation, while by imbed-ding SD block, ResNet50 adds oating-point operations of two FC layers and simultaneously changes the output dimen-sion of each stack, ultimately reducing the overall GFLOPs of the network. By calculation, the ResNet50-SD network corre-sponds to the original ResNet50 network relatively decreas-ing by 10.22% (0.39566 GFLOPs). It can thus be seen that the computational cost of ResNet50 is signi cantly reduced by imbedding SD block. The training time of the network in a real environment is also affected by the bandwidth of GPU, code quality, and other factors.

*Discussion*

In practice, it is acceptable that the complexity and compu-tational cost of the network increases slightly with SD block

1. placed in VGG16. By adding a small amount of extra expen-diture, the accuracy of the network is dramatically improved (see the experiment results in Section [V)](#page6). The SD block is placed in ResNet50 to form a new stack block for connection, changing the output dimension of each stack block, reducing the complexity and computational cost of the network, and improving accuracy.

**V. EXPERIMENTS**

In this section, the implementation of the proposed SD block is introduced and its effectiveness is demon-strated using benchmark datasets such as MNIST [45], CIFAR-10, CIFAR-100 [46], ImageNet 2012 [23], and Places365-Standard [47].

**A. IMPLEMENTATION**

The proposed SD block is implemented on the PyTorch framework. For data augmentation, 224 224 pixels are ran-domly cropped from training images. In addition, batch size is not xed during training and the learning rate is diminished by segments for network training.

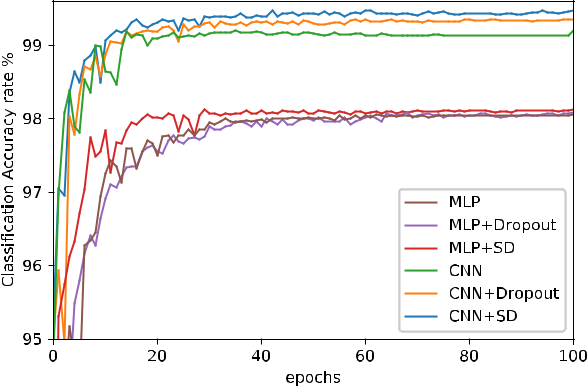
**B. MNIST**

The benchmark dataset MNIST is composed of handwritten digit images. All images are resized to 28 28 pixels. Among

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**FIGURE 3.** Test accuracy of different models on the MNIST dataset.



the resized images, 60,000 images are used as the training set, while others are used as the testing set. For fair comparison, the proposed SD block is embedded into multi-layer percep-tron (MLP) and a simple convolution neural network (CNN). Additionally, the MLP is composed of a fully-connect layer and a ReLUs layer. The CNN contain two convolution layers followed by a pool layer and a fully connect layer. The models are trained by stochastic gradient descent (SGD) optimizer and the initial learning rate is set to 0.1 and decreased 10 times every 30 epochs.

As illustrated in Table [2,](#page6) the standard dropout layer and the proposed SD block are employed in both MLP and CNN for comparison. The accuracy of MLP is 98.06%. After employ-ing dropout layer, the accuracy is improved to 98.09%. In addition, by embedding the proposed SD block into MLP, the accuracy rises again by 0.04%. However, the accuracy of CNNCSD achieves the best performance among CNN, CNNCDropout, and CNNCSD. Both experiments demon-strate the effectiveness of the proposed SD block. The accura-cies of the six models are illustrated in Fig. [3.](#page7) After adopting the SD block, the classi cation accuracy can be observed to reach the optimal value more quickly. In addition, the accu-racy of the MLPCSD and CNNCSD changes more smoothly than other curves, indicating that the proposed SD block is effective.

**C. CIFAR-10 AND CIFAR-100**

Benchmark datasets CIFAR-10 and CIFAR-100 consist of 60,000 32 32 colour images in 10 and 100 classes

respectively. For the former dataset, 6,000 images per class are divided in 5,000 training images and 1,000 testing images. Similarly, 600 images per class in CIFAR-100 are divided in 500 training images and 100 testing images. For comparison, the proposed SD block is employed in VGG16 and ResNet50 and both are trained by SGD optimization. The learning rate is initially set to 0.1, and decreased by a factor of 10 in epoch 90 and a factor of 100 in epoch 150.

As presented in Table [3,](#page7) the proposed SD block assists the VGG16 and ResNet50 datasets to achieve the best perfor-mance. Speci cally, after adding SD block, the accuracy of VGG16CSD rises to 89.04% and 57.23%, which is higher than the VG-G16CDropout. However, the ResNet50CSD rises by 3.01% on CIFAR-10. The accuracy curves of the four models are provided in Fig. [4.](#page8)

**D. ImageNet2012**

The ImageNet2012 dataset is composed of 1.2 million train-ing images and 50,000 testing images. Both images are cho-sen from 1,000 classes of the ImageNet dataset. Following existing methods, the *Top-1* and *Top-5* accuracy rates are adopted for evaluation.

As illustrated in Table [4,](#page8) four models are used for quan-titative comparisons and contain the same architecture as the models in Table [3.](#page7) Results demonstrate that the SD block is bene cial to model performance and ef ciency. Speci cally, after adopting the SD block, both VGG16 and ResNet50 achieve the best performance in terms of the *Top-1* and *Top-5* accuracy rates. Moreover, the model sizeand GFLOPs of VGG16CSD is slightly increased, while the model size and GFLOPs of ResNet50CSD is slightly decreased. The accuracy curves of the four models are pro-vided in Fig. [5.](#page8)

**E. PLACES365-STANDARD**

Places365-Standard is a benchmark dataset that consists of single object images. In this dataset, approximately 1.8 million images are split into the training set, and about 365,000 images are used for the validation set. The networks of VGG16, ResNet50, and ResNet152 are utilized to verify the effectiveness of the SD block and corresponding results are presented in Table [5.](#page9)

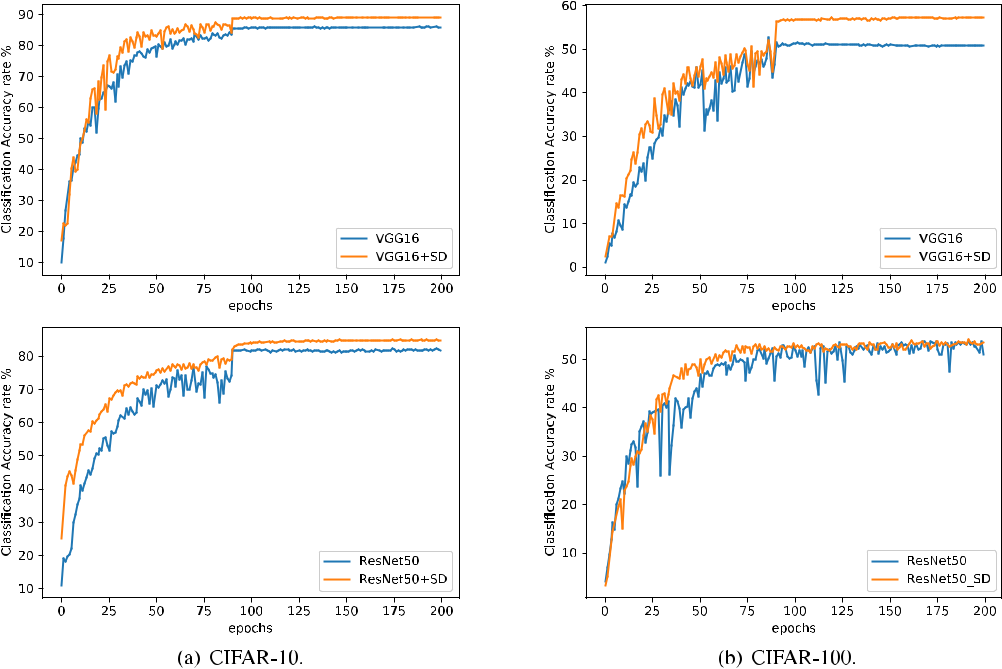
As illustrated, VGG16CDropout obtains the Top-1 accu-racy of 55.24% and VGG16CSD reaches 56.31%. By

**TABLE 3.** Accuracy rates of different models on CIFAR-10 and CIFAR-100. The detail architectures of VGG16 and ResNet50 are provided in Table [1.](#page5)



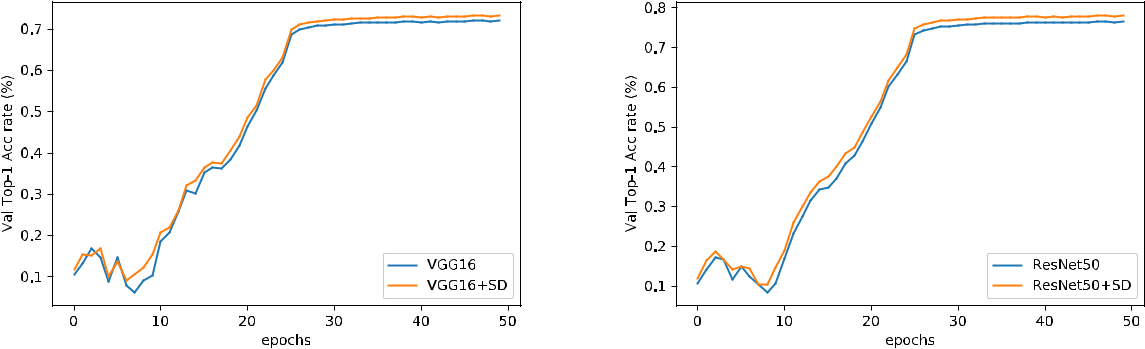
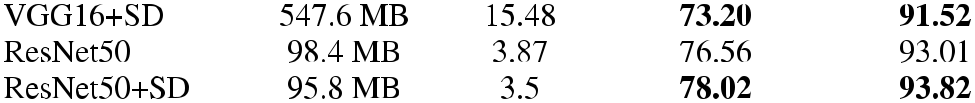
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**FIGURE 4.** Testing accuracy (%) of VGG16, ResNet50, VGG16CSD and ResNet50CSD on CIFAR-10 and CIFAR-100.

**TABLE 4.** Comparison of **Top-1** and **Top-5** accuracy rates (%) of VGG16, ResNet50, VGG16CSD and ResNet50CSD on ImageNet2012 dataset.



**FIGURE 5.** Curves of validation **Top-1** accuracy on ImageNet2012 dataset. (**Left**): VGG16 and VGG16CSD; (**Right**): ResNet50 andResNet50CSD.

comparing the two metrics of VGG16CDropout and VGG16CSD, it can be observed that the SD block can improve classi cation performance. ResNet50 obtain the Top-1 accuracy of 55.18%, ResNet152 achieves 54.74% and ResNet152CSD can reach 55.32%, which shows that the metrics of the ResNet152CSD are closer to ResNet50 than ResNet152. Comparing the last four models indicates that the

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SD block can help to mitigate the dif culty of training deeper networks to some extent.

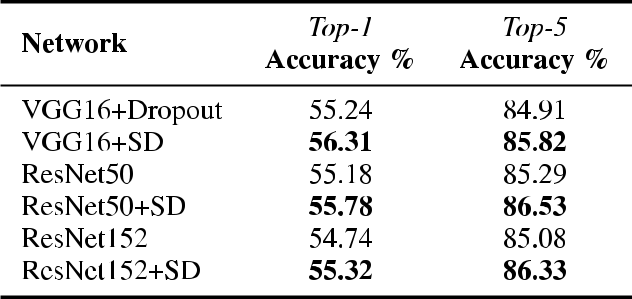
1. **COMPARING THE PERFORMANCE OF REDUCING OVERFITTING**

In this section, we design two experiments to verify the performance of the proposed SD block on the over tting

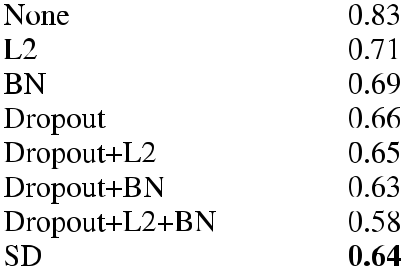
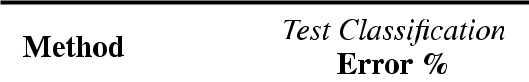
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**TABLE 5.** Comparison of **Top-1** and **Top-5** accuracy rates (%) of VGG16,ResNet50, and ResNet152 and corresponding improved networks on Places365-Standard dataset.

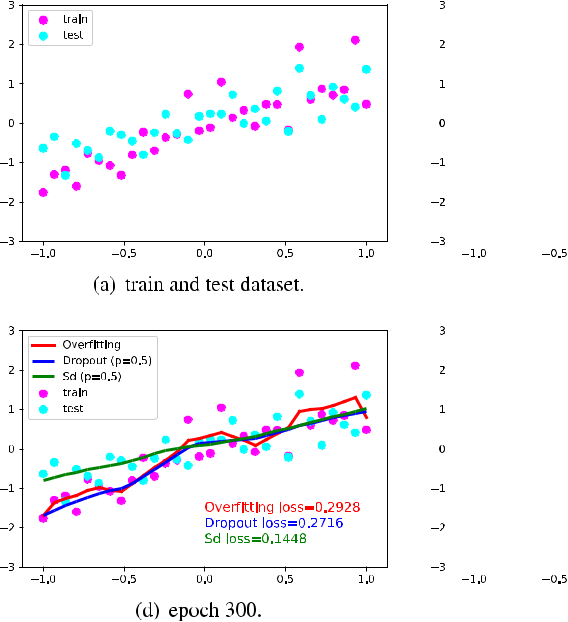


**TABLE 6.** The results of comparison for different regularization methodson MNIST dataset. ‘‘None’’ means the network is trained without using any regularization methods.

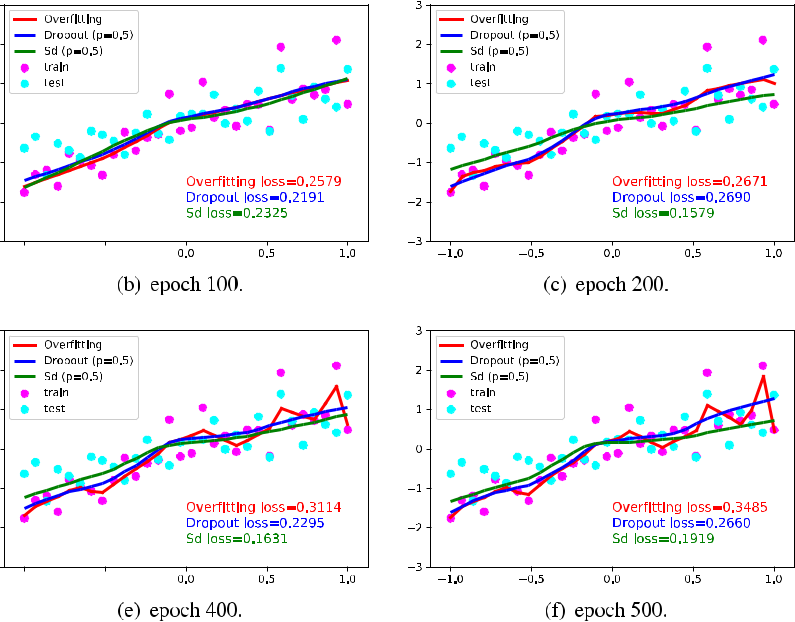
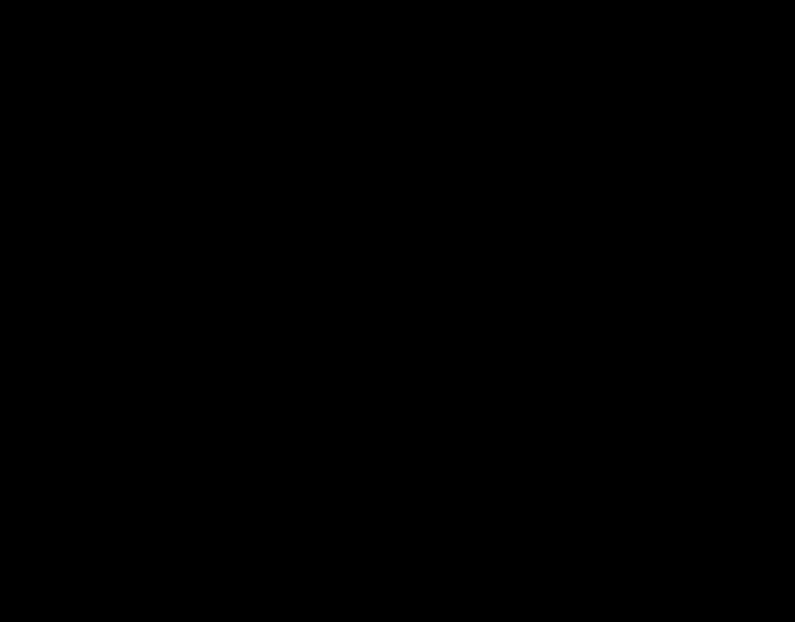


problem. In the rst experiment, other three commonly used regularization methods such as L2 [9], BN [18], Dropout [17] are introduced to compare. We construct the same network architecture (28 28-32 24 24-64 12 12-1024-1024-2048-10) with ReLUs and train it with the four regu-larization methods (including SD) and the SGD optimizer by 100 epochs on the MNIST dataset. The initial learning rate is set to 0.1 and decreases to 0.05 in epoch 15, 0.01 in epoch 31, 0.005 in epoch 61 and 0.001 in epoch 71. Table [6](#page9) shows the validation results of different regularization methods. The results demonstrate that the proposed SD can achieve a lower generalization error than other three regularization methods (0.64% vs. 0.71%, 0.69% and 0.66%), and the combination of the SD with L2 and BN reaches the lowest error (0.56%).

Fewer data can highlight the problem of over tting, so in the second experiment, we randomly sample 30 points



data in the interval ( 1,1) of linear space as another dataset. The same model architecture (30-300-300-1) with ReLUs is trained applying the *Adaptive moment estimation* (Adam) optimizer with Dropout and the proposed SD. The learning rate is xed with 0.01 and training epoch is set to 500. Fig. [6](#page9) shows the test result after every 100 epochs of train-ing. Fig. 6(f) is the nial result which demonstrates that the proposed SD block achieves the lowest test loss (0.1919 vs. 0.2660 and 0.3485), and the generalization performance of the green line (SD) is better than the blue line (Dropout) and the red line (over tting).



**FIGURE 6.** The fitting situation of dropout and SD on dataset during the training process. Fig. 6(a) shows the training and testing datasets. InFig. 6(b), the red line called as ‘‘Overfitting’’ represents that the network is overfitting without using any regularization methods. It is not easy to distinguish the performance among SD, Dropout and overfitting at epoch 100. From Fig. 6(c) to Fig. 6(f), the red line gradually fits the train data perfectly, but it is not well done in test data. These four pictures shows the generalization performance of SD (green line) is better than Dropout (blue line).

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**VI. CONCLUSION**

In this paper, the novel SD technique is proposed for solving the problem of over tting. The proposed SD block main-tains the most important channels by dropping out features according to the global information of each channel. The method is composed of three steps: channel compression calculation, channel weight calculation, and sorting and drop-ping calculation. The proposed SD is further embedded into state-of-the-art backbone CNN models such as VGGNet16, ResNet50, and ResNet152 for image classi cation tasks. Extensive experiments on ve challenging image classi - cation datasets demonstrate that the proposed SD block is effective and widely applicable.

In the future work, we will try to extend the proposed SD block, and apply it into high-level computer vision task such as tacking [48]. We will also focus on theoretical analysis. We believe it will contribute to further improvement and application of the algorithm.

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