

DRC Control

Clearance:

Minimum track width: mm

Minimum via size: mm

Minimum uVia size: mm

☒ Refill all zones before performing DRC

☐ Report all errors for tracks (slower)

☐ Test tracks against filled copper areas (very slow)

☐ Create report file:

Messages

Problems / Markers (11) Unconnected Items (4)

Track too close to pad
@(130.762 mm, 110.443 mm): Track 0.250 mm [<no net>] on B.Cu, length: 1.435 mm
@(129.747 mm, 109.428 mm): Pad 3 of J2 on All copper layers

Track too close to pad
@(129.172 mm, 109.428 mm): Track 0.250 mm [<no net>] on B.Cu, length: 0.575 mm
@(129.747 mm, 109.428 mm): Pad 3 of J2 on All copper layers

Track too close to pad
@(121.032 mm, 117.568 mm): Track 0.250 mm [<no net>] on B.Cu, length: 11.512 mm
@(121.032 mm, 117.568 mm): Pad of H4 on All copper layers

Track too close to pad
@(121.032 mm, 105.971 mm): Track 0.250 mm [<no net>] on B.Cu, length: 11.403 mm
@(121.032 mm, 94.568 mm): Pad of H1 on All copper layers

Track too close to pad

Delete Marker Delete All Markers Run DRC Close List Unconnected

Screen Shot of the DRC configuration window

The final design does not pass all the tests. The components chosen were too large for the constrained area of the board that we were working with. The components were therefore too close to each other making the tracks to form a gridlock. As a result, not all components were connected.