

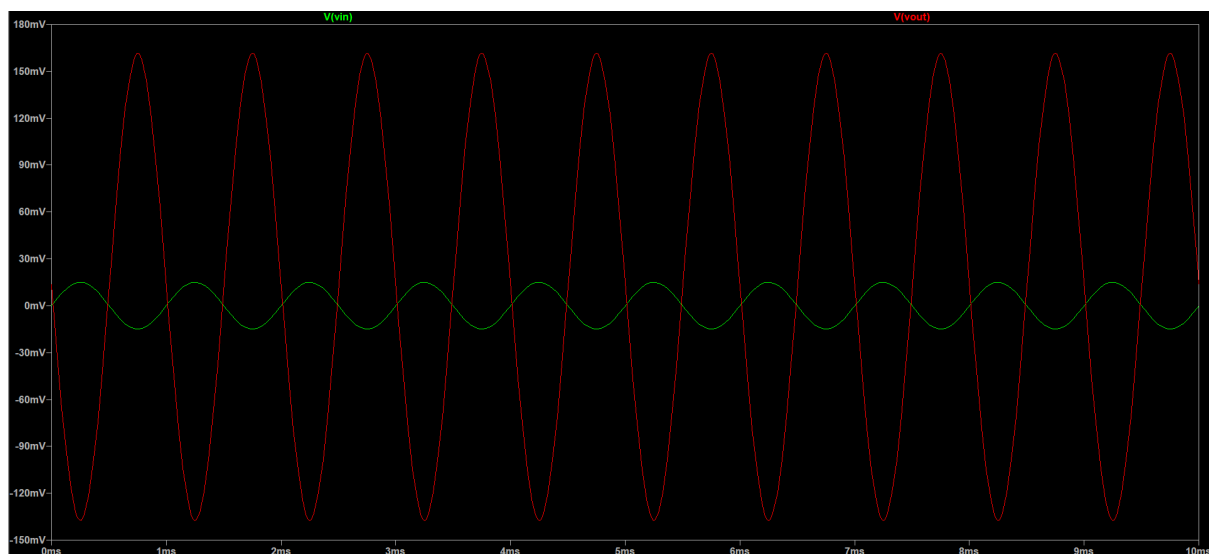
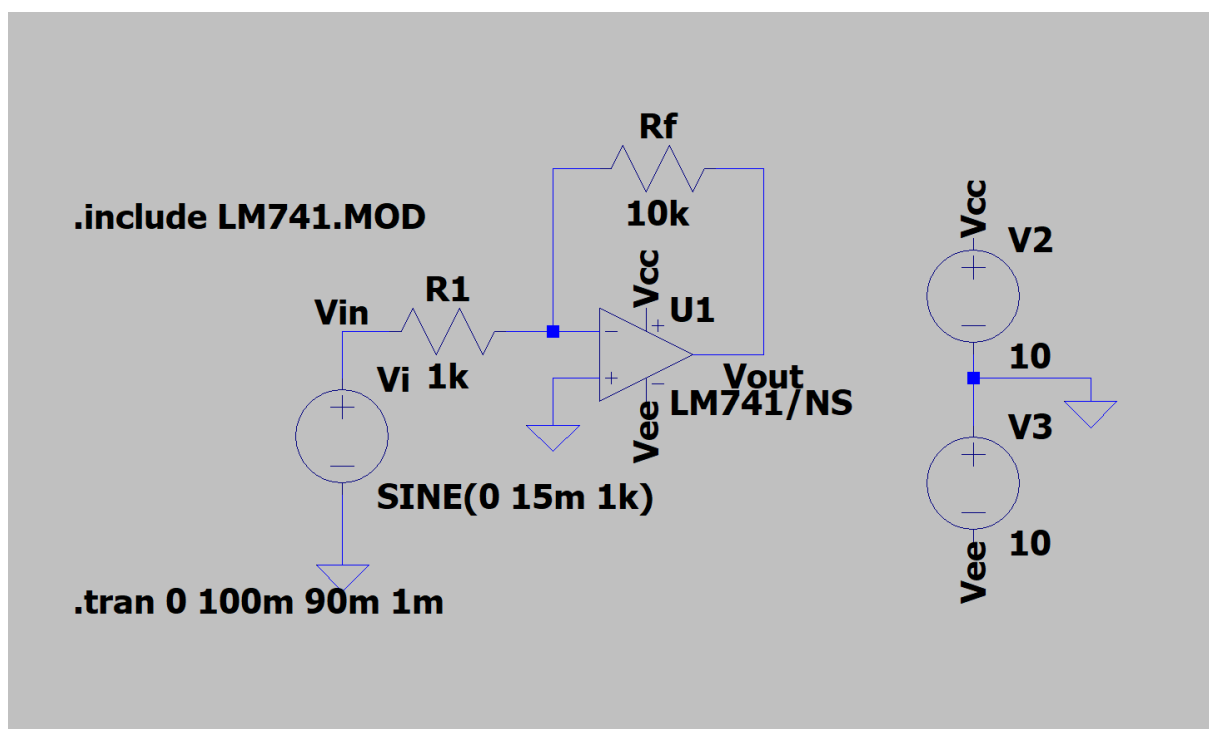
Aim: To study the working of inverting, non-inverting, differentiator and integrator circuits using operational amplifiers.

Software used:

Inverting Amplifier:

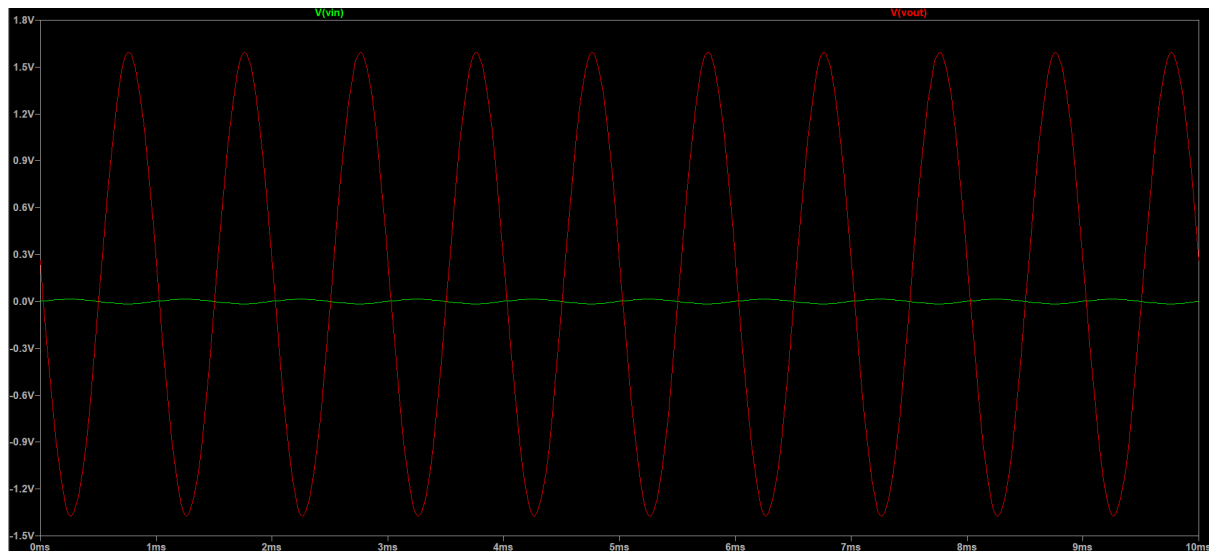
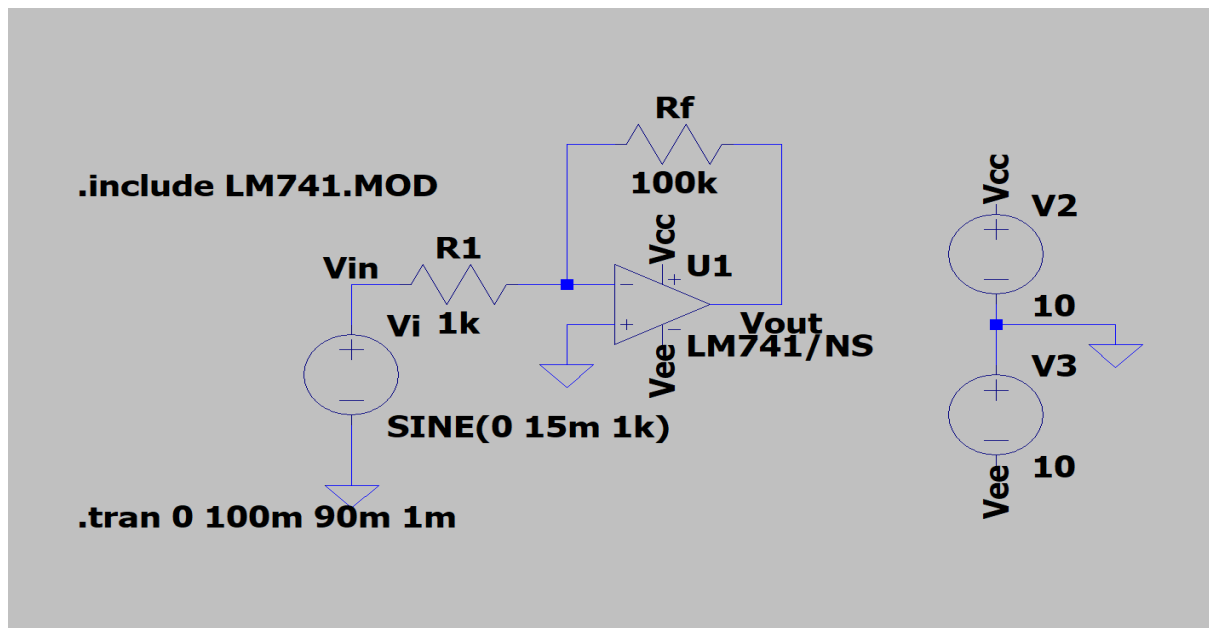
The simulations are done with three feedback resistances 10k, 100k, 1000k and gain is found to be 10, 100, 1000 respectively. (since, $\text{gain} = -R_f/R_1$)

Case 1: $R_f = 10k$



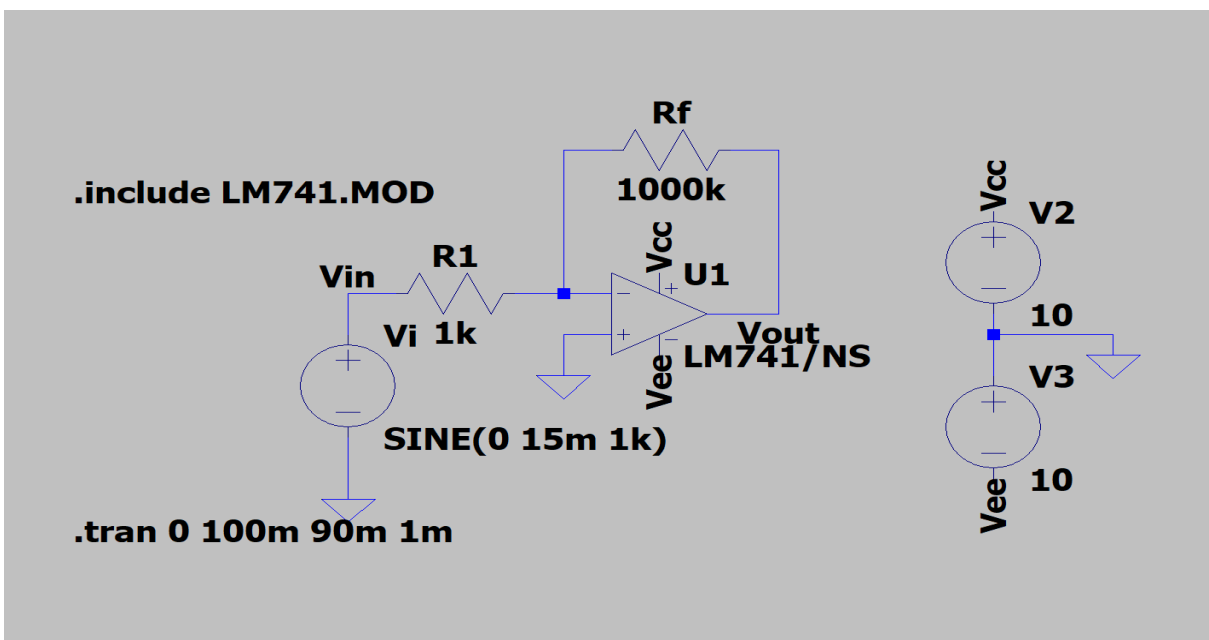
The output is out of phase with the input and has a gain of 10 V/V.

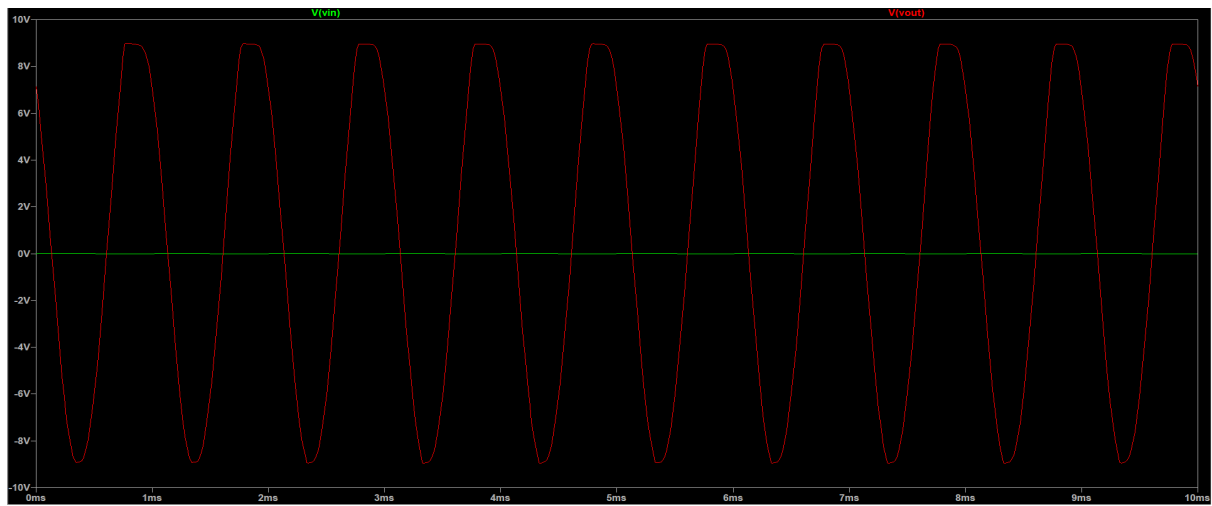
Case 2: $R_f = 100k$



The output is out of phase with the input and has a gain of 100 V/V.

Case 3: $R_f = 1000k$



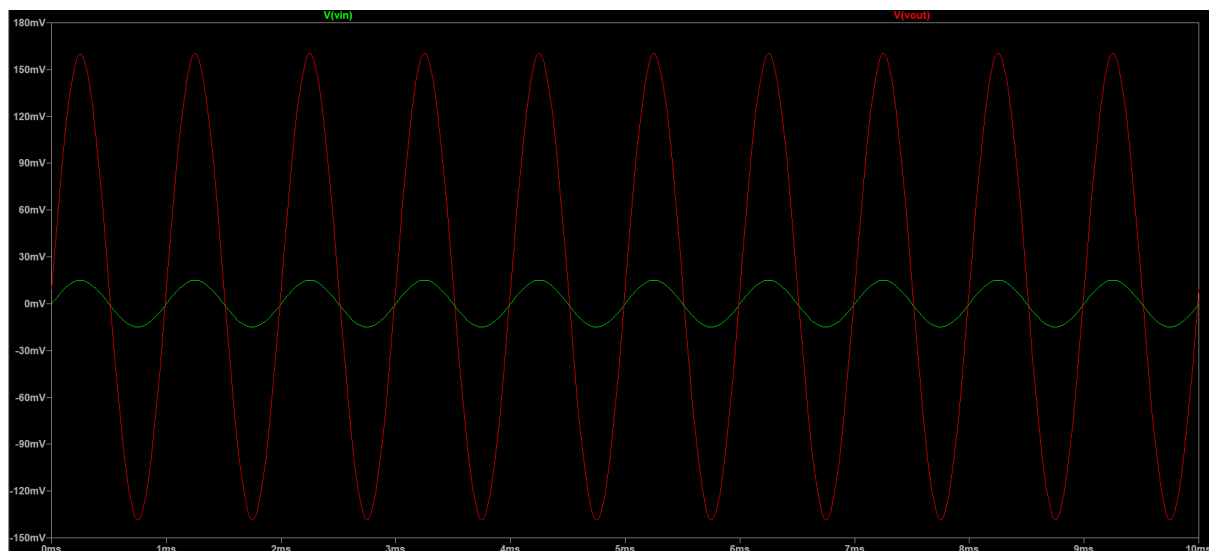
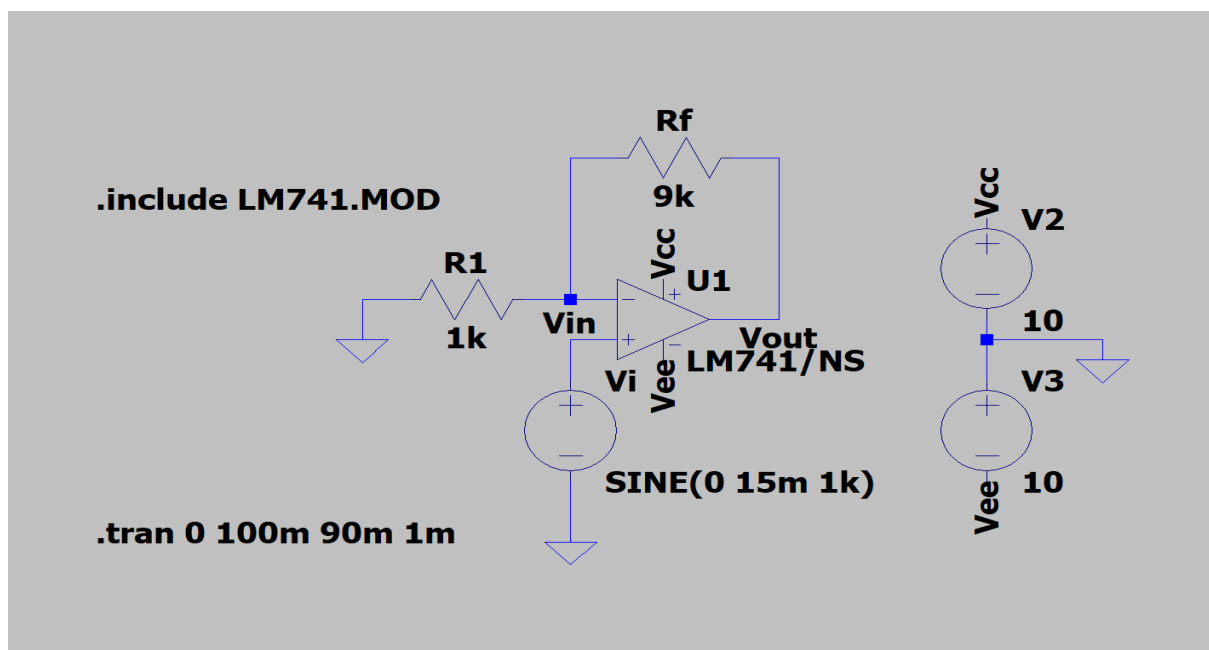


The output waveform here is **clipped off** because the output voltage for those is values is greater than the power supply voltages (i.e., V_{cc} & V_{ee}).

Non-Inverting Amplifier:

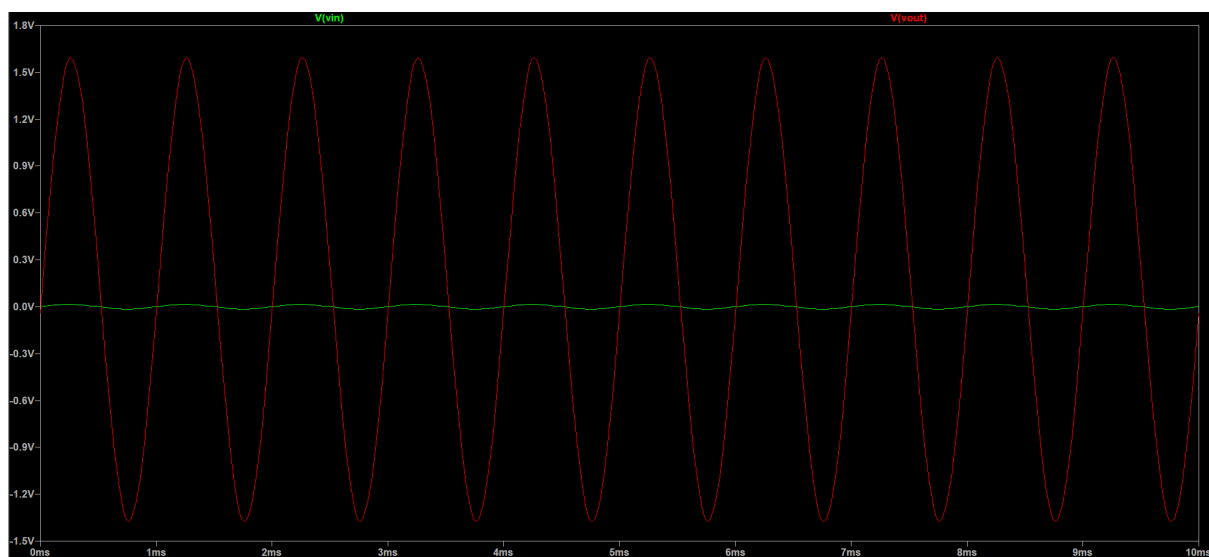
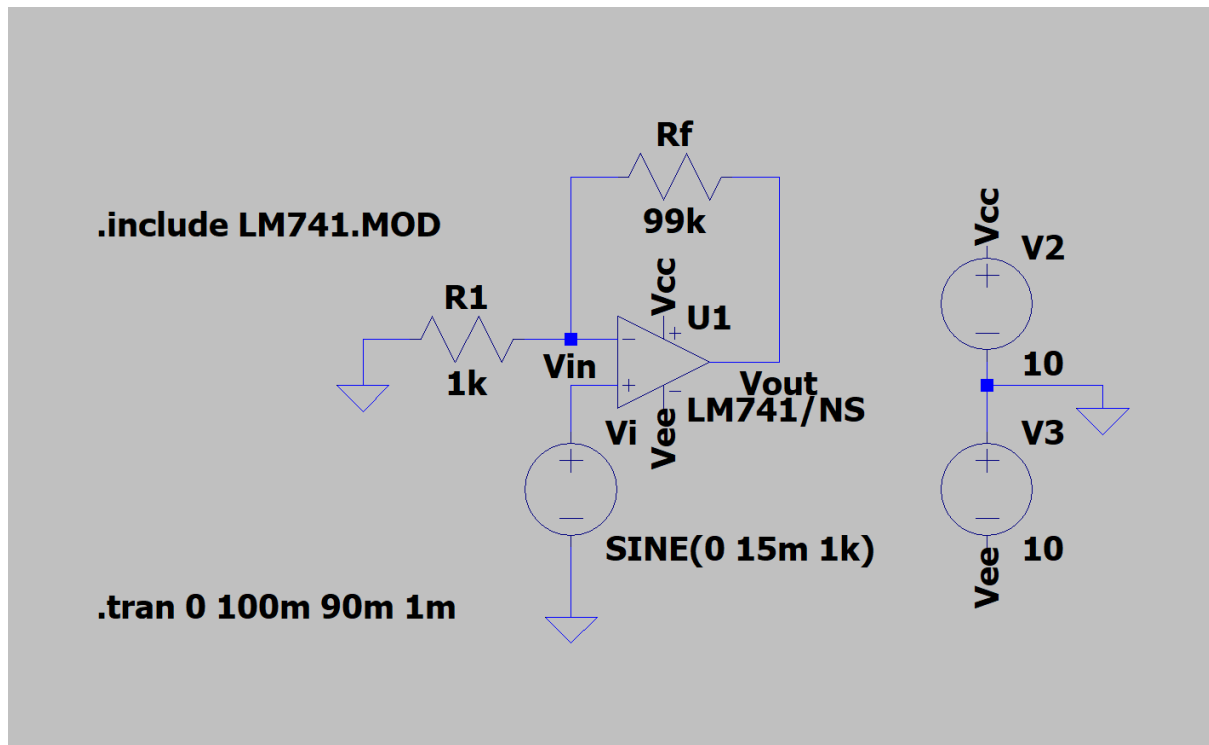
The simulations are done with three feedback resistances 9k, 99k, 999k and gain is found to be 10, 100, 1000 respectively. (since, $\text{gain} = (1 + R_f/R_1)$)

Case 1: $R_f = 9k$



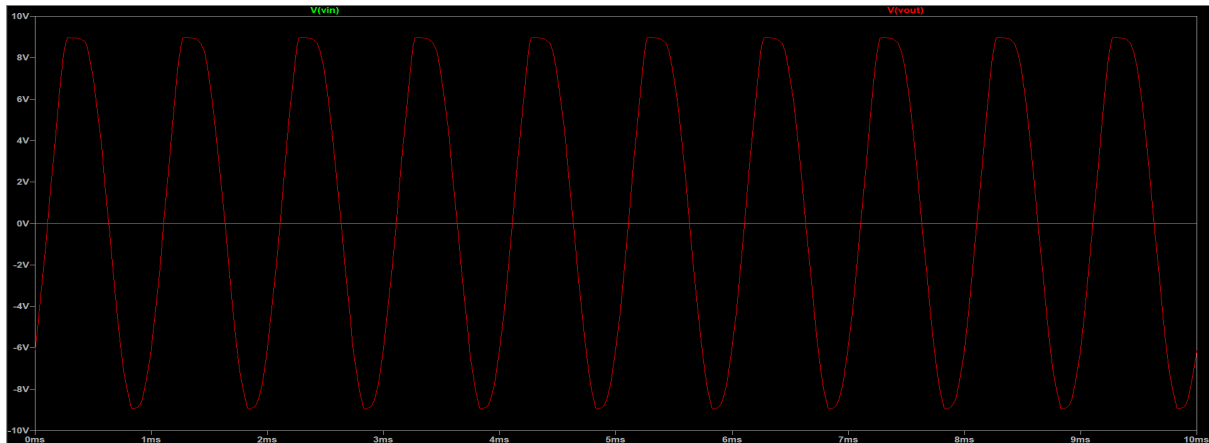
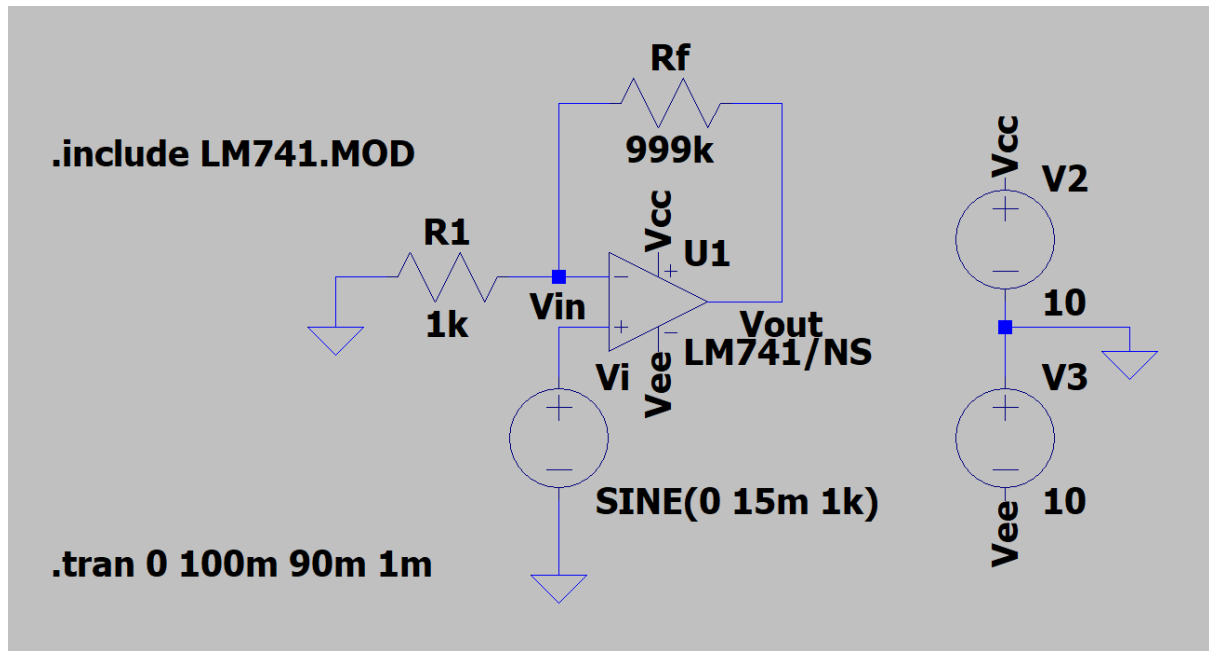
The output is in-phase with the input and has a gain of 10 V/V.

Case 2: $R_f = 99k$



The output is in-phase with the input and has a gain of 100 V/V.

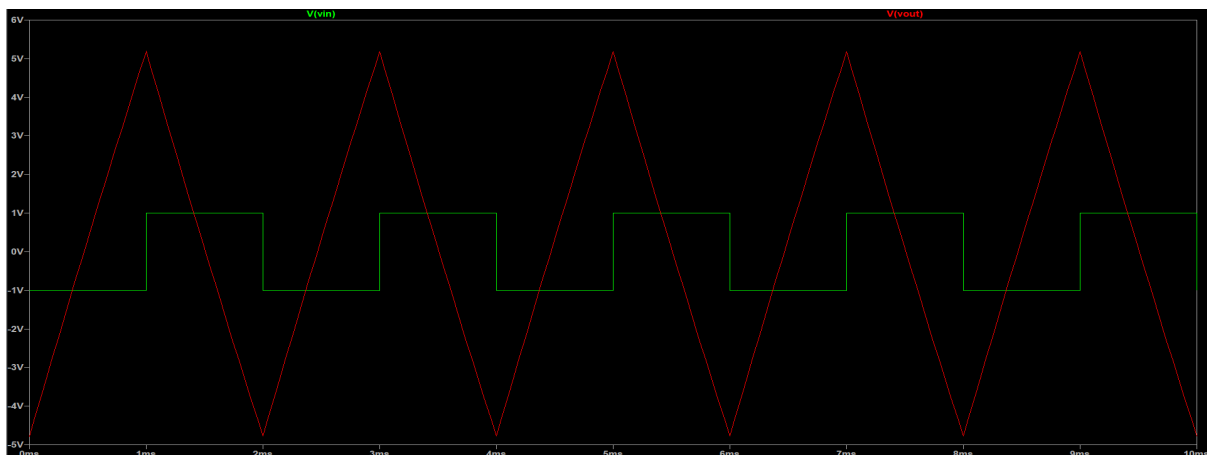
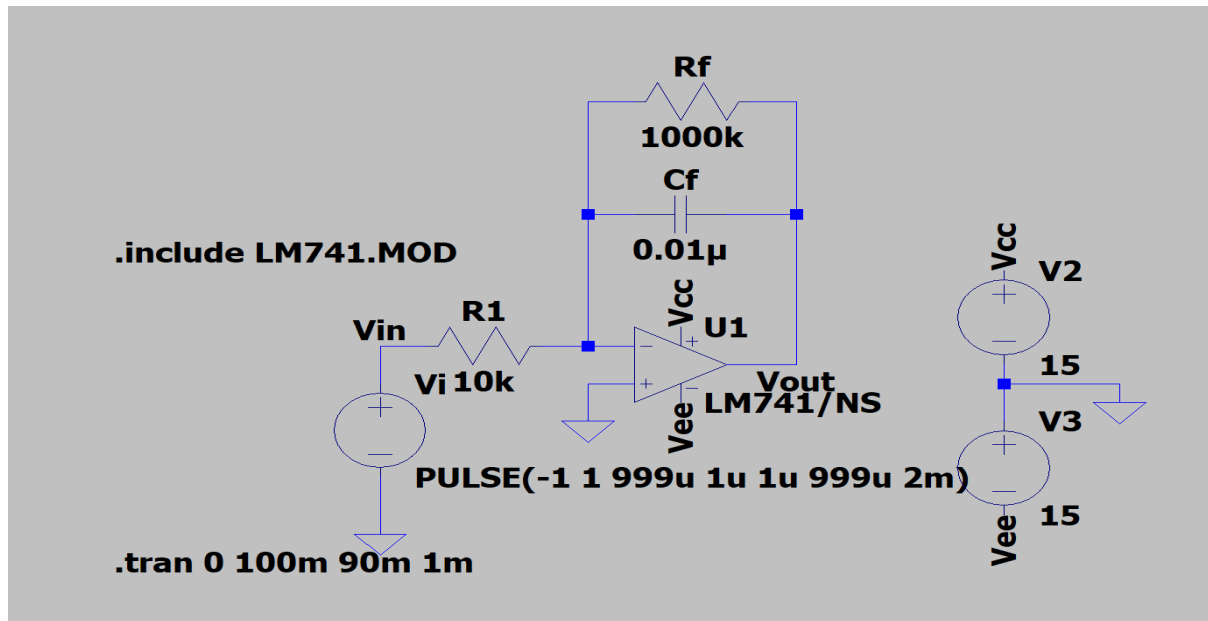
Case 3: $R_f = 999k$



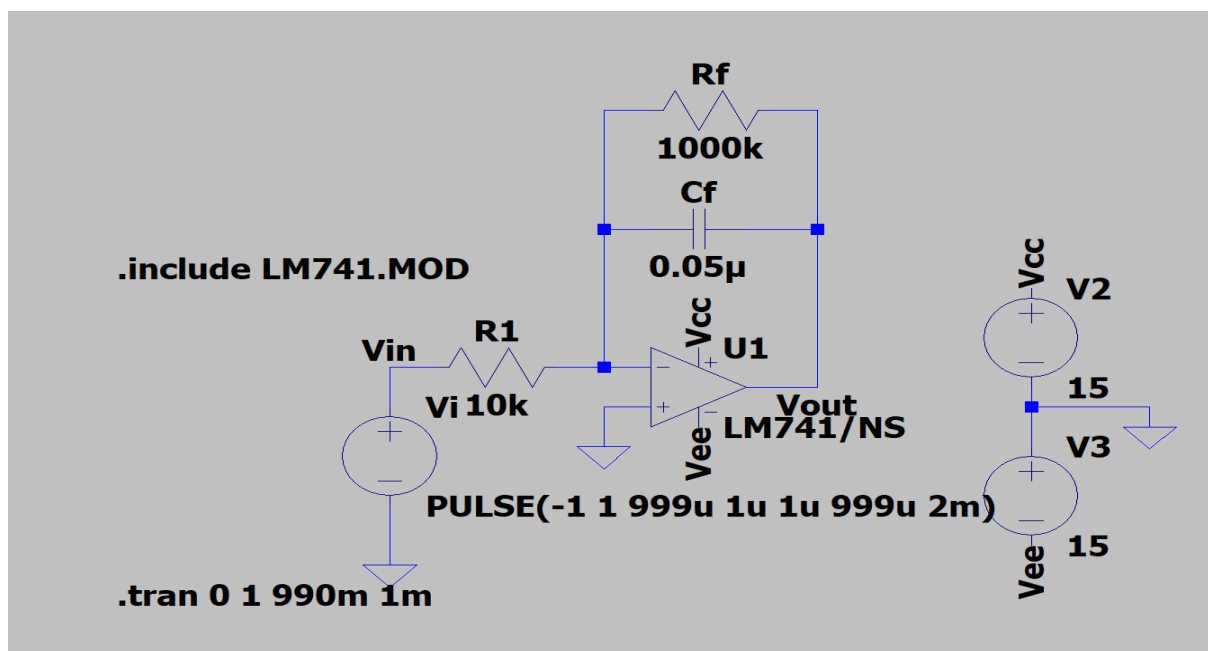
The output waveform here is **clipped off** because the output voltage for those values is greater than the power supply voltages (i.e., V_{cc} & V_{ee}).

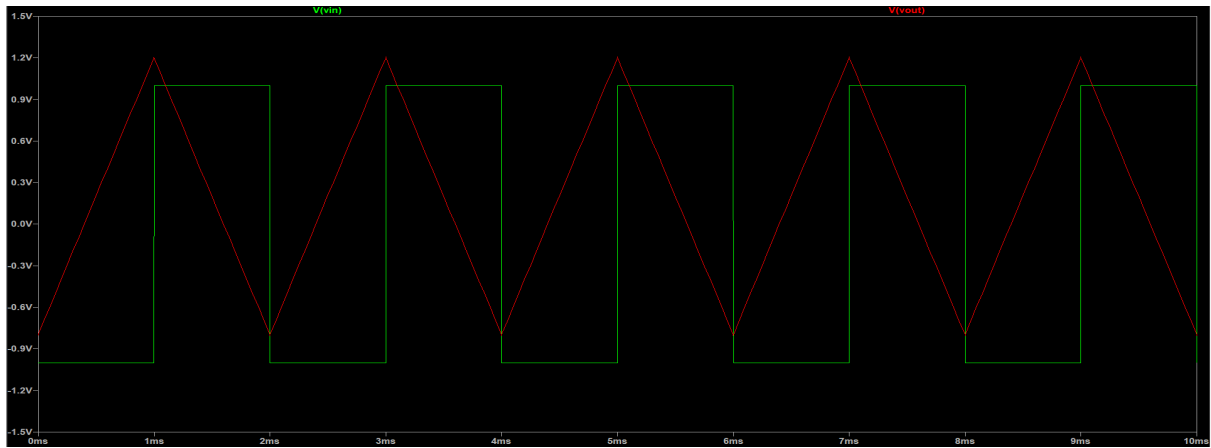
Integrator:

Case 1: Square wave of amplitude 1V as input with $R_1 = 10k$, $C_f = 0.01\mu$, $R_f = 1000k$

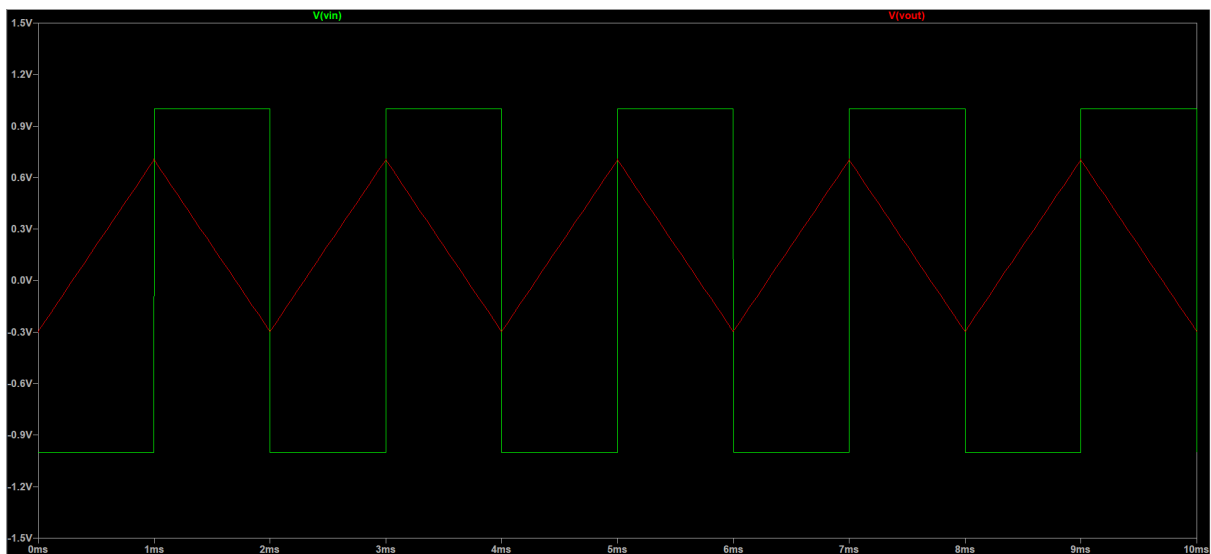
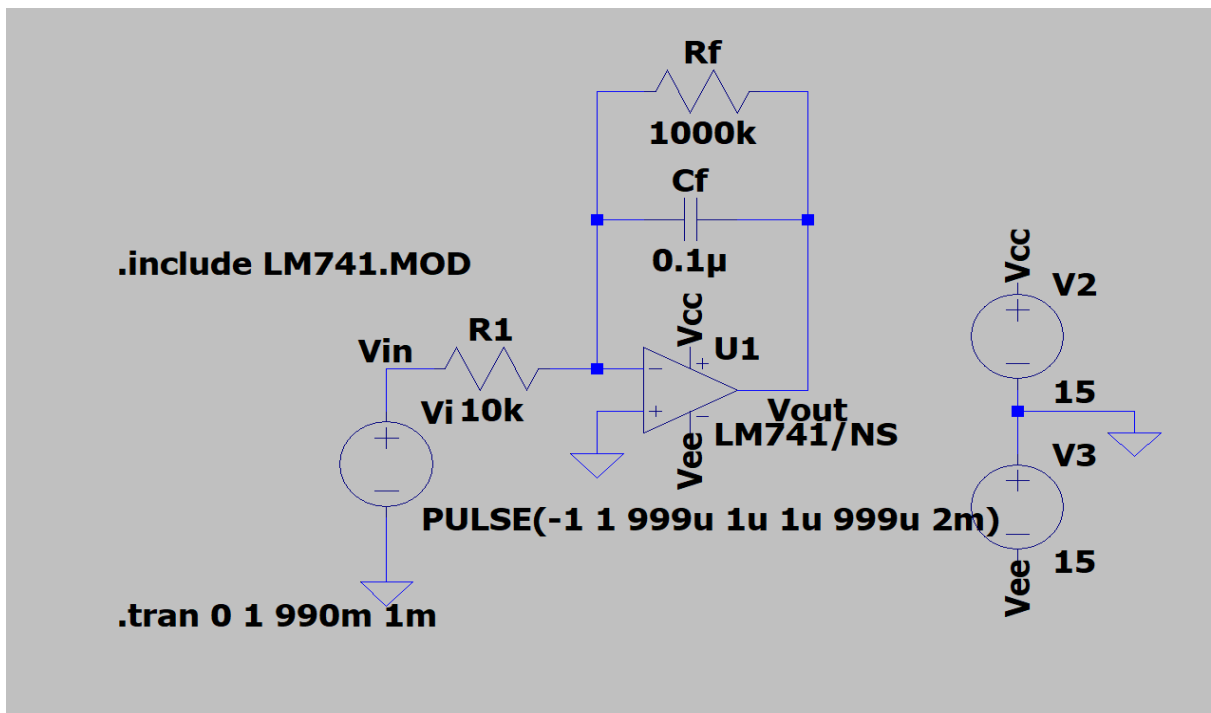


Case 2: Square wave of amplitude 1V as input with $R_1 = 10k$, $C_f = 0.05\mu$, $R_f = 1000k$

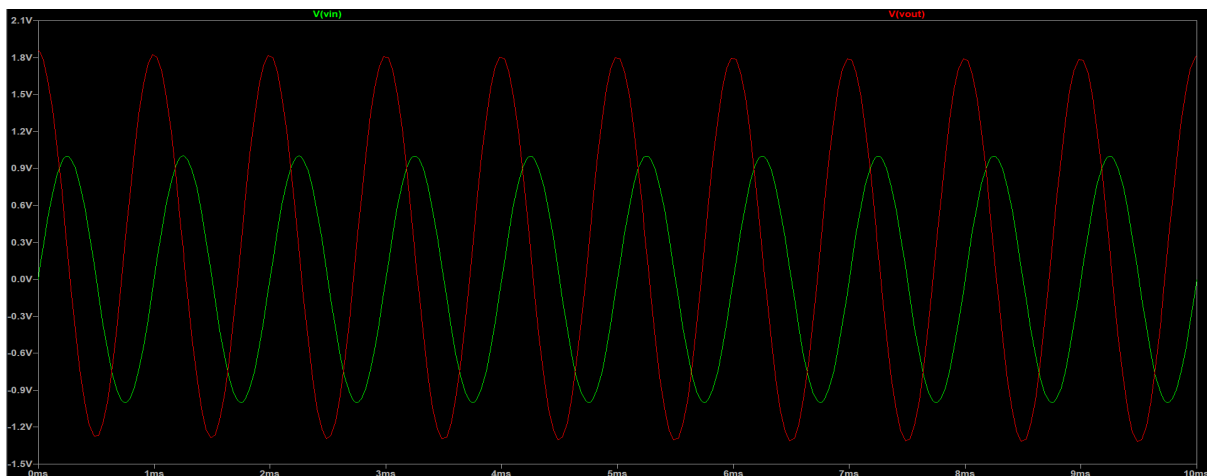
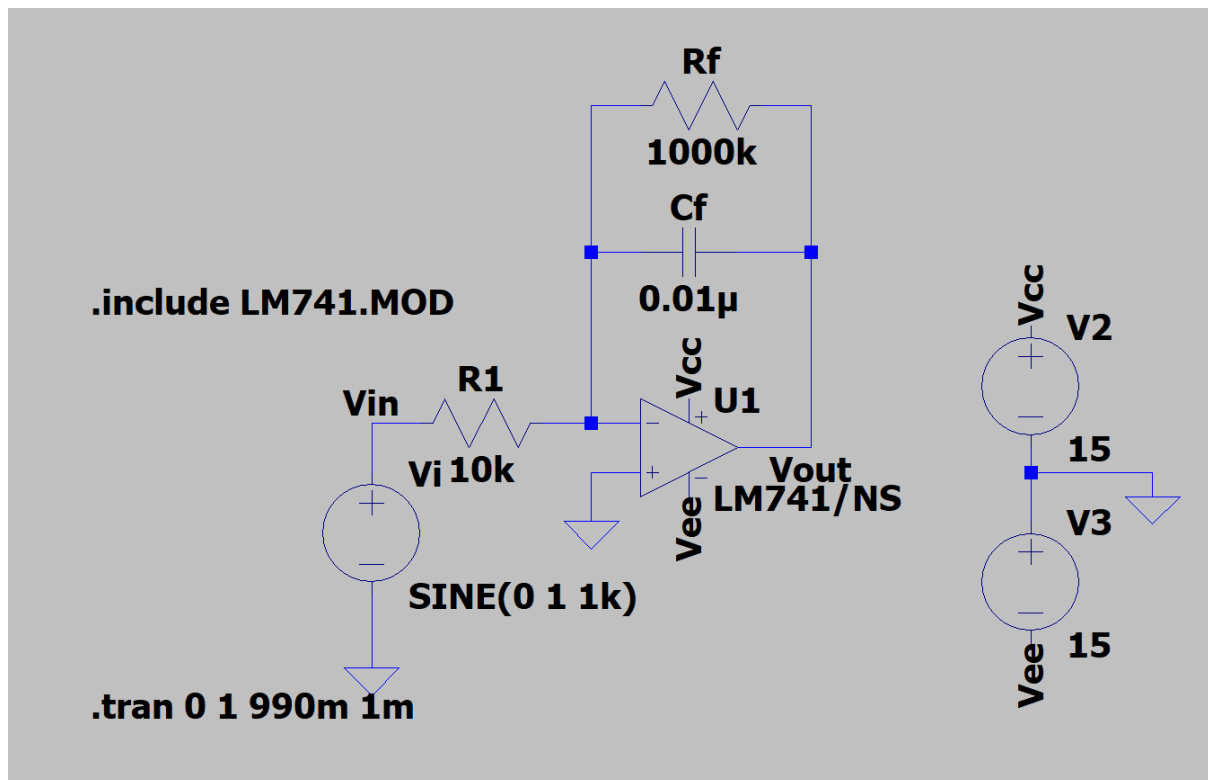




Case 3: Square wave of amplitude 1V as input with $R_1 = 10k$, $C_f = 0.1\mu$, $R_f = 1000k$



Case 4: Sine wave of amplitude 1V as input with $R_1 = 10k$, $C_f = 0.01\mu$, $R_f = 1000k$



As can be seen from the above plot,

- 1) V_{out} is leading V_{in} by a phase of 90 degrees
- 2) $V_{out} = 3.2$ V peak to peak sine wave and $V_{in} = 2$ V peak to peak sine wave.

Transfer function, $\frac{V_o(s)}{V_i(s)} = -\frac{(\frac{1}{sC_f} \parallel R_f)}{R_1} = -\frac{\frac{R_f}{1+sR_fC_f}}{R_1} = \frac{-R_f/R_1}{1+sR_fC_f}$

\Rightarrow Gain magnitude, $\left| \frac{V_o(j\omega)}{V_i(j\omega)} \right| = \frac{R_f/R_1}{\sqrt{1+\omega^2 R_f^2 C_f^2}}$

$\Rightarrow \left| \frac{V_o(j\omega)}{V_i(j\omega)} \right| = \frac{(\frac{1000}{10})}{\sqrt{1+(2\pi \times 10^3 \times 10^6 \times 10^{-7})^2}} = \frac{100}{\sqrt{1+(2\pi \times 10)^2}} \approx \frac{100}{2\pi \times 10}$

$\Rightarrow \text{gain} \approx 1.6 \text{ V/V}$

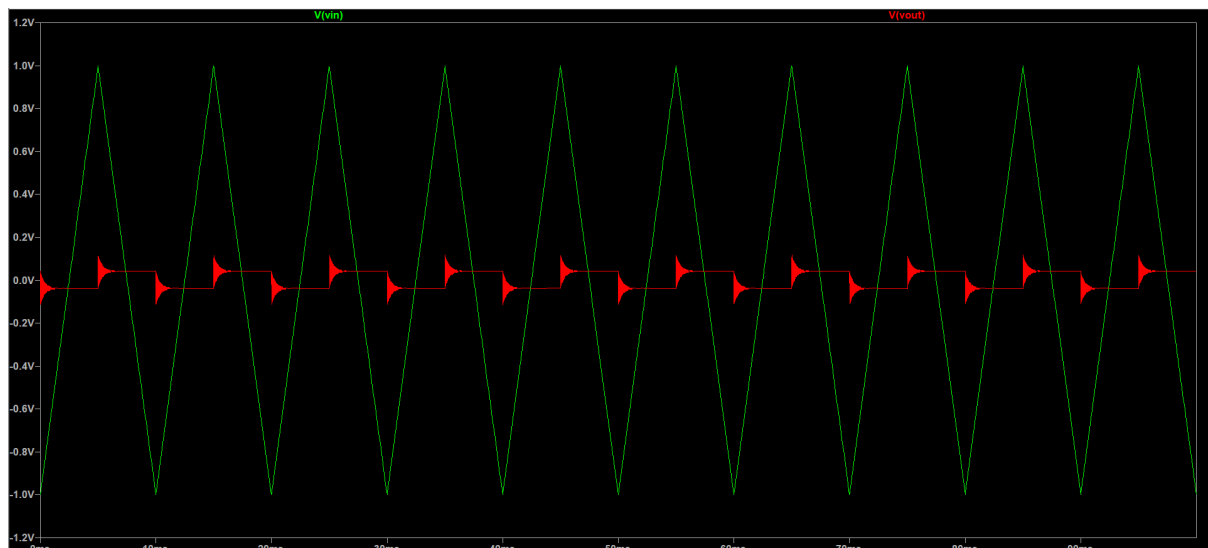
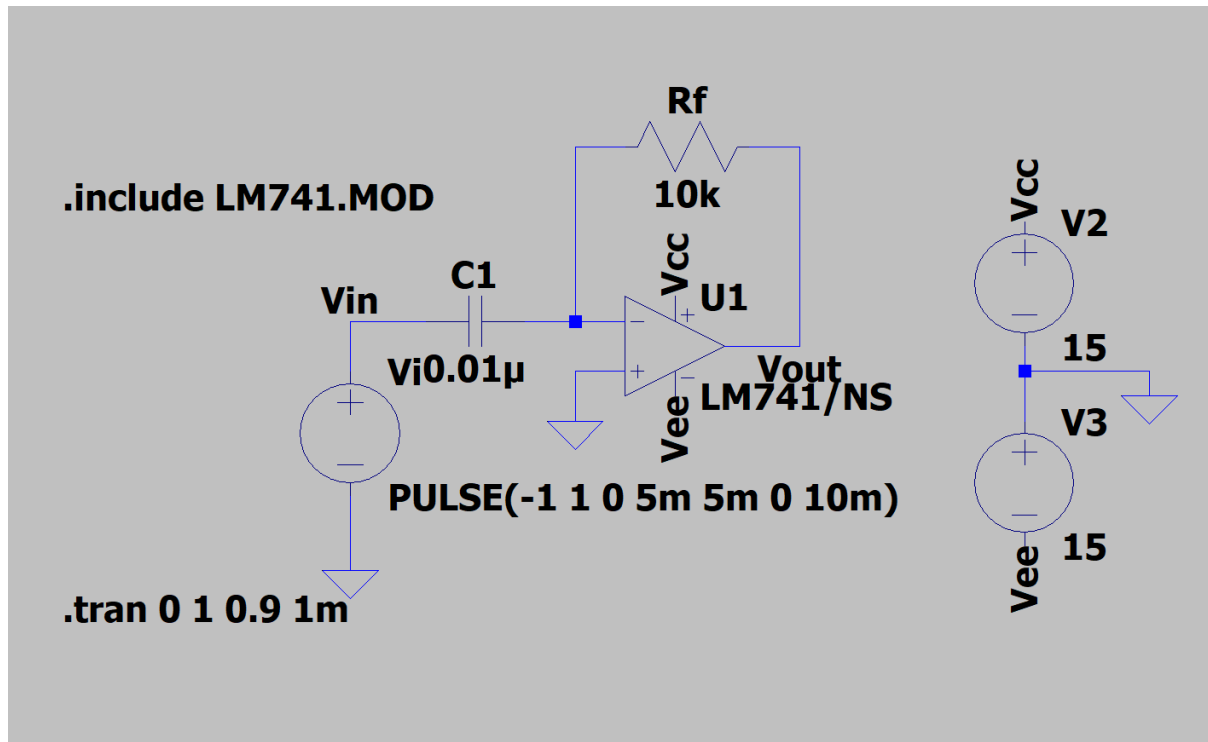
$\Rightarrow |V_o(j\omega)| = 1.6 \times |V_i(j\omega)|$
 $= 1.6 \times 2 \text{ V peak to peak}$

$\Rightarrow (V_o(j\omega)) = 3.2 \text{ V peak to peak}$

Purpose of resistor R_f : introducing R_f in parallel with capacitor C_f restricts the gain to $-R_f/R_1$ for low frequencies. The gain **without** R_f (i.e., $1/(w \cdot R_1 \cdot C_f)$) for dc voltages would be infinity which implies that for any tiny dc component of input signal would saturate the output voltage.

Differentiator:

Case 1: Triangle wave of amplitude 1V as input with $C_1 = 0.01\mu$, $R_f = 10k$



As can be seen from the above plot,

V_{in} = Triangular wave of amplitude 1V and frequency 100Hz.

V_{out} = Square wave of amplitude 40mV and frequency 100Hz.

$$V_{out} = -R_f C_1 \frac{dV_{in}}{dt}$$

$$\Rightarrow V_{out} = - (10 \times 10^3) \times (0.01 \times 10^{-6}) \times \left[\frac{2}{5 \times 10^{-8}} \right] \quad \left(\text{when } V_{in} \text{ is changing from } -1V \text{ to } 1V \right)$$

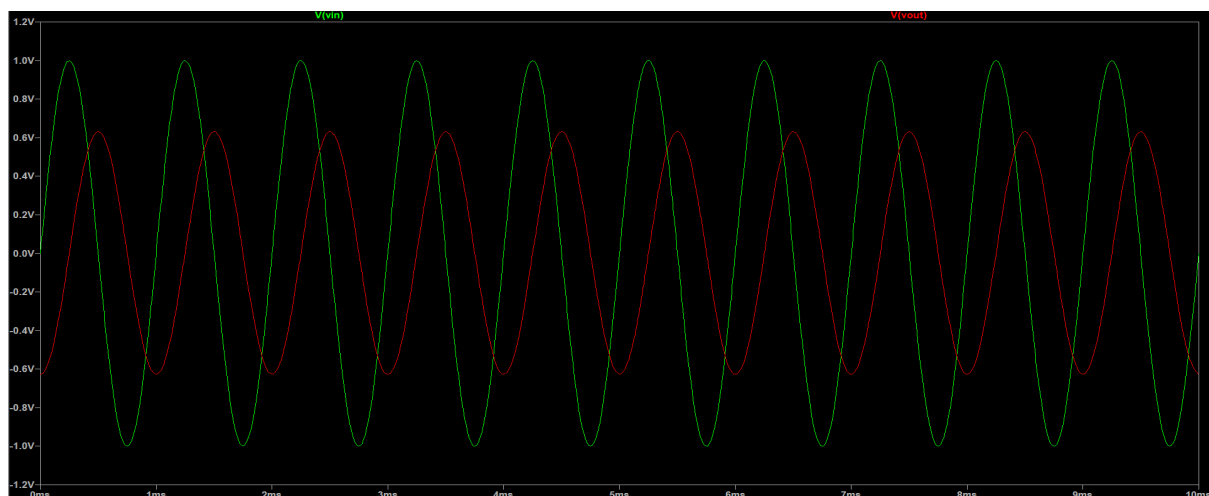
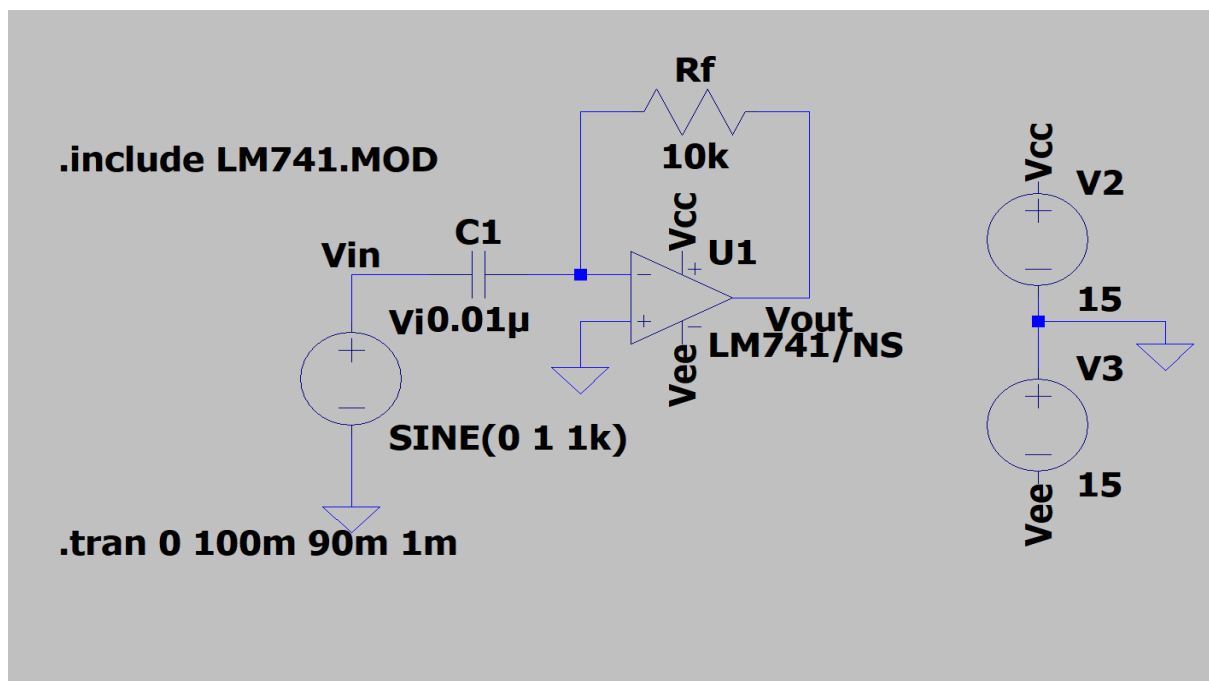
$$\Rightarrow V_{out} = -0.04V = -40mV$$

$$\Rightarrow V_{out} = - (10 \times 10^3) \times (0.01 \times 10^{-6}) \times \left[\frac{-2}{5 \times 10^{-8}} \right] \quad \left(\text{when } V_{in} \text{ is changing from } 1V \text{ to } -1V \right)$$

$$\Rightarrow V_{out} = +0.04V = 40mV$$

$\therefore V_{out}$ is square wave of amplitude 40 mV.

Case 2: Sine wave of amplitude 1V as input with $C_1 = 0.01\mu$, $R_f = 10k$



As can be seen from the above plot,

- 1) V_{out} is lagging V_{in} by a phase of 90 degrees
- 2) V_{in} = Sine wave of amplitude 1V and frequency 1kHz.
 V_{out} = Sine wave of amplitude 0.63 V and frequency 1kHz.

$$V_{out} = -R_f C_1 \frac{dV_{in}}{dt}$$

$$\Rightarrow V_{out} = -(10 \times 10^3)(0.01 \times 10^{-6}) \times \frac{d}{dt}(\sin(2\pi \times 10^3 t + \phi))$$

$$\Rightarrow V_{out} = -0.1 \times 10^{-3} \times 2\pi \times 10^3 \times \cos(2\pi \times 10^3 t + \phi)$$

$$\Rightarrow V_{out} = \underline{0.628} \sin(2\pi \times 10^3 t + \phi - \underline{\frac{\pi}{2}})$$

$\therefore V_{out}$ is a sine wave of amplitude 0.628 V and $f = 1 \text{ kHz}$
and V_{out} is lagging V_{in} by a phase of $\frac{\pi}{2}$.