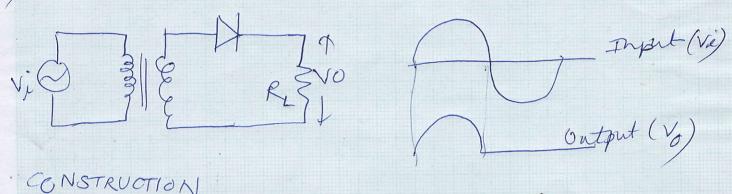
LECTURE 5 TH OCT 2020 RECTIFIERS

A rectifier is a circuit which uses one or more déodes to convert a.c. voltage ente pulsating d.c. voltage.

(I) HALF WAVE RECTIFIER.



CONSTRUCTION It consusts of a single diode in series with load resistor. The emport is transformed coupled so that the source voltage can be stepped up or down and a c. power is isolated from the rectifier circuit to reduce chances of getting shock.

 $\frac{\sqrt{2}}{\sqrt{1}} = \frac{N2}{N_1}$ where $V_1 = lms$ of primary voltage $\sqrt{2} = lms$ of secondary voltage N, = No, of turns in primary N2 = No, of truns in sandaery winding.

Worlding!
(i) + ve half cycle The diode is forward biased and acts as a short circuit. Voltage is produced across 'sed resistor. The voltage produced across RL

talt eyele of a.c. input voltage. conduct There is no autent flow or vottage (1) - we half eycle! ospaceos RL i.e. id=0, 4=0 The circuiat uses only half cycle of the ac input vottage: known as half wave lectified PARAMETERS (1) AVERAGE OR D.C. VALUE OF LOAD VOLTAGE (Vdc) Vdc = Area under the cueve over full cycle $\int_{0}^{T} \frac{v \, dv}{2\pi} = \int_{0}^{T} \sqrt{m} \sin v \, dv = \frac{\sqrt{m}}{2\pi} \left(-\cos v\right)^{T}$ $=\frac{V_m}{2\pi}\left(+1-(-1)\right)=\frac{V_m}{2\pi}\times \mathcal{R}=\frac{V_m}{11}$ Vdc=01318Vm - 3TI (2) AVERAGE OR DC VALUE OF LOAD CURRENT. Ide = Vdc = Vm = Im RL = TRL = TT Im = Vm RL "Idc = Im = 0,318 Im

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(3) RM3 CURRENT. Igms = \\ \frac{1}{2\Times \text{Im}^3 \text{Sin^2odo}} = \[\frac{1}{2\Times \text{Im}^3 \text{Sin^2odo}} \] $= \int \frac{Im^2}{2\pi} \int \frac{dt}{t} \left(1 - \cos 2\theta\right) d\theta$ $=\int \underline{Im^2} \left[\left(0 \right)_0^{T} - \left(\underline{Sein^20} \right)_0^{T} \right]$ $= \left| \frac{Im}{4\pi} \left[\frac{1}{100} - \frac{1}{4} \right] \right| = \frac{Im^2}{2}$ (4) RIPPLE FACTOR The output voltage (or word auxent) of a redifiee consists of 2 components namely dicicomponent and aici component. The aic component present in the output es Called sipple. The sipple, more offective will Smaller the sipple, more offective will be the rectifier ~= The rms value of a.c. component of sutput voltage The dic. component of 0/p voltage = In (rms) = Vr(2ms) Ide. IRMS = \Idc+ Izems = \Idc+I2ems

Idc. Irms Idc Irjams

IRMS = SI+ (Indms)2 Squaring both sides. $\frac{f_{\lambda ms}}{f^2 dc} = 1 + \left(\frac{f_{\lambda/\lambda ms}}{f_{\lambda/\lambda ms}}\right)^2$ $\frac{f_{\lambda/\lambda ms}}{f_{\lambda/\lambda ms}} = \left(\frac{f_{\lambda/\lambda ms}}{f_{\lambda/\lambda ms}}\right)^2 - \left(\frac{f_{\lambda/\lambda ms}}{f_{\lambda/\lambda ms}}\right)^2$ $= \left(\frac{f_{\lambda/\lambda ms}}{f_{\lambda/\lambda ms}}\right)^2 - \left(\frac{f_{\lambda/\lambda ms}}{f_{\lambda/\lambda ms}}\right)^2$ $\gamma = \frac{1}{1} \frac{1}{1}$ This indicates that the amount of a.c. Component present in the output of a half wave sectifier is 121% of dic. output voltage satio of sectification (5) Efficiency or satio n = d.c. power delivered to load aic input power from kansformer secondary = Pdc = Idc RL

Pac = Idc RL

(Rf+RL) $=\frac{(Im)^{R}}{(Im)^{R}}RL = \frac{4}{7}(Rf+RL)$ $=\frac{4}{7}(Rf+RL)$ $=\frac{4}{7}(Rf+RL)$

12 (FL+1) = 42 as Rf (1) as (5) =01406 (6) Peak Inverse voltage is called. The marriage reverse voltage is called Peak Driveise voltage (PIV) During - ve half apple when the didde is severse biased, the manimum value of voltage comming across the diode is called Peak Threise voltage. PIV = Vm. (7) Form factor = & ms value = Im/2 = T/2 average value = Im/1 = 1,57. Advantages of Half Wave redifier 1. Uses only one diode, 2 circuit es very easy to design 3. No centre top on transformer. Disadvantages (1) High sipple factor (1,21) (2) Very low efficiency (40,16%)