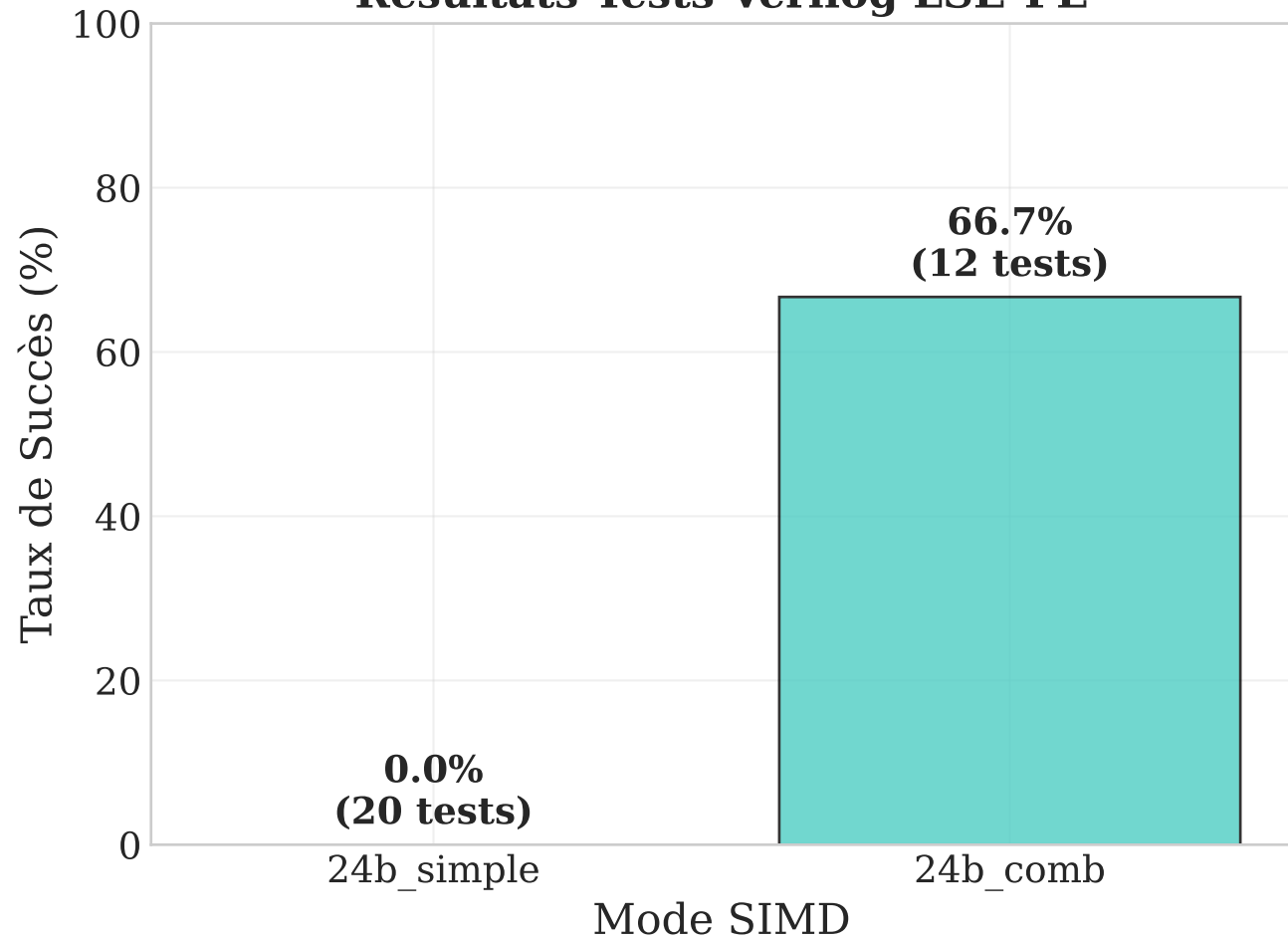


Résultats Tests Verilog LSE-PE



Distribution des Erreurs Verilog

