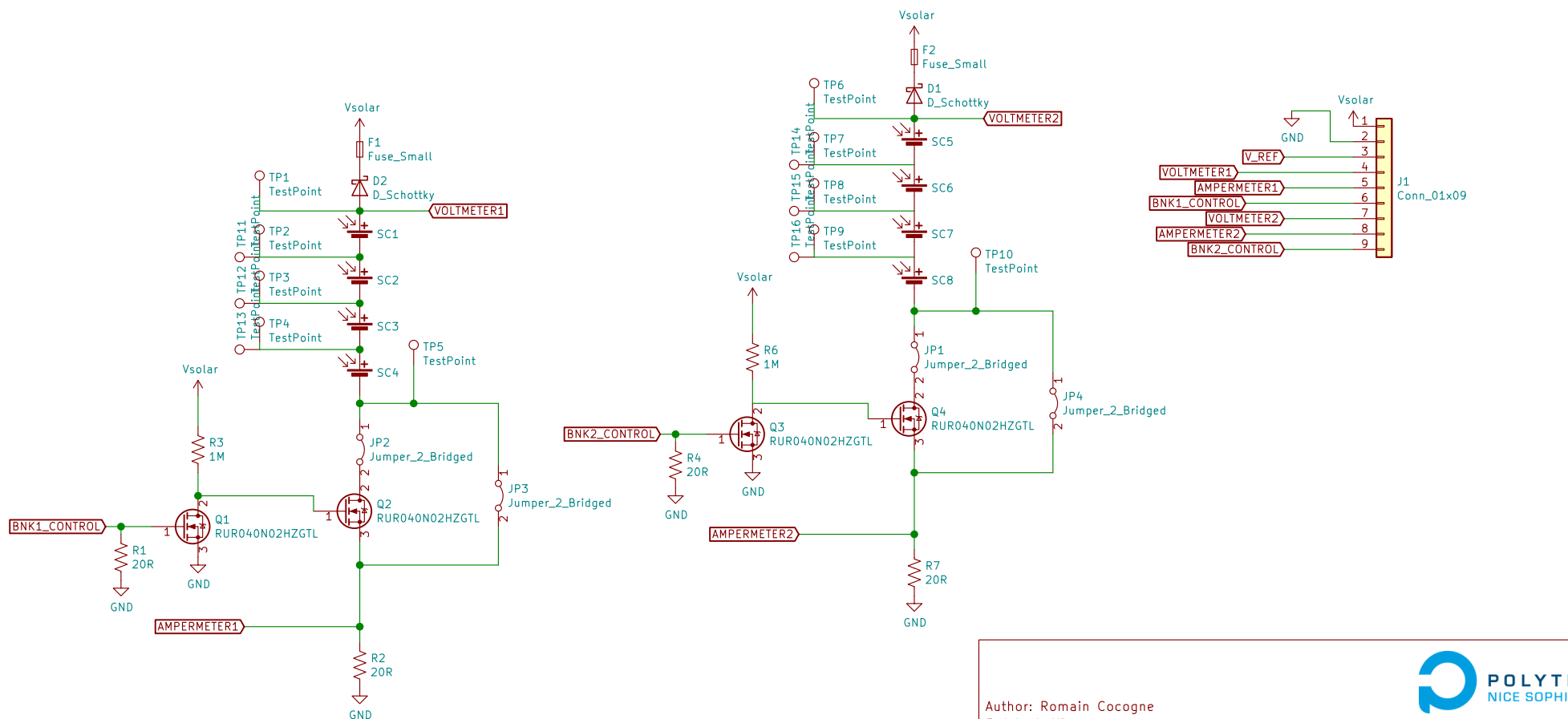


logic 0 or 1 -> 0V or 3.3V  
 0V -> NMOS1 off -> PMOS2 grid short circuited -> PMOS2 on  
 3.3V -> NMOS1 on -> PMOS2 grid to Vsolar -> PMOS2 off



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Sheet: /  
 File: 1\_solar\_bank.sch

**Title: Prototype 1**

Size: A4 Date: 2020-05-23

KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

**Rev: V1**

Id: 1/1

