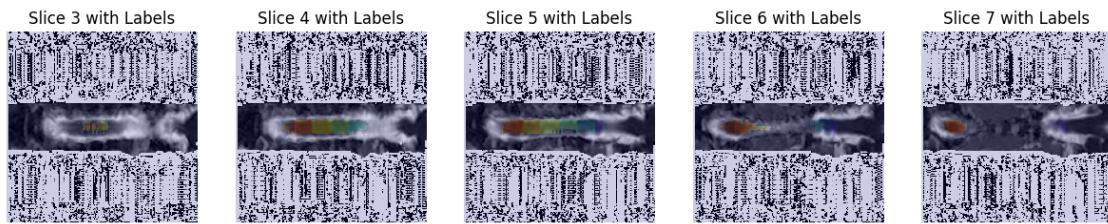


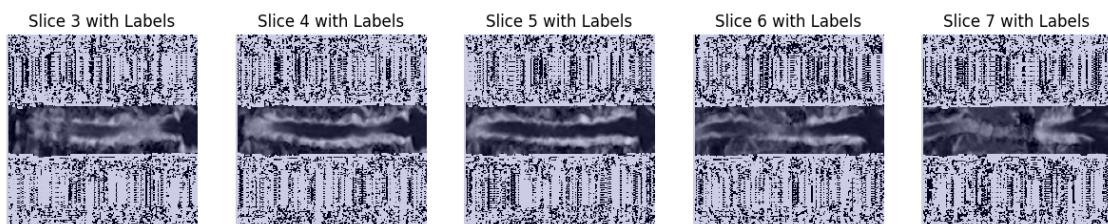
CINOCIS_CQ

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Processing sub-001 - ses-M60



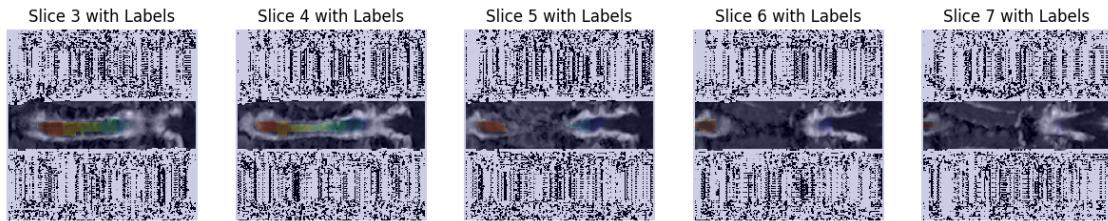
Processing sub-002 - ses-M60



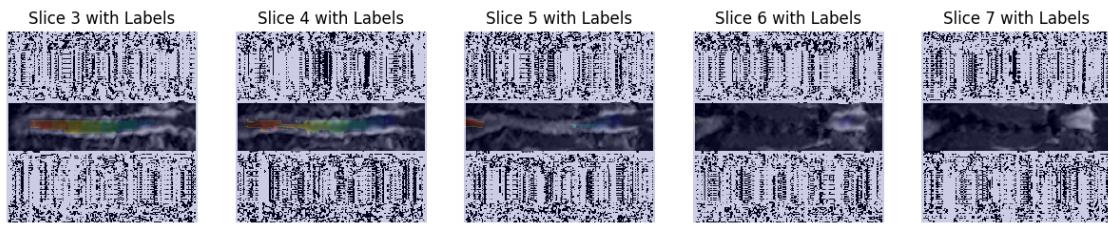
Processing sub-003 - ses-M3



Processing sub-003 - ses-M60



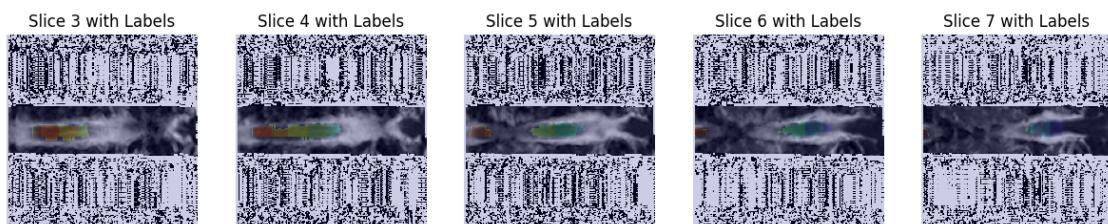
Processing sub-004 - ses-M3



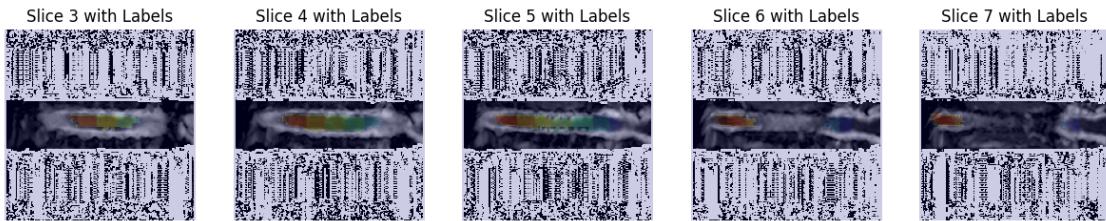
Processing sub-005 - ses-M3



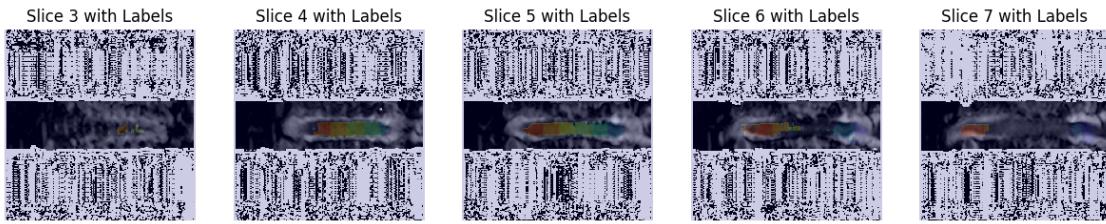
Processing sub-005 - ses-M60



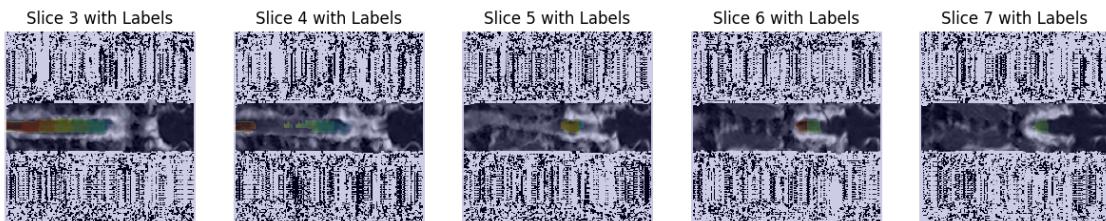
Processing sub-006 - ses-M3



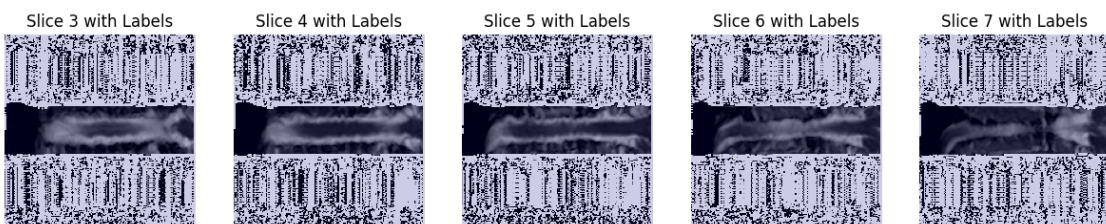
Processing sub-007 - ses-M3



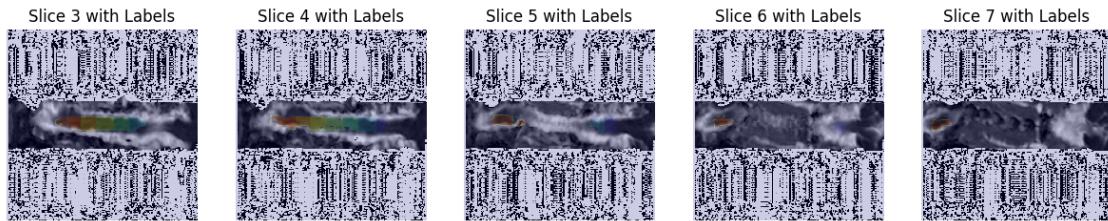
Processing sub-007 - ses-M60



Processing sub-008 - ses-M3

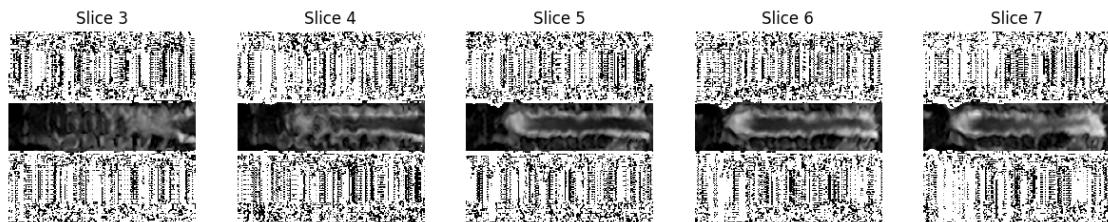


Processing sub-008 - ses-M60

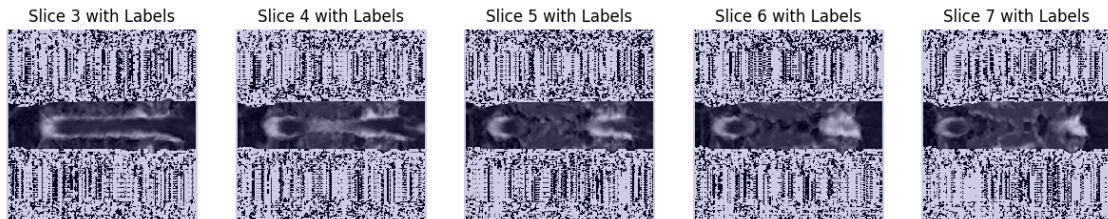


Processing sub-009 - ses-M3

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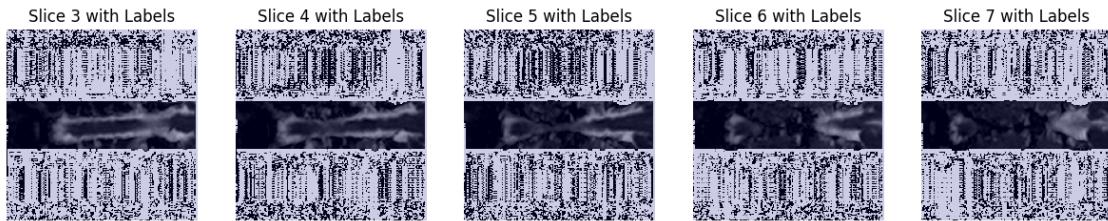
Processing sub-009 - ses-M60



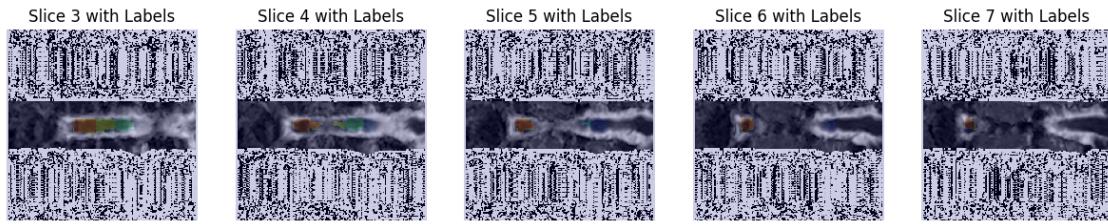
Processing sub-010 - ses-M3



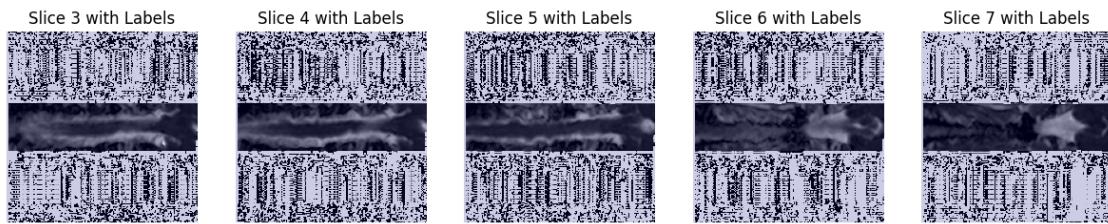
Processing sub-011 - ses-M3



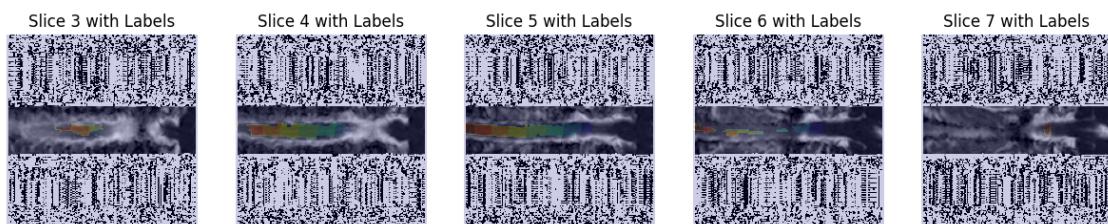
Processing sub-011 - ses-M60



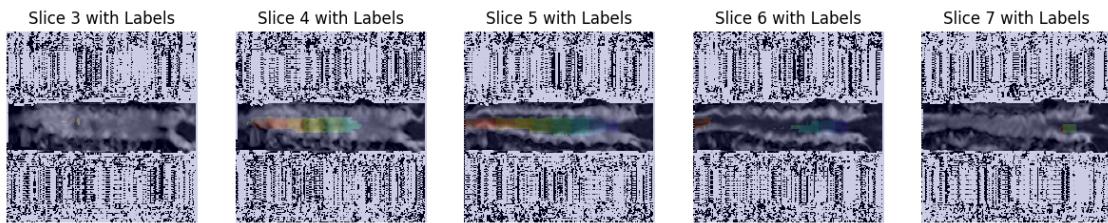
Processing sub-012 - ses-M3



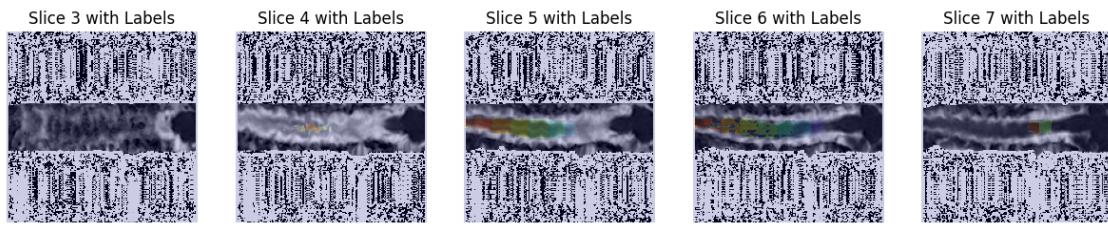
Processing sub-012 - ses-M60



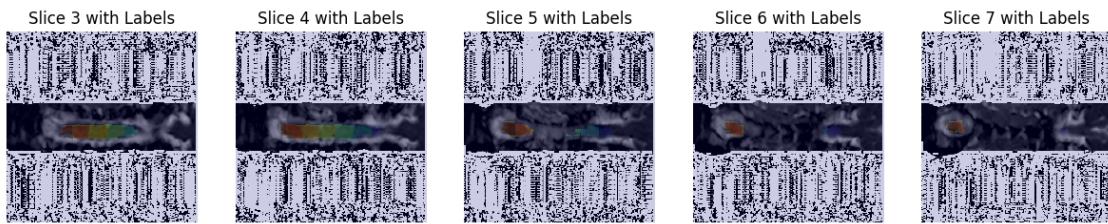
Processing sub-013 - ses-M3



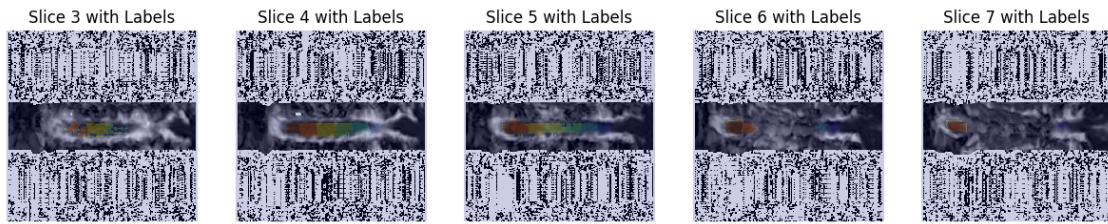
Processing sub-013 - ses-M60



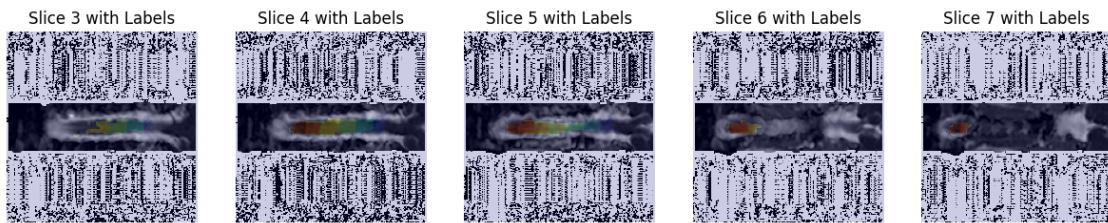
Processing sub-014 - ses-M3



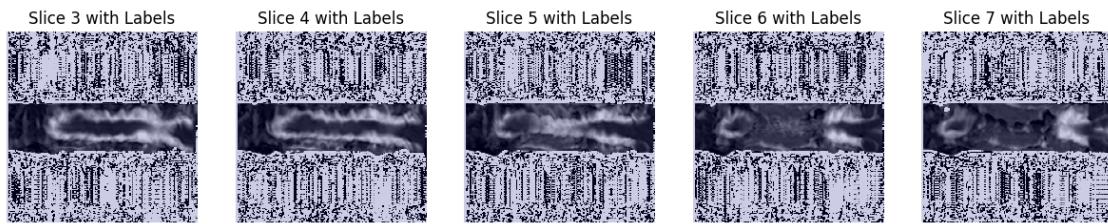
Processing sub-014 - ses-M60



Processing sub-015 - ses-M3



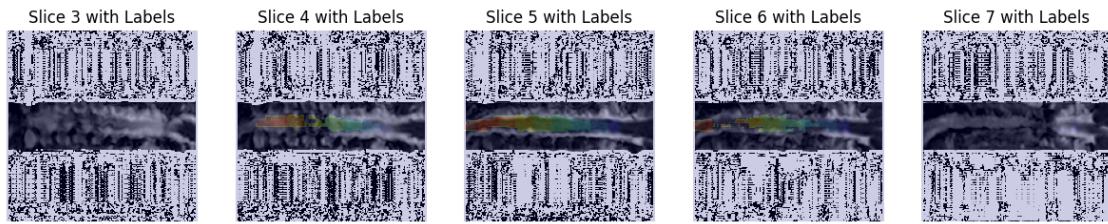
Processing sub-015 - ses-M60



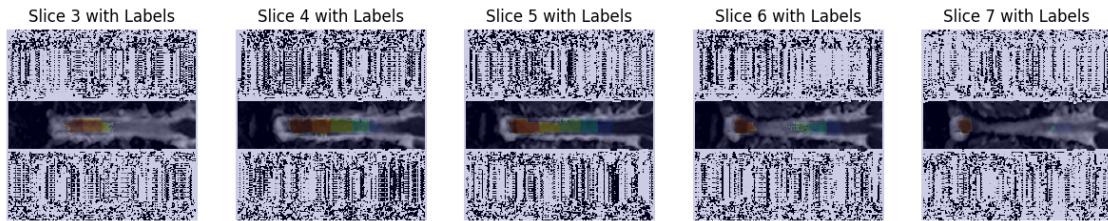
Processing sub-016 - ses-M3



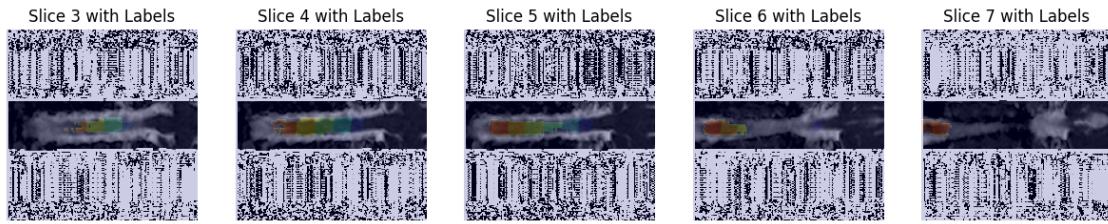
Processing sub-017 - ses-M3



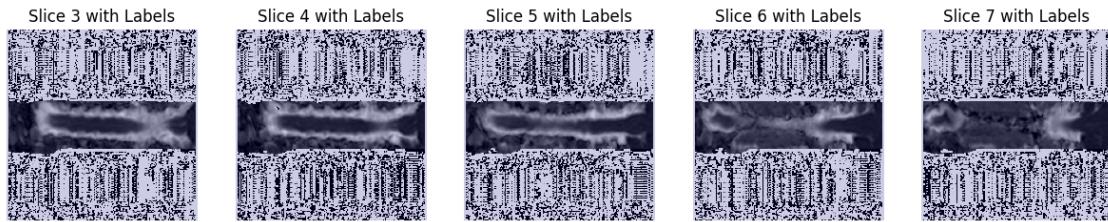
Processing sub-018 - ses-M3



Processing sub-019 - ses-M3

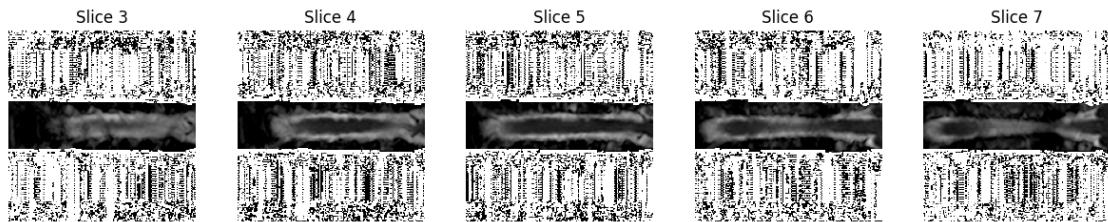


Processing sub-019 - ses-M60

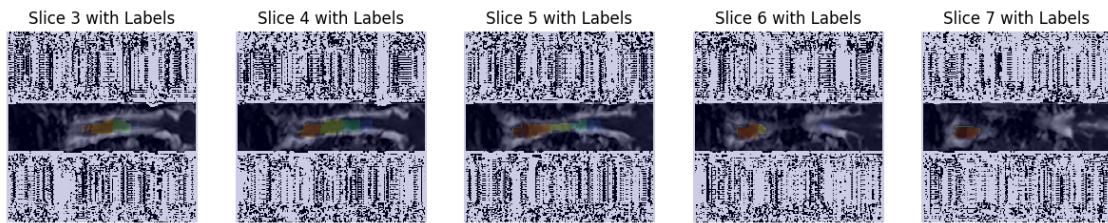


Processing sub-020 - ses-M3

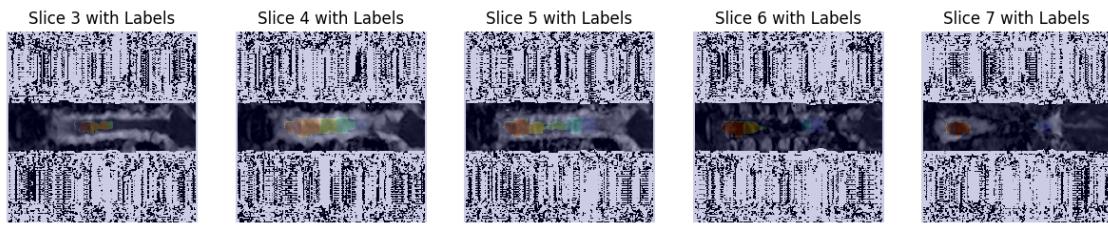
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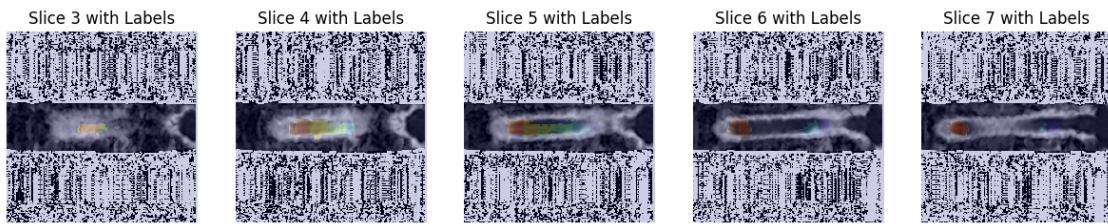
Processing sub-021 - ses-M3



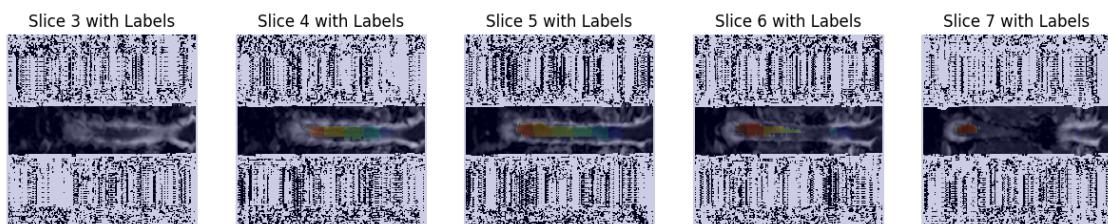
Processing sub-022 - ses-M3



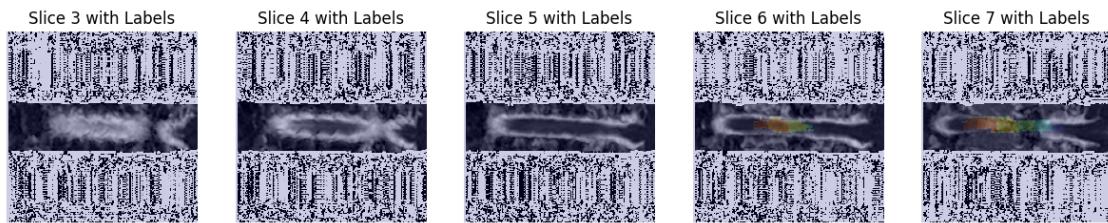
Processing sub-022 - ses-M60



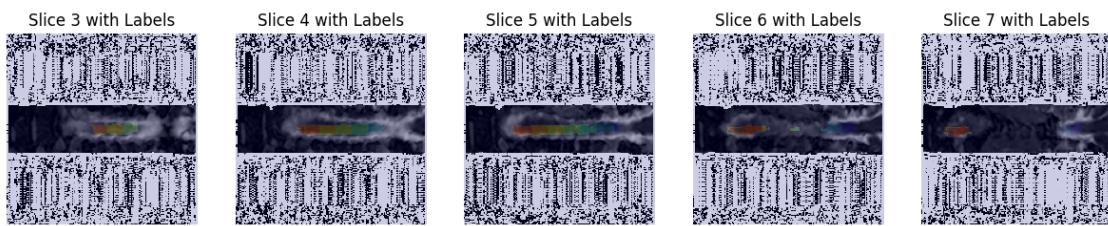
Processing sub-023 - ses-M3



Processing sub-023 - ses-M60



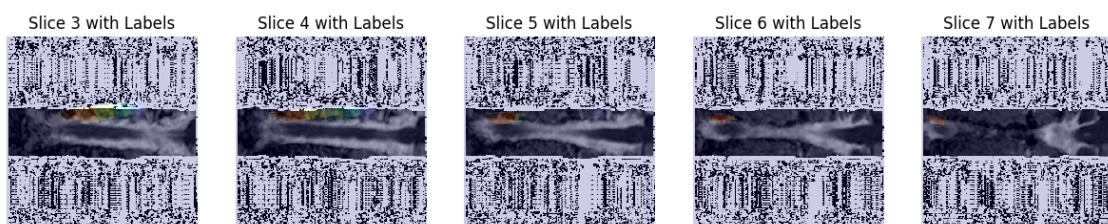
Processing sub-024 - ses-M3



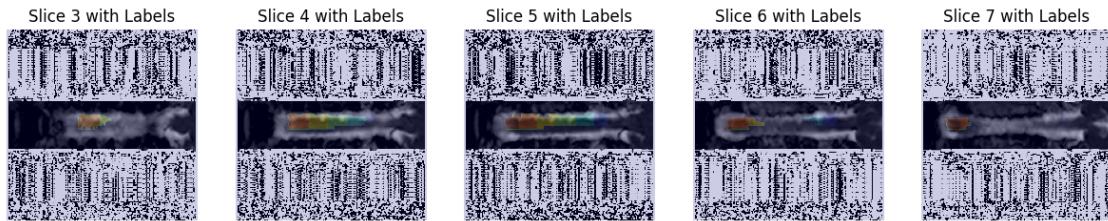
Processing sub-025 - ses-M3



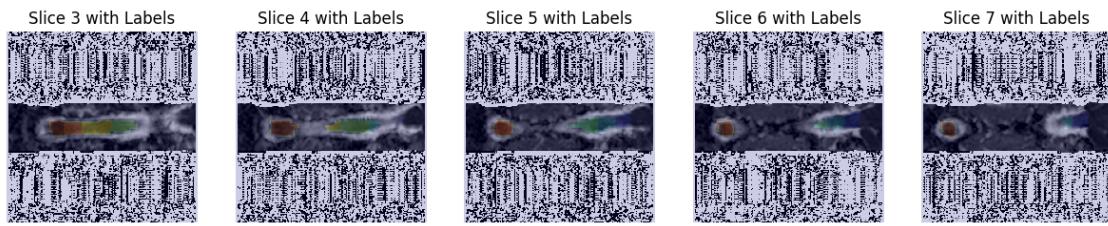
Processing sub-025 - ses-M60



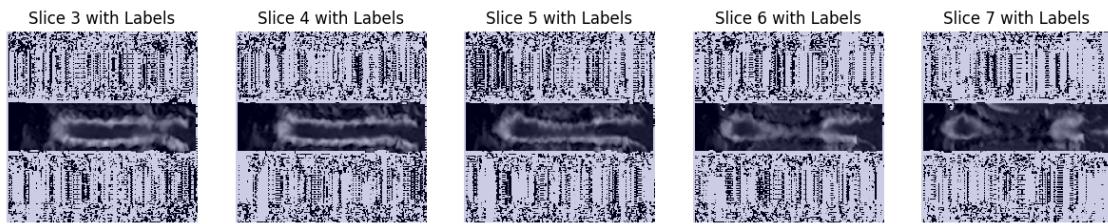
Processing sub-026 - ses-M3



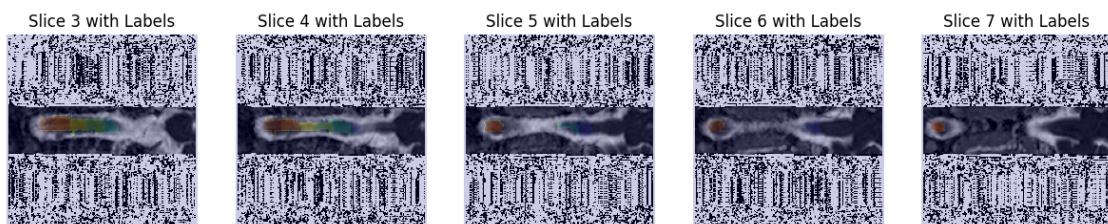
Processing sub-026 - ses-M60



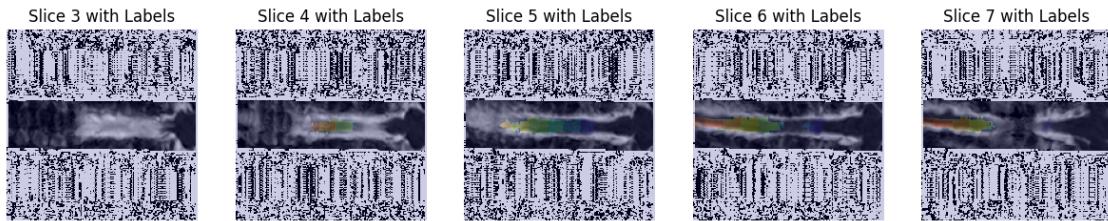
Processing sub-027 - ses-M3



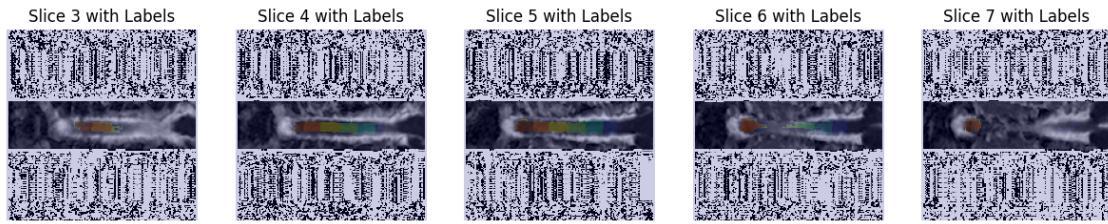
Processing sub-028 - ses-M60



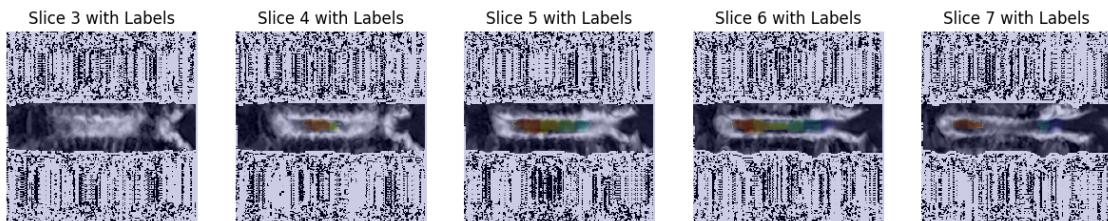
Processing sub-029 - ses-M60



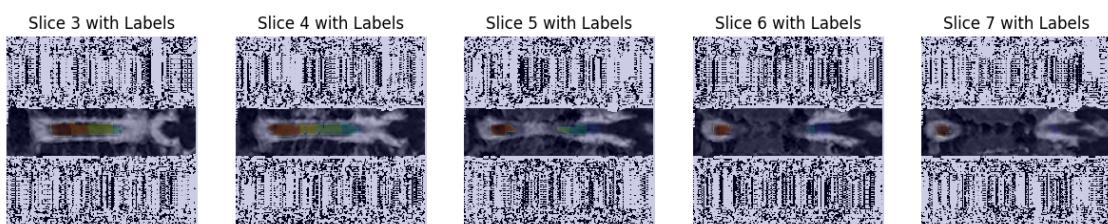
Processing sub-030 - ses-M3



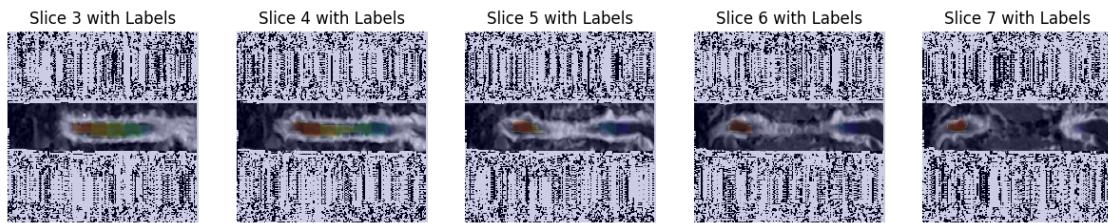
Processing sub-031 - ses-M3



Processing sub-031 - ses-M60



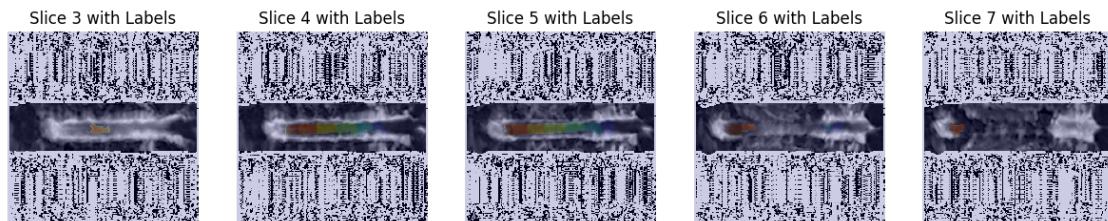
Processing sub-032 - ses-M3



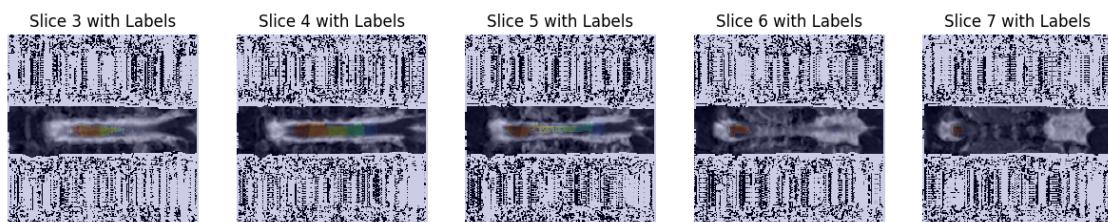
Processing sub-032 - ses-M60



Processing sub-033 - ses-M3

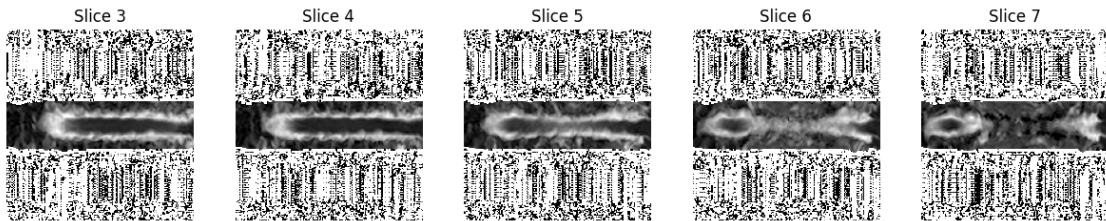


Processing sub-033 - ses-M60



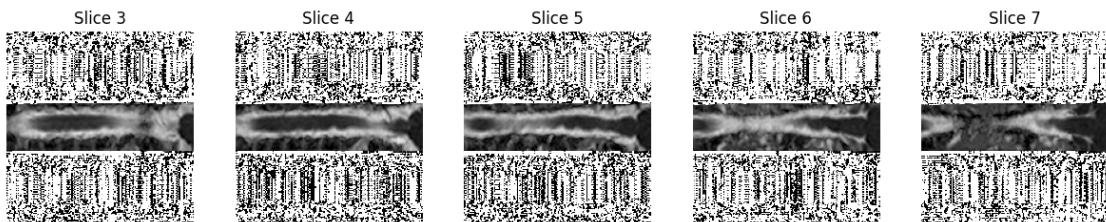
Processing sub-034 - ses-M3

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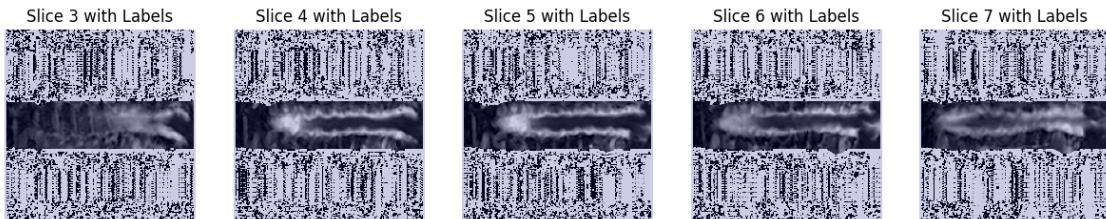


Processing sub-034 - ses-M60

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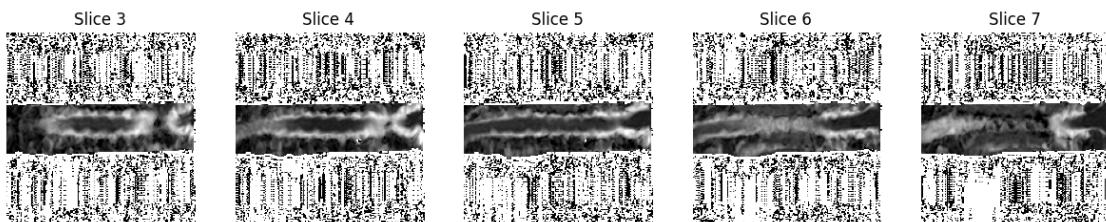


Processing sub-035 - ses-M3

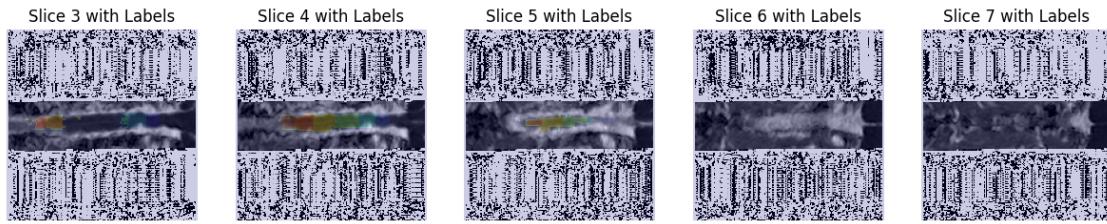


Processing sub-036 - ses-M3

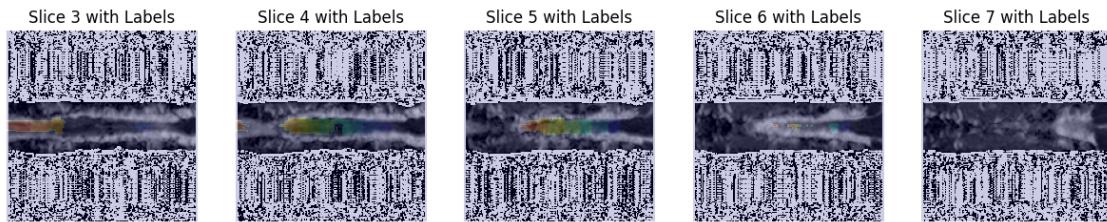
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Processing sub-037 - ses-M3



Processing sub-037 - ses-M60

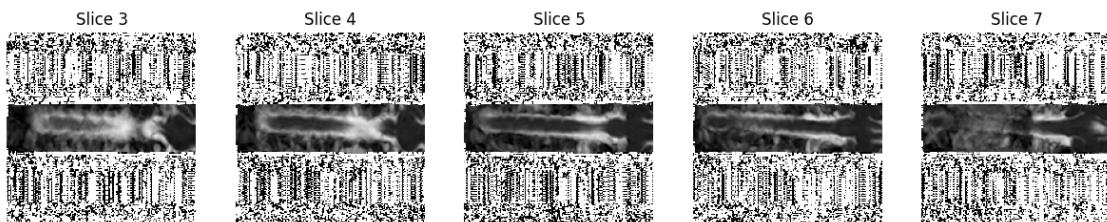


Processing sub-038 - ses-M3



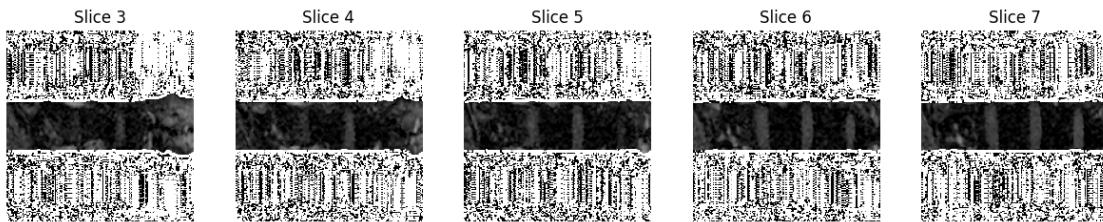
Processing sub-038 - ses-M60

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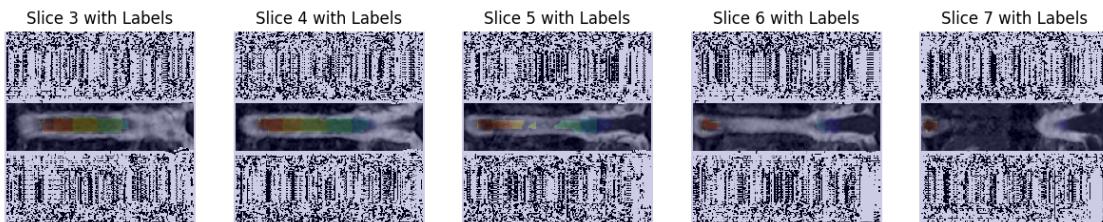


Processing sub-039 - ses-M3

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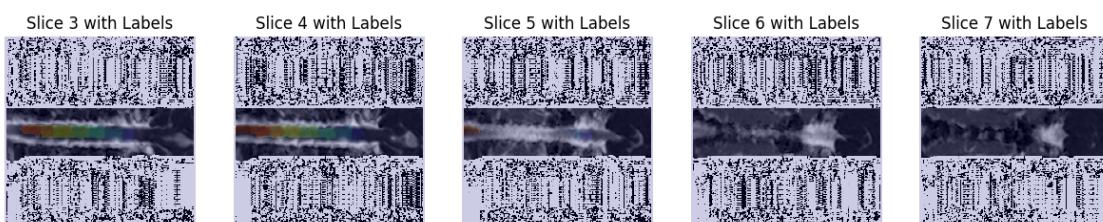
Processing sub-039 - ses-M60



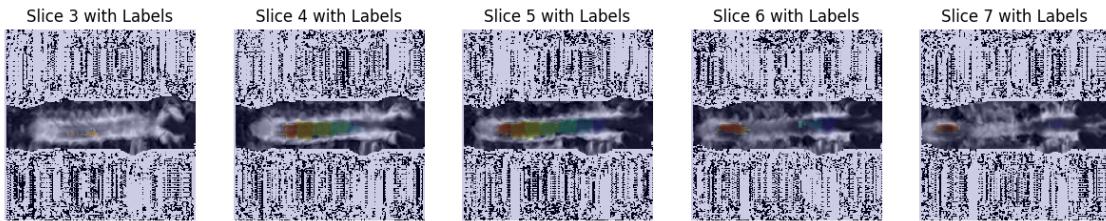
Processing sub-040 - ses-M3



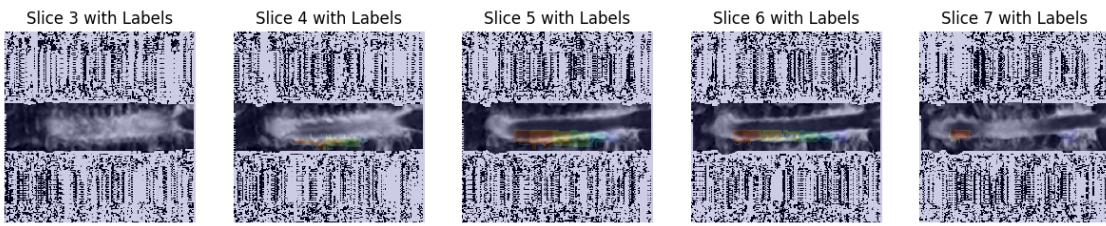
Processing sub-041 - ses-M3



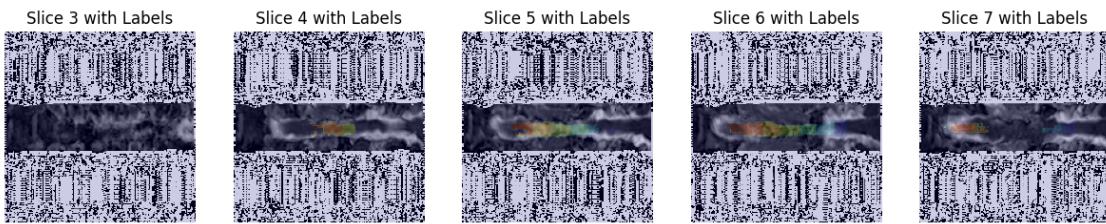
Processing sub-042 - ses-M3



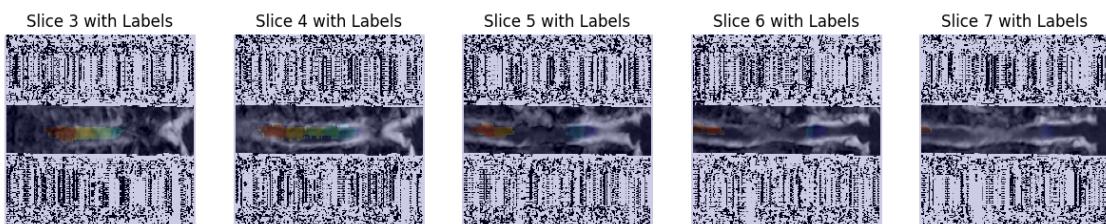
Processing sub-042 - ses-M60



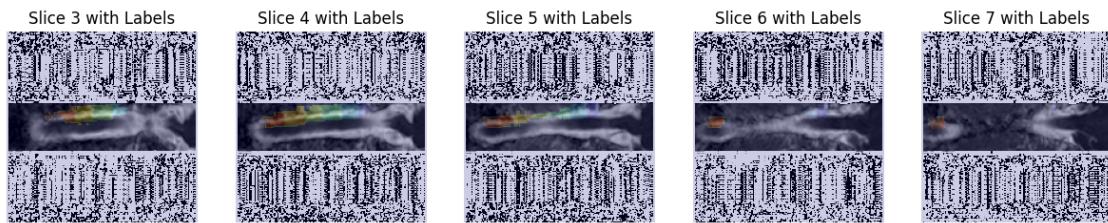
Processing sub-044 - ses-M3



Processing sub-044 - ses-M60

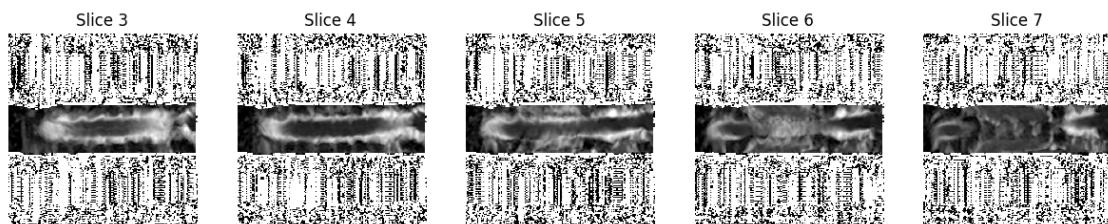


Processing sub-045 - ses-M60

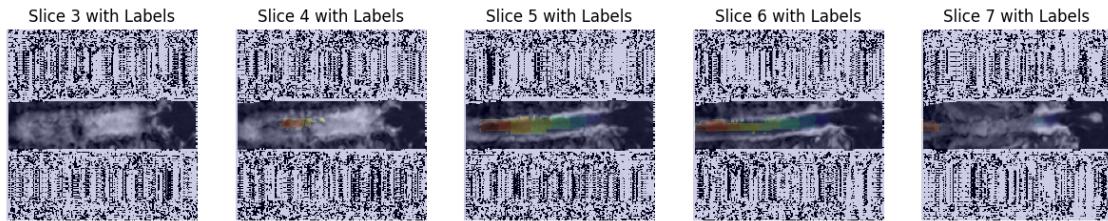


Processing sub-046 - ses-M3

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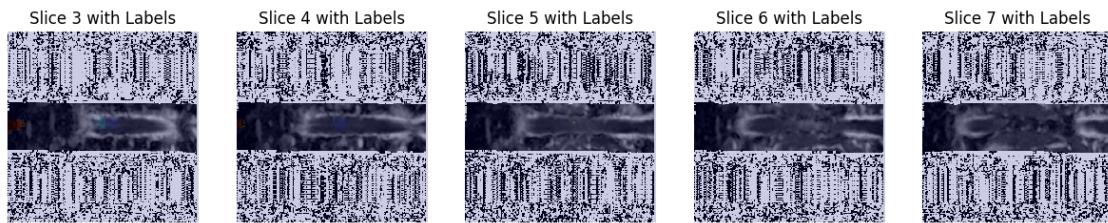
Processing sub-046 - ses-M60



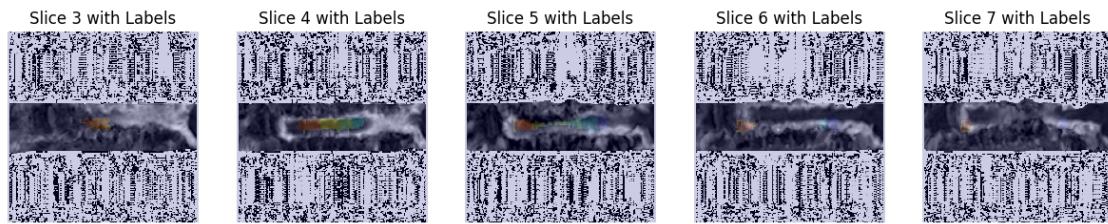
Processing sub-048 - ses-M3



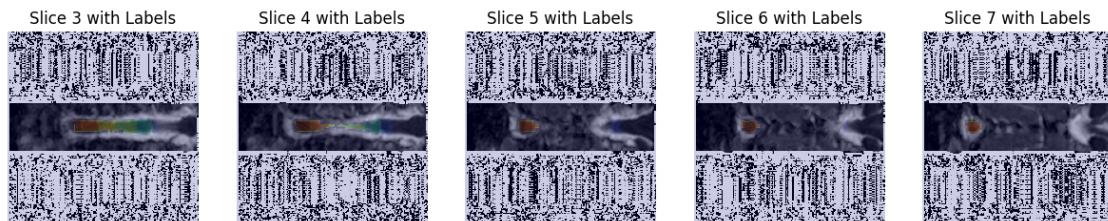
Processing sub-048 - ses-M60



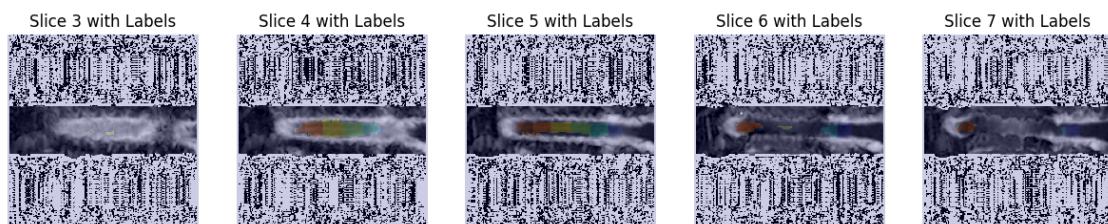
Processing sub-049 - ses-M3



Processing sub-049 - ses-M60



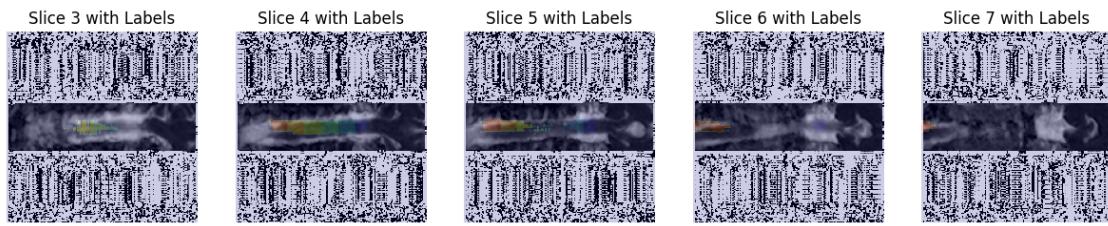
Processing sub-050 - ses-M3



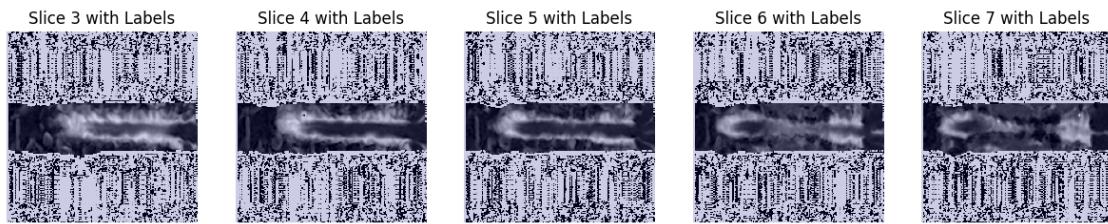
Processing sub-050 - ses-M60



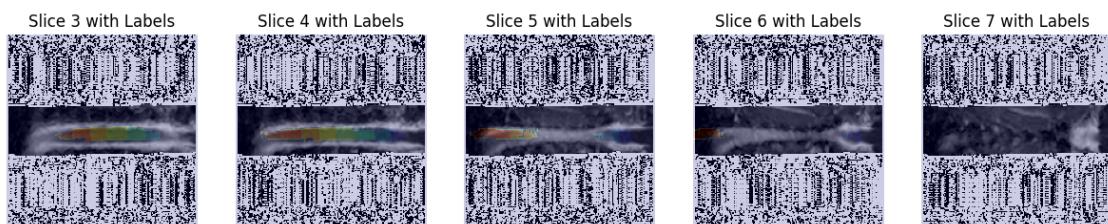
Processing sub-051 - ses-M60



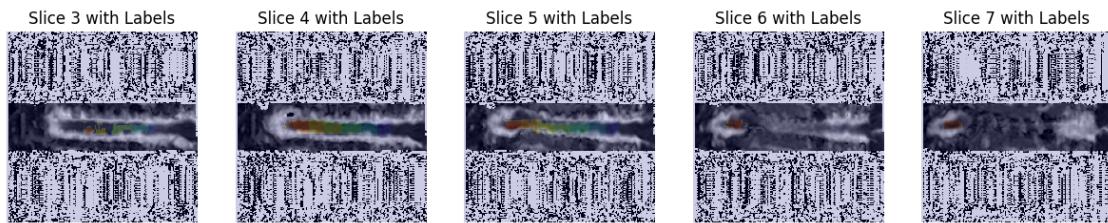
Processing sub-053 - ses-M3



Processing sub-053 - ses-M60

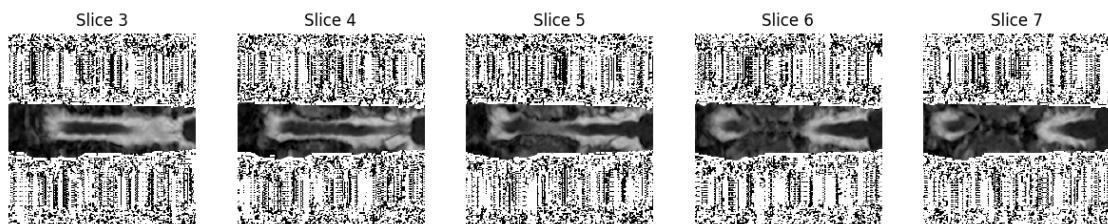


Processing sub-054 - ses-M3

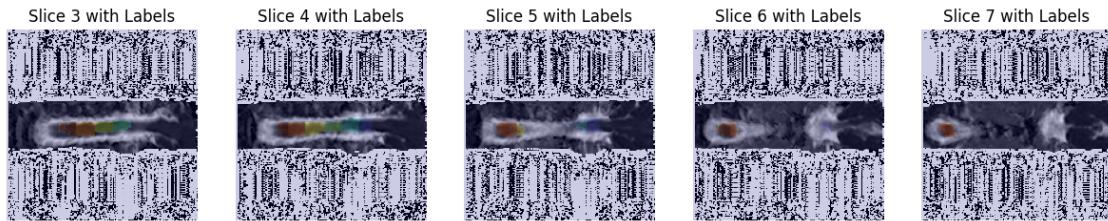


Processing sub-054 - ses-M60

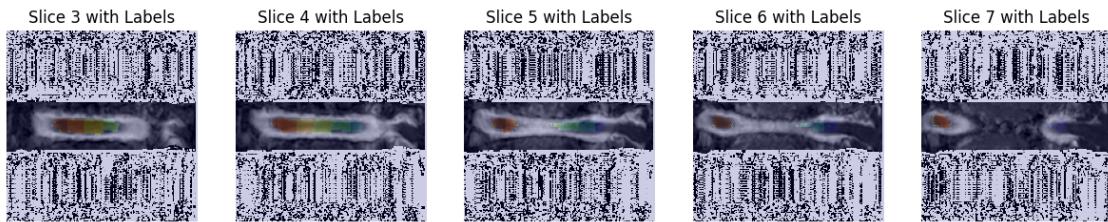
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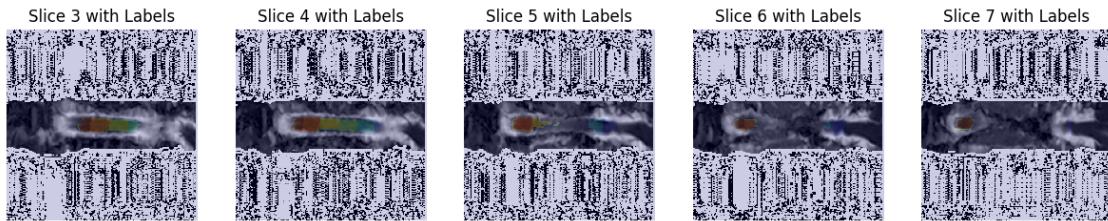
Processing sub-056 - ses-M3



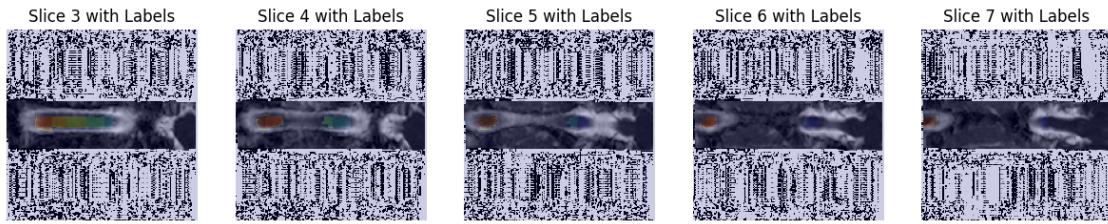
Processing sub-056 - ses-M60



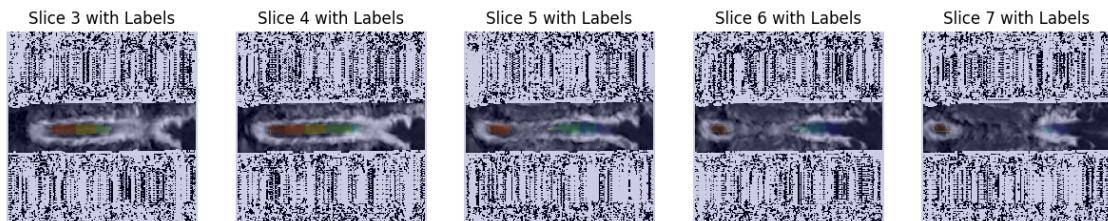
Processing sub-058 - ses-M3



Processing sub-058 - ses-M60



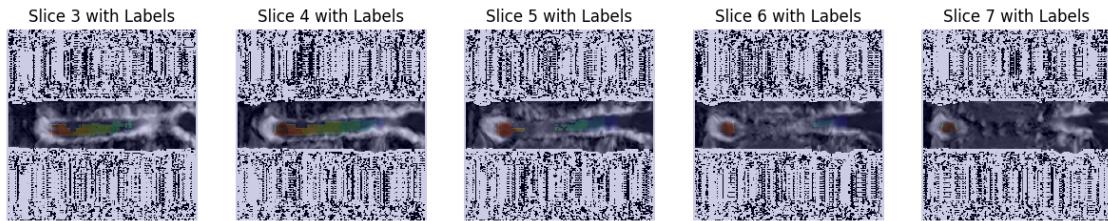
Processing sub-059 - ses-M3



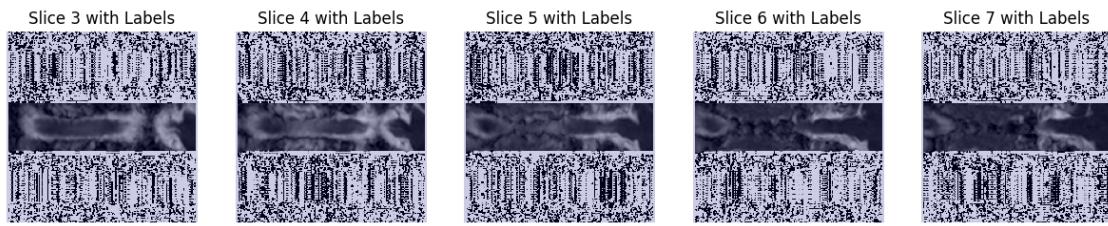
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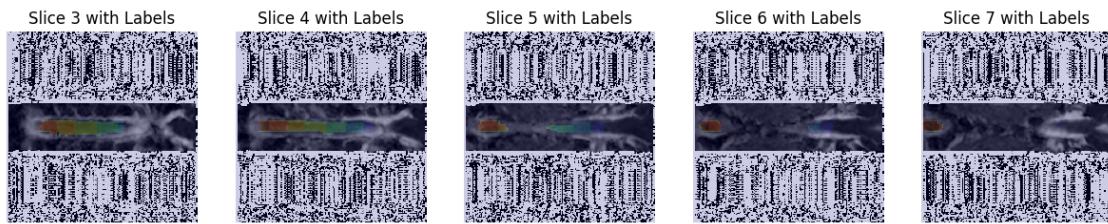
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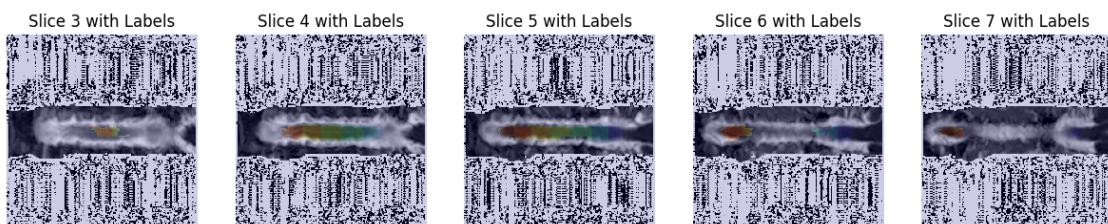
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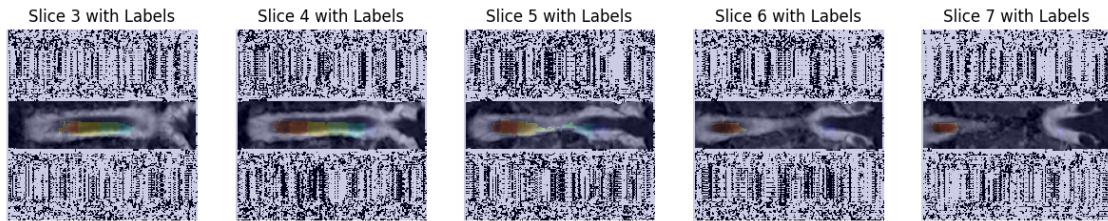
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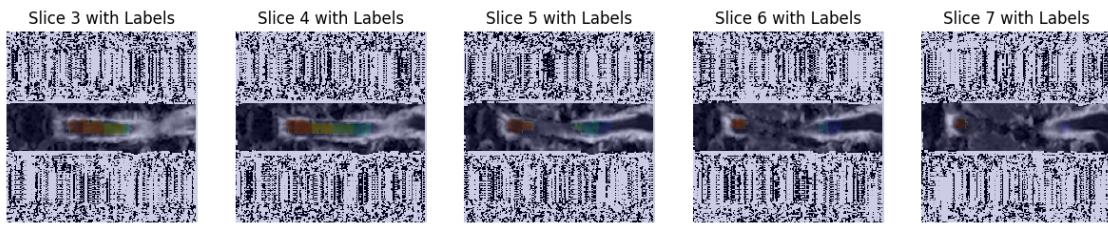
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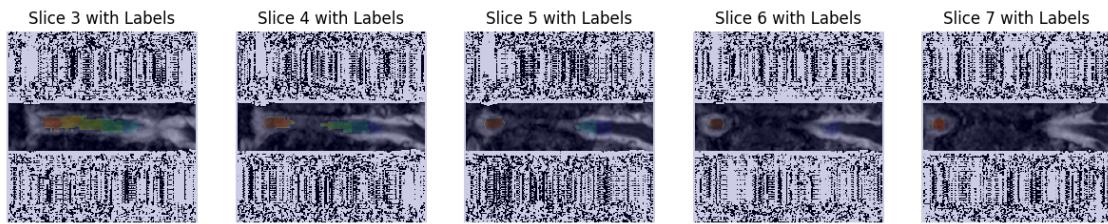
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Processing sub-065 – ses-M3



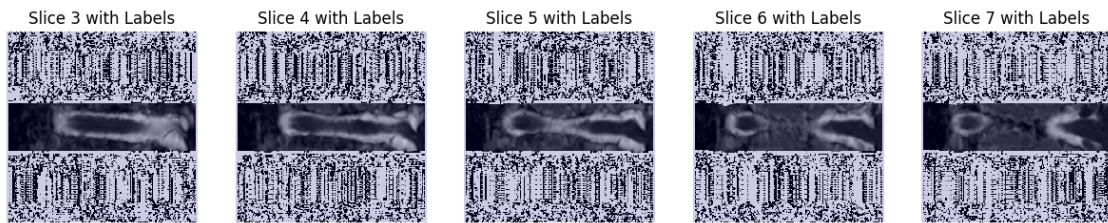
Processing sub-065 – ses-M60



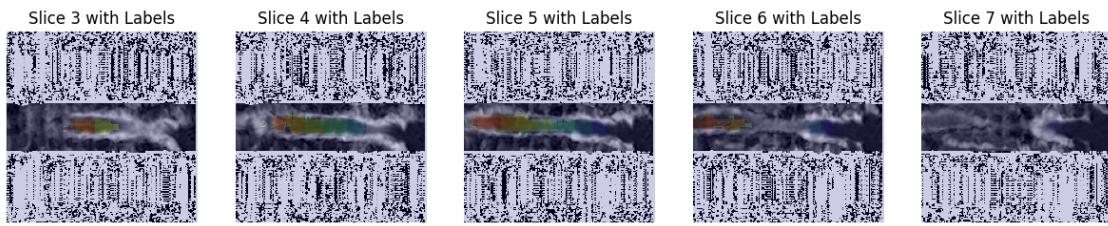
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Processing sub-066 – ses-M60



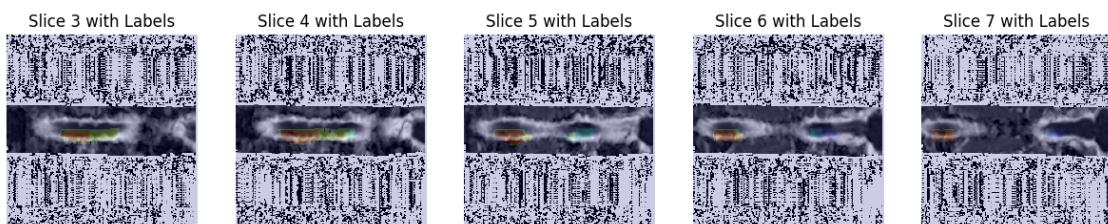
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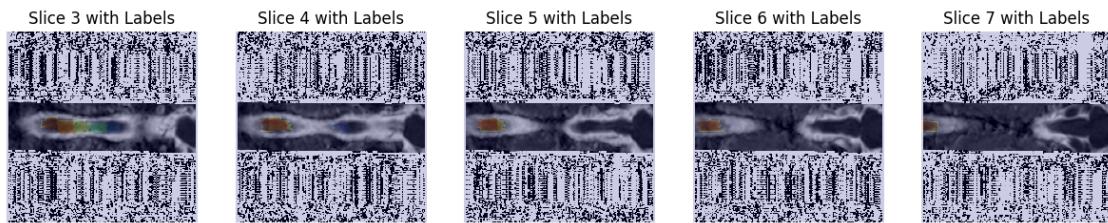
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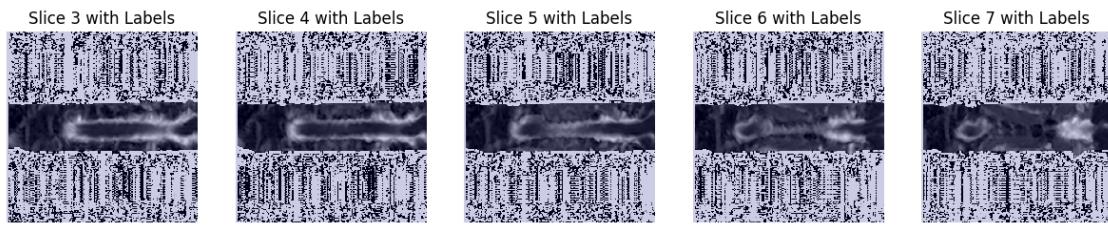
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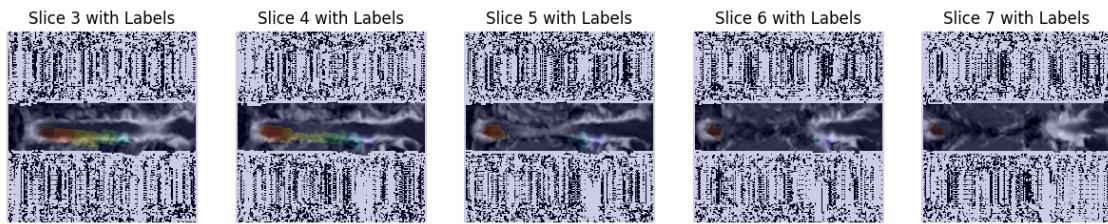
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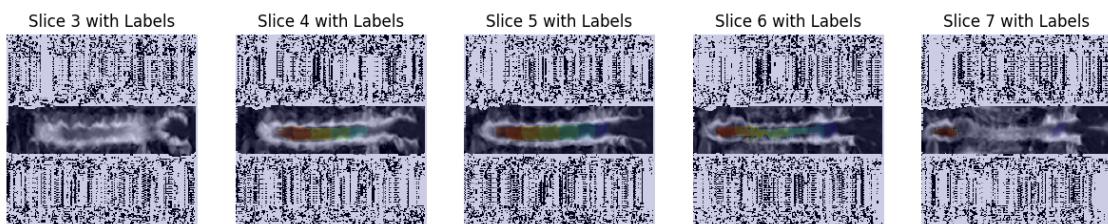
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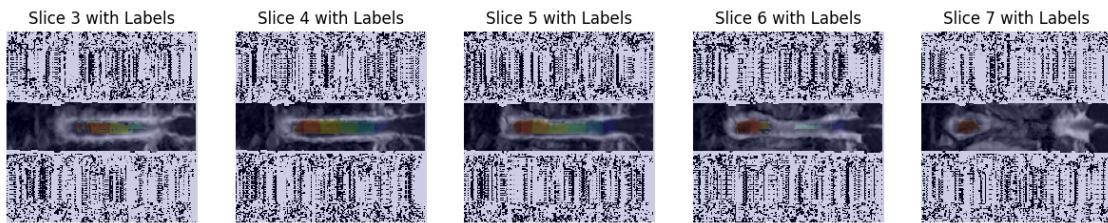
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Processing sub-071 - ses-M3



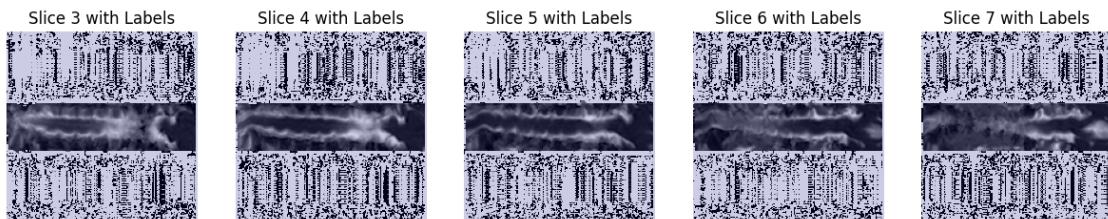
Processing sub-071 - ses-M60



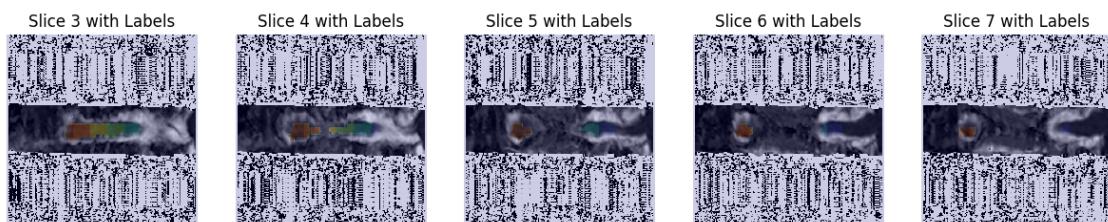
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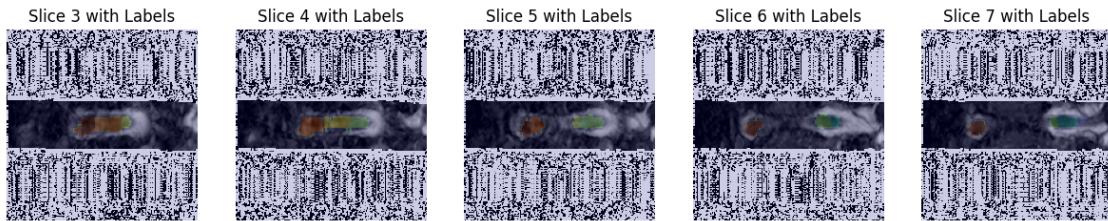
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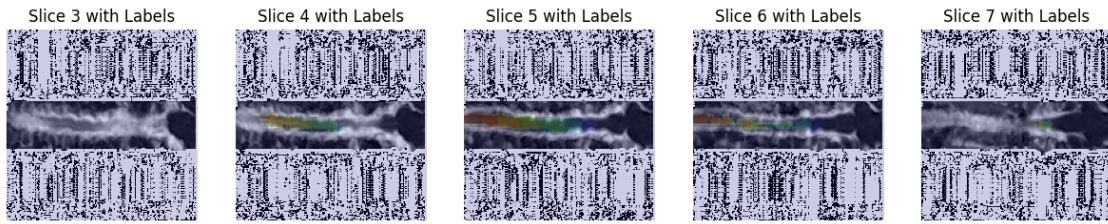
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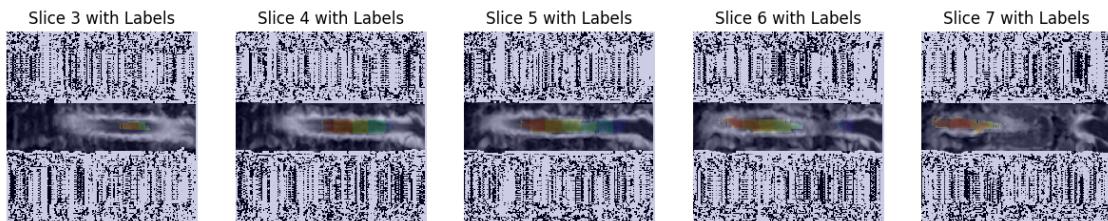
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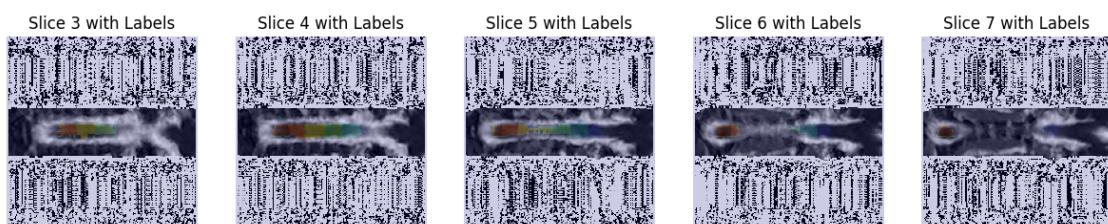
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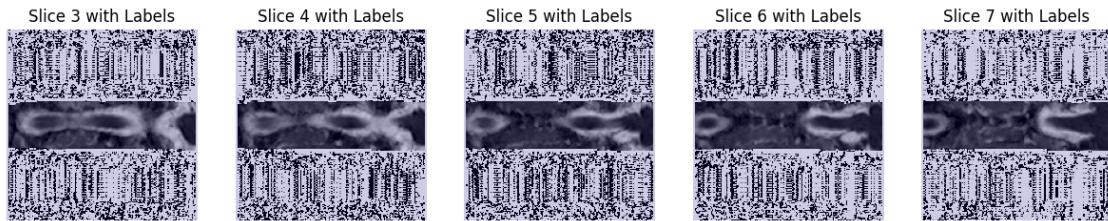
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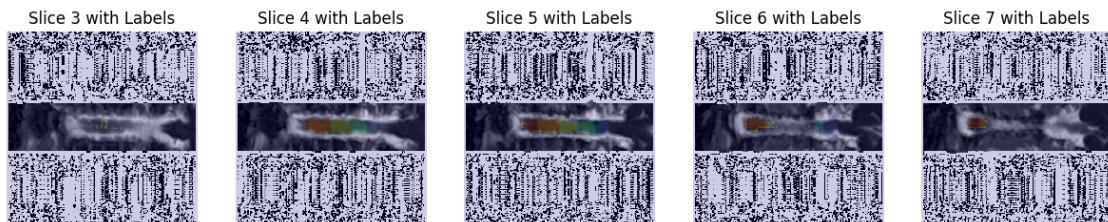
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Processing sub-076 - ses-M60

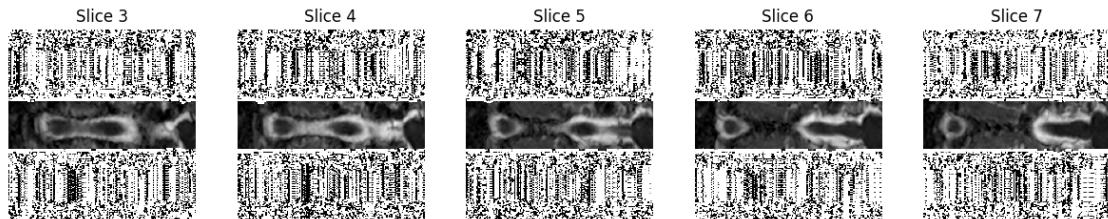


Processing sub-077 - ses-M3

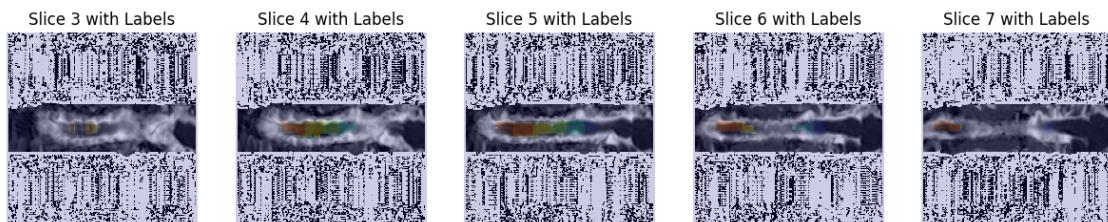


Processing sub-077 - ses-M60

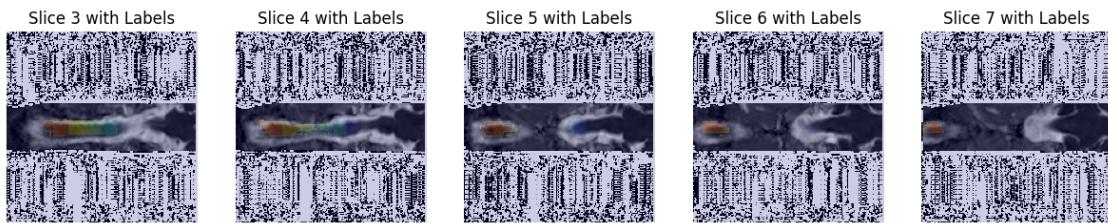
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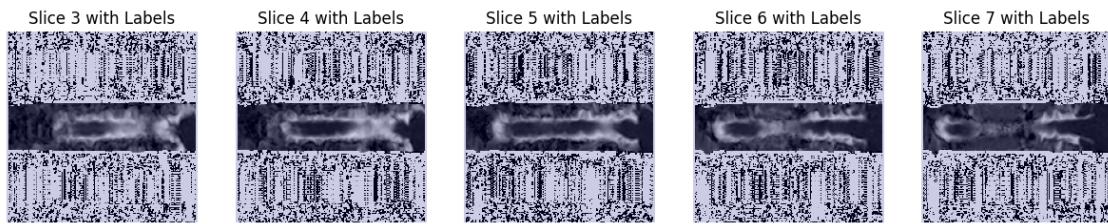
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Processing sub-078 - ses-M60

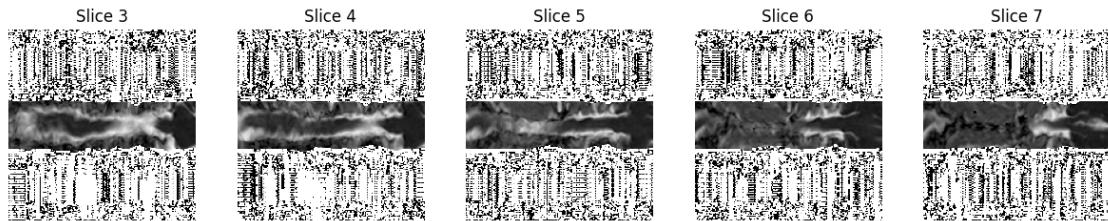


Processing sub-079 - ses-M3

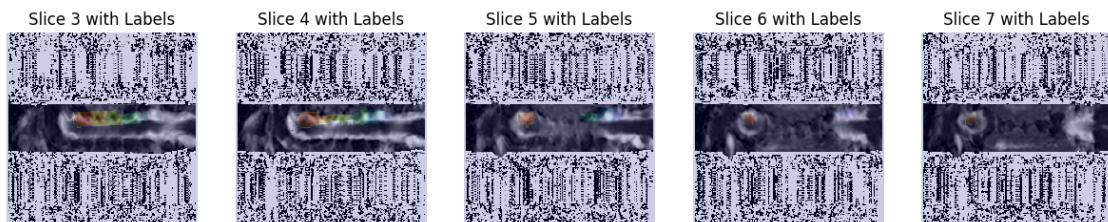


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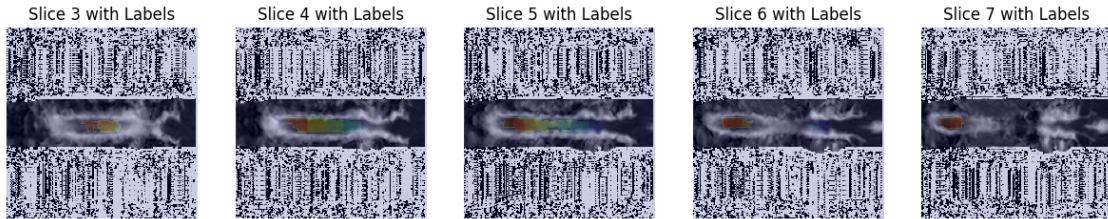
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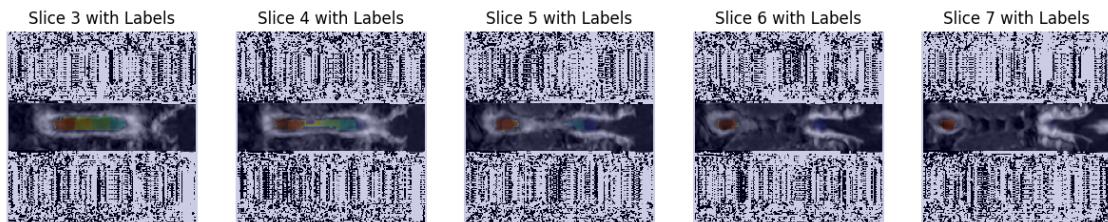
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Processing sub-082 - ses-M3

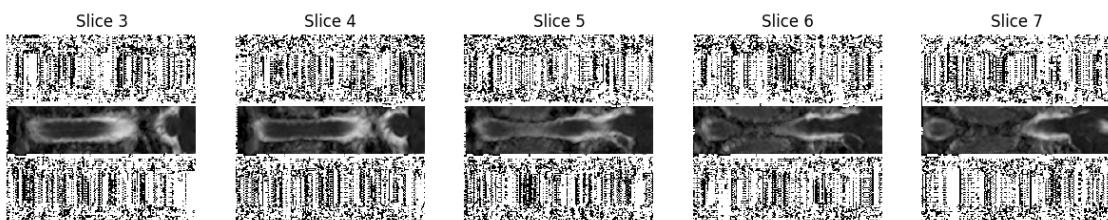


Processing sub-082 - ses-M60



Processing sub-083 - ses-M60

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