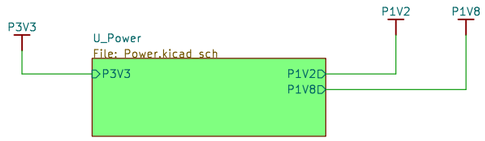
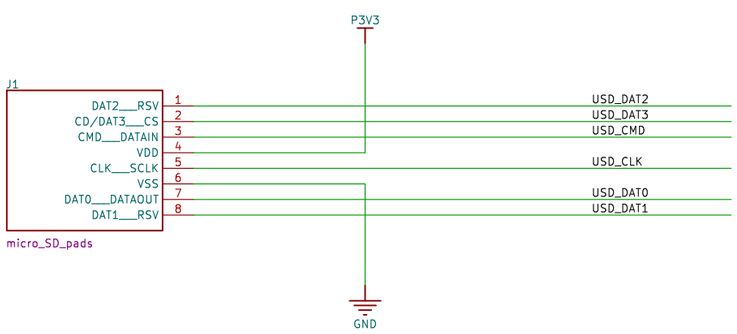
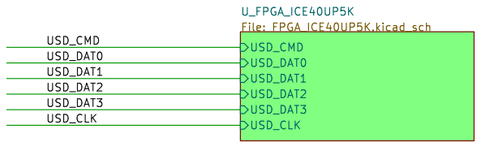
Power



MicroSD Pads



FPGA



Signaloid

PROJECT	Signaloid C0-microSD		A3
TITLE	**Root**		
REVISION	2.2	PAGE	1 of 3
AUTHOR	Signaloid	DATE	2024-11-06
FILE	C0-microSD.kicad_sch		

P1V2
R5
1
VCORE
C3
100nF
GND
P1V2
R6
100
VPLL
C1
4.7uF
C13
4.7uF
C14
4.7uF
C4
100nF
GND
GND
GND
GND
P3V3
VPP2V5
D1
SDM02M30LP3-7B
C5
100nF
GND
P3V3
C6
100nF
U1
C2 VCC
B2 VCCPLL
D4 VPP_2V5
A3 VCCIO_0
A1 IOT_36b
A2 IOT_37a
B3 IOT_46b_G0
A4 IOT_47a
A5 RGB0
B5 RGB1
C5 RGB2
B4 GND
E2 GND
BANK 0
BANK 1
BANK 2
USD_DAT0
USD_DAT1
USD_CLK
USD_CMD
LED_GREEN
LED_RED
TP_B0_RGB2
TP14
TP_C0.40
GND
iCE40UP5K-UWG30ITR
P1V8
P1V8
P1V8
C7
100nF
GND
R3
10k
C8
100nF
GND
R2
10k
D2 VCCIO1
F3 CRESET_B
C3 IOB_10a
F4 IOB_11b_G5
D3 IOB_12a_G4_CDONE
E3 IOB_13b
B1 IOB_24a
F2 IOB_25b_G3
F1 IOB_32a_SPI_SO
E1 IOB_33b_SPI_SI
D1 IOB_34a_SPI_SCK
C1 IOB_35b_SPI_SS
C4 VCCIO_2
E5 IOB_0a
D5 IOB_2a
F5 IOB_3b_G6
E4 IOB_9b
TP_B1_11B
CDONE
TP_B1_24A
TP_B1_25B
CONF_MOSI
CONF_MISO
CONF_SCLK
CONF_CS
TP_B2_0A
TP_B2_9B
USD_DAT2
USD_DAT3
P3V3
TP8
TP_C0.40
TP10
TP_C0.40
TP11
TP_C0.40
TP13
TP_C0.40
TP15
TP_C0.40
P3V3
R1
1.4k
LED_S
DS1
SML-P11MTT86R
LED_GREEN
set to approx. 1mA
P3V3
R8
1.62k
LED_R_S
DS2
SML-P11UTT86R
LED_RED
set to approx. 1mA
P1V8
C2
100nF
GND
R7
DNP
CONF_NC_A2
U2
B3 VCC
A2 NC_1
A5 NC_2
A6 NC_3
B1 NC_4
B6 NC_5
C1 NC_6
C6 NC_7
D1 NC_8
D6 NC_9
E1 NC_10
E6 NC_11
F2 NC_12
F5 NC_13
CONF_NC_A5
CONF_NC_A6
CONF_NC_B1
CONF_NC_B6
CONF_NC_C1
CONF_NC_C6
CONF_NC_D1
CONF_NC_D6
CONF_NC_E1
CONF_NC_E6
CONF_NC_F2
CONF_NC_F5
AT25QL128A-UUE-T
P1V8
R4
10k
B4 CS
D3 SCK
E3 I/O_0(SI)
C4 I/O1(SO)
D4 I/O_2(WP)
C3 I/O3(HOLD)
E4 GND
CONF_CS
CONF_SCLK
CONF_MOSI
CONF_MISO
GND
P1V8
TP1B1
TP_C0.80
CDONE
TP1
TP_C0.40
TP2B1
TP_C0.80
CRESET_B
TP2
TP_C0.40
TP3B1
TP_C0.80
CONF_CS
TP3
TP_C0.40
TP4B1
TP_C0.80
CONF_SCLK
TP4
TP_C0.40
TP5B1
TP_C0.80
CONF_MISO
TP5
TP_C0.40
TP6B1
TP_C0.80
CONF_MOSI
TP6
TP_C0.40

Signaloid

PROJECT	Signaloid C0-microSD		A3
TITLE	***iCE40 FPGA***		
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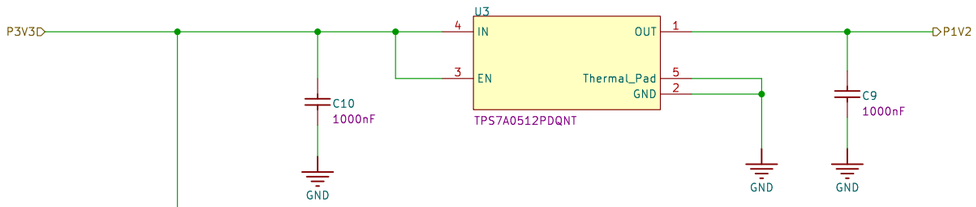
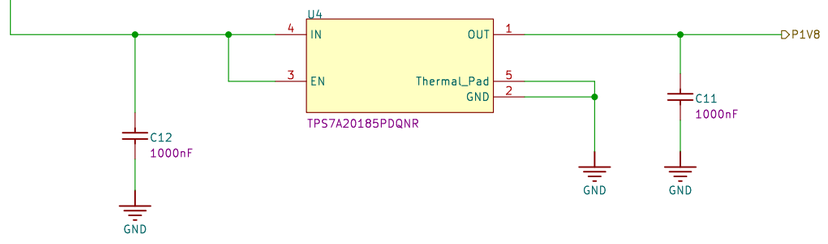
1V2 Regulator

FPGA Core



1V8 Regulator



Signaloid

PROJECT	Signaloid C0-microSD		A3
TITLE	**Power**		
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FILE	C0-microSD.kicad_sch		