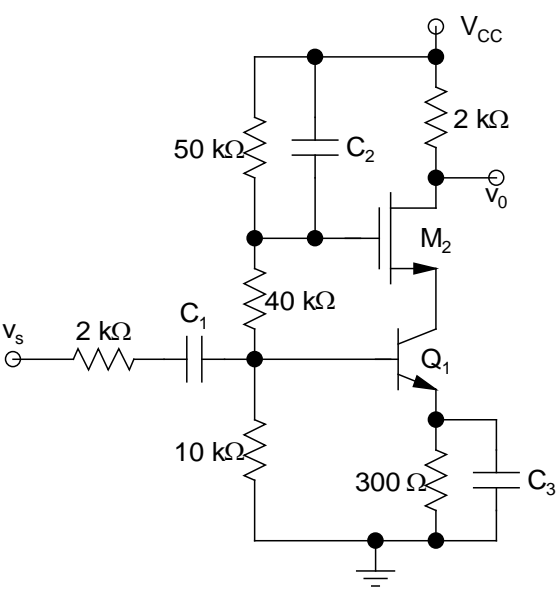


Neglect base current, Early effect, body effect, CLM effect.

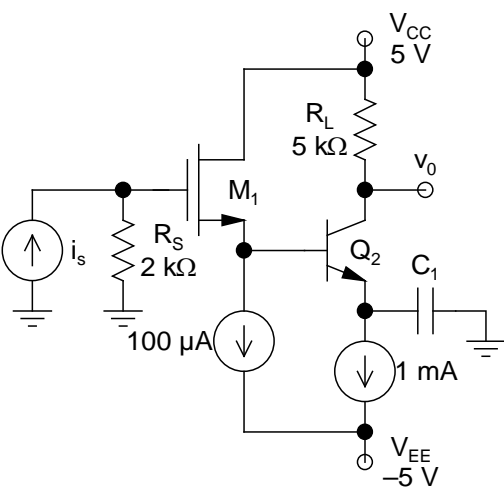
Data: For Q_1 : $r_{E1} = 26 \, \Omega$, $\beta_1 = 100$; for M_2 : $g_{m2} = 2 \, \text{mA/V}$.

- a) Determine the ac small-signal midband voltage gain (v_0/v_s). **2.5**
- b) Using IVTC method, and the technique of total capacitance minimization, choose the values of C_1 , C_2 , and C_3 , such that the tilt in the output v_0 for pulse excitation of frequency $1.57 \, \text{kHz}$, does not exceed 2%. **9.5**



C_1 is an extremely large value capacitor. 100 μA and 1 mA current sources are ideal. Neglect base current, Early effect, body effect, and CLM effect. Other data: for M_1 : $g_{m1} = 200 \mu\text{A/V}$, $C_{gs1} = 10 \text{ pF}$, $C_{gd1} = 1 \text{ pF}$, neglect C_{sb1} and C_{db1} ; for Q_2 : $\beta_2 = 200$, $C_{\pi 2} = 20 \text{ pF}$, $C_{\mu 2} = 2 \text{ pF}$.

- a) Evaluate the ac small-signal midband transresistance (v_0/i_s). 3
- b) Using ZVTC method, evaluate the fall time of v_0 for pulse response. 8
- c) Which capacitor is primarily responsible for this performance? Justify. 1



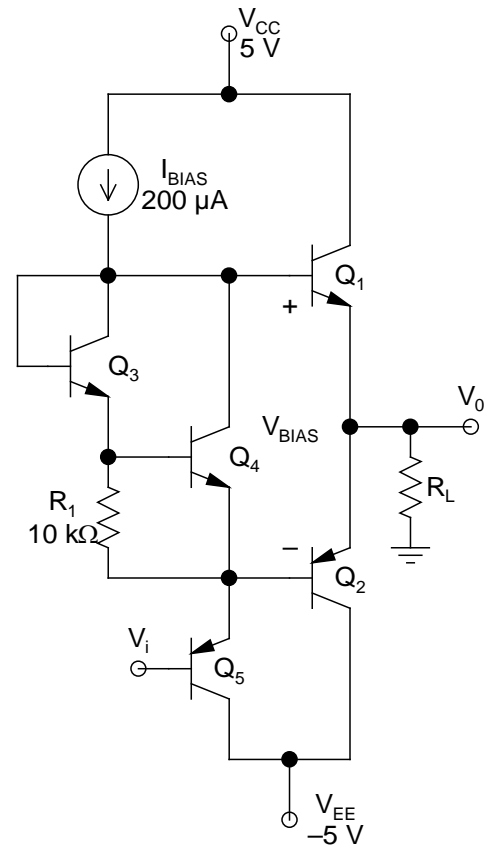
$I_{S3} = I_{S4} = 2 \text{ fA}$, $I_{S1} = I_{S2} = 10 \text{ fA}$. I_{BIAS} is ideal. All transistors have $\beta = 200$ (neglect any drop in the value of β at low current levels).

With R_L removed:

- Perform a self-consistent analysis, and find the split of I_{BIAS} in Q_3 and Q_4 . 3
- Evaluate V_{BIAS} . 2
- What should be the DC offset of V_i to ensure that the DC offset of V_0 is zero? 1
- If V_i is a $\pm 4 \text{ V}$ sinusoid superimposed on the DC offset calculated in part c), what's the peak-to-peak swing of V_0 ? Justify your answer. 2
- What is the *total* standby power dissipation? 1

With $R_L = 1 \text{ k}\Omega$:

- Determine the power conversion efficiency. 3



**Department of Electrical Engineering
Indian Institute of Technology, Kanpur**

EE 210

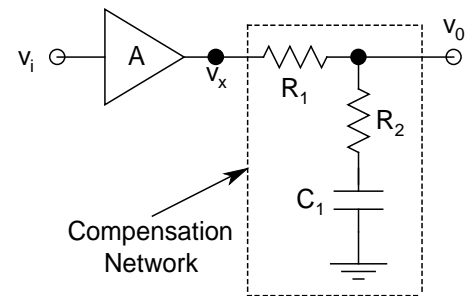
END-SEM P4

6.5.21

- a) A system has low-frequency gain of 50 dB, and two negative real poles, having pole frequencies at 1 Mrad/sec and 100 Mrad/sec.
- i) What is its unity-gain bandwidth? **2**
 - ii) If it is used in a negative feedback configuration, determine the feedback factor (f), at which the poles would *just* start to have imaginary components. **2**
 - iii) If f is increased beyond the value calculated in part ii), comment on the stability of the system, giving clear physical justification(s). **3**
- b) Under unity negative feedback, the loop gain characteristic of a 3-pole (all negative and real) system is found to cross 0 dB at 2 MHz, with the total phase at this point equal to -120° .
- i) What is the phase margin of the system? **1**
 - ii) Is the gain margin positive or negative? Justify. **1**
 - iii) Based on the answers of parts i) and ii), comment on the stability of the system. **1**
 - iv) If the locations of the second and third pole frequencies are 3 MHz and 5 MHz respectively, find the location of the first pole frequency. **2**

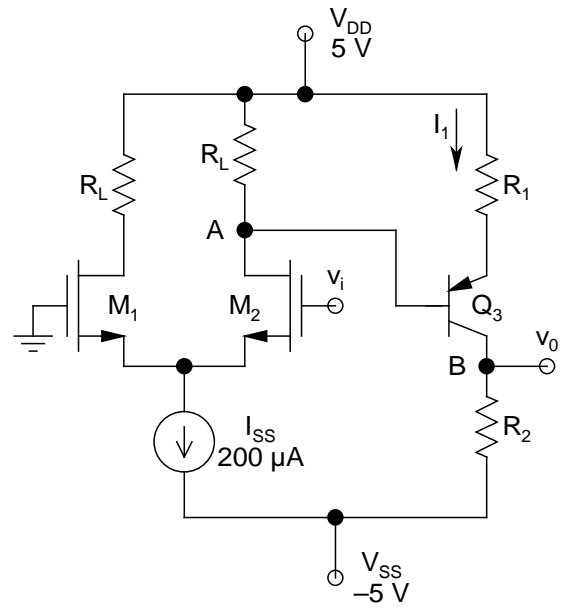
The amplifier block A has low-frequency gain $|v_x/v_i| = 90$ dB, and three negative real poles at frequencies 5 MHz, 25 MHz, and 500 MHz.

- a) Derive the transfer function v_0/v_x , and show that the compensation network (within the dotted line) actually performs pole-zero compensation (PZC). What are the expressions for the pole and zero frequencies (in terms of R_1 , R_2 , and C_1)? **4**
- b) Now, for unconditional stability, evaluate the required values of R_2 and C_1 , if $R_1 = 10$ k Ω . **4**
- c) What is the phase margin of the compensated system? **2**
- d) What modification would you do to this compensation network, for it to perform dominant pole compensation (DPC) instead of PZC? Elaborate your answer. **2**

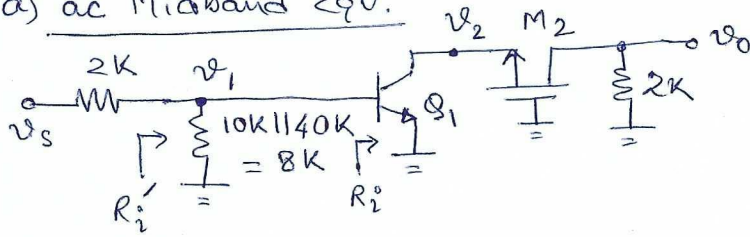


M_1 - M_2 perfectly matched. I_{SS} ideal. Neglect base current, body effect, CLM effect, and Early effect.
Other data: For M_1 - M_2 : $V_{TN0} = 1$ V, $k'_N = 40 \mu\text{A}/\text{V}^2$, $W/L = 80$; for Q_3 : $\beta = 100$.

- a) Choose R_L , R_1 , and R_2 , such that the DC levels of the voltages at nodes A and B are 2.3 V and 0 V respectively, and $I_1 = 100 \mu\text{A}$ (DC). 3
- b) What is the voltage dropped across I_{SS} ? 3
- c) Using the results of part a), find the ac small-signal midband voltage gain v_0/v_i . 6



a) ac Midband Egu.



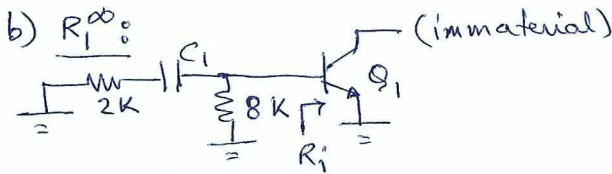
$$r_{E1} = 26\Omega, r_{\pi1} = 2.6K, g_{m2} = 2mA/V$$

$$\frac{v_o}{v_2} = +g_{m2} \times 2K = \underline{+4}$$

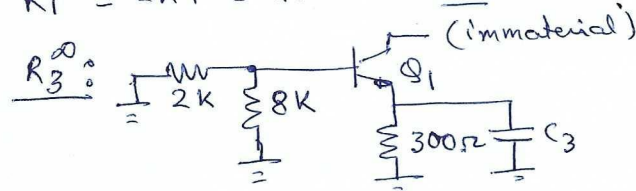
$$\frac{v_2}{v_1} = -\frac{1/g_{m2}}{r_{E1}} = \underline{-19.23}$$

$$R_i = r_{\pi1} = \underline{2.6K} \quad R_i' = 8K \parallel 2.6K = \underline{2K} \Rightarrow \frac{v_1}{v_s} = \frac{R_i'}{R_i' + 2K} = \underline{0.5}$$

$$\Rightarrow \boxed{\frac{v_o}{v_s} = -38.46}$$



$$R_1^\infty = 2K + 8K \parallel 2.6K = \underline{4K}$$



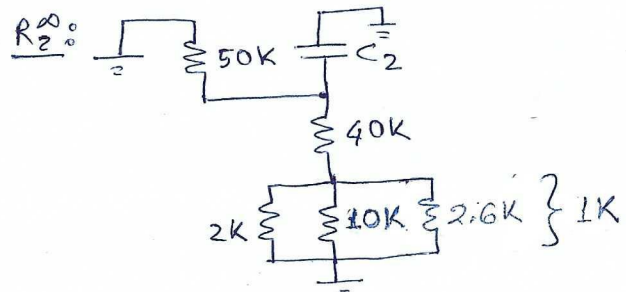
$$R_3^\infty = 300 \parallel \left[r_{E1} + \frac{2K \parallel 8K}{\beta + 1} \right] = \underline{36.7\Omega}$$

$$P = \frac{\pi f_L}{f} \times 100 = 2 \text{ for } f = 1.57 KHz \Rightarrow f_L = \underline{10Hz} = \sqrt{f_d^2 + 2 \left(\frac{f_d}{10} \right)^2} \Rightarrow f_d = \underline{9.9Hz}$$

C_3 sees the least resistance \Rightarrow To make ΣC minimum, choose C_3 to contribute f_d , & C_1 & C_2 each contribute $f_d/10$.

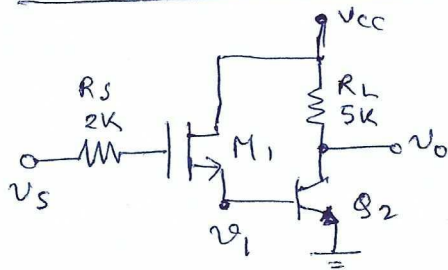
$$\Rightarrow C_1 = \frac{1}{2\pi (f_d/10) R_1^\infty} = \boxed{40.2\mu F} \quad C_2 = \frac{1}{2\pi (f_d/10) R_2^\infty} = \boxed{7.15\mu F}$$

$$\& C_3 = \frac{1}{2\pi f_d R_3^\infty} = \boxed{438\mu F}$$



$$R_2^\infty = 50K \parallel 40K = \underline{22.5K}$$

a) ac Midband Schematic: Convert the source $i_s - R_s$ to its Thevenin eqv. ($v_s = i_s R_s$)



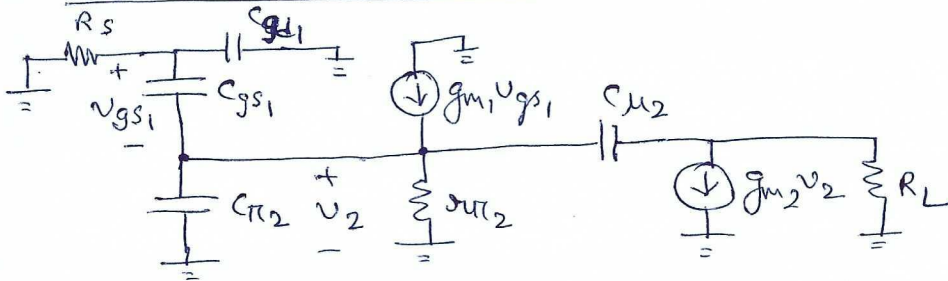
$$r_{E2} = 26\Omega, r_{\pi2} = 5.2k, g_{m1} = 200\mu A/V$$

$$\frac{v_o}{v_i} = -\frac{R_L}{r_{E2}} = -192.3$$

$$\frac{v_i}{v_s} = \frac{r_{\pi2}}{1/g_{m1} + r_{\pi2}} = 0.51$$

$$\Rightarrow \frac{v_o}{v_s} = -98.1 \Rightarrow \frac{v_o}{i_s} = -1.96 \times 10^5 \Omega = \boxed{-196 k\Omega}$$

b) High-Freq. Ekv. for ZVTC:



$$\underline{R_{gs1}^o} : R_{gs1}^o = \frac{R_s + r_{\pi2}}{1 + g_{m1} r_{\pi2}} = 3.53 k\Omega \Rightarrow \tau_1 = C_{gs1} R_{gs1}^o = 35.3 ns$$

$$\underline{R_{gd1}^o} : R_{gd1}^o = R_s = 2k \Rightarrow \tau_2 = R_{gd1}^o C_{gd1} = 2 ns$$

$$\underline{R_{\pi2}^o} : R_{\pi2}^o = r_{\pi2} \parallel \frac{1}{g_{m1}} = 2.55 k \Rightarrow \tau_3 = R_{\pi2}^o C_{\pi2} = 51 ns$$

$$\underline{R_{\mu2}^o} : R_{\mu2}^o = R_{\pi2}^o + R_L + g_{m2} R_{\pi2}^o R_L = 498 k \Rightarrow \tau_4 = C_{\mu2} R_{\mu2}^o = 1 \mu s$$

$$\Rightarrow \tau_{net} = 1.0883 \mu s \Rightarrow f_H = \frac{1}{2\pi \tau_{net}} = 146.2 kHz \Rightarrow t_f = \frac{0.35}{f_H} = \boxed{2.4 \mu s}$$

c) $C_{\mu2}$, \because it produces the largest time constant.

a) Assume $V_{BE4} = \underline{0.7V} \Rightarrow I_{C3} \approx I_{R1} = \underline{70\mu A} \Rightarrow I_{C4} = I_{BIAS} - I_{C3} = \underline{130\mu A}$

Self-consistent analysis:

$$I_{C3} = \frac{I_{C4}}{\beta_4} + \frac{V_T \ln I_{C4}/I_{S4}}{R_1} = \underline{65.4\mu A} \Rightarrow \underline{I_{C4} = 134.6\mu A}$$

Another iteration would change these values very little.

b) $I_{C1} = I_{C2} = \sqrt{I_{C3} I_{C4}} \times \sqrt{\frac{I_{S1} I_{S2}}{I_{S3} I_{S4}}} = \underline{469\mu A}$

$$\Rightarrow V_{BIAS} = V_T \ln \frac{I_{C1} I_{C2}}{I_{S1} I_{S2}} = V_T \ln \frac{I_{C3} I_{C4}}{I_{S3} I_{S4}} = \underline{1.28V}$$

c) DC offset of V_i to make DC offset of $V_o = 0$:

$$V_I = -V_{BIAS} = \underline{-1.28V}$$

d) $\underline{V_o = \pm 4V}$ $\because V_o$ would follow V_i , due to Q_5 & Q_1 - Q_2 being emitter followers with gain ~ 1 .

e) $P_{standby} = (V_{CC} + |V_{EE}|) \times (I_{BIAS} + I_{C1}) = \underline{6.69mW}$

f) $P_L = \frac{V_{OM}^2}{2R_L} = \underline{8mW}$

$$P_{supply} + P_{standby} = \frac{2V_{CC}V_{OM}}{\pi R_L} + 6.69mW = \underline{19.4mW}$$

$$\eta = \frac{P_L}{P_{supply} + P_{standby}} = \underline{0.412 \text{ (or } 41.2\%)}$$

a) i) At 100 Mrad/s, Gain = 10dB

$$\Rightarrow 10 = 40 \log_{10} \left(\frac{\omega_T}{100} \right) \Rightarrow \boxed{\omega_T = 177.8 \text{ Mrad/s}}$$

ii) $f = \left[\frac{(\omega_{P1} + \omega_{P2})^2}{4\omega_{P1}\omega_{P2}} - 1 \right] \frac{1}{A_0}$ with $A_0 = 50\text{dB} = \underline{316.2}$

$$= \boxed{0.0775}$$

iii) Will remain stable, \because poles will be complex conjugate with constant real part & will remain in the LHP.

b) i) $PM = 180^\circ - |\Phi|_{L=0\text{dB}} = \boxed{60^\circ}$

ii) With +ve PM, GM got to be -ve

iii) With +ve PM & -ve GM, the system is of course stable.

iv) $-120^\circ = -\tan^{-1} \frac{2\text{MHz}}{f_1} - \tan^{-1} \frac{2\text{MHz}}{3\text{MHz}} - \tan^{-1} \frac{2\text{MHz}}{5\text{MHz}}$

$$\Rightarrow \boxed{f_1 = 954 \text{ KHz}}$$

$$a) \frac{v_o}{v_x} = \frac{R_2 + 1/sC}{R_1 + R_2 + 1/sC} = \frac{1 + sR_2C}{1 + s(R_1 + R_2)C} = \frac{1 + j\omega/\omega_z}{1 + j\omega/\omega_p} \quad (\text{PZC form})$$

$$\omega_z = \frac{1}{R_2C} \quad \omega_p = \frac{1}{(R_1 + R_2)C}$$

$$b) f_z = \underline{5 \text{ MHz}} = \frac{1}{2\pi R_2C} \dots \textcircled{1}$$

$$A_0 = 90 \text{ dB} = 3.16 \times 10^4 \Rightarrow f_p = \frac{25 \text{ MHz}}{3.16 \times 10^4} = \underline{791 \text{ Hz}} = \frac{1}{2\pi(R_1 + R_2)C} \dots \textcircled{2}$$

$$\Rightarrow \boxed{R_2 = 1.58 \Omega} \quad \boxed{C = 20.15 \text{ nF}} \quad (\text{from } \textcircled{1} \text{ \& } \textcircled{2})$$

c) from Asymptotic Bode Plot:

$$\phi/25 \text{ MHz} = -90^\circ \Big|_{f_p \text{ at } 791 \text{ Hz}} - 45^\circ \Big|_{25 \text{ MHz pole}} \\ = \boxed{-135^\circ}$$

(500 MHz pole won't contribute any phase at 25 MHz)

$$\Rightarrow \text{PM} = 180^\circ - |\phi| = \boxed{45^\circ}$$

$$\left[\text{Exact Value: } \phi = -90^\circ - 45^\circ - \tan^{-1} \frac{25 \text{ MHz}}{500 \text{ MHz}} = \underline{-138^\circ} \Rightarrow \boxed{\text{PM} = 42^\circ} \right]$$

d) Short R_2 \Rightarrow It will become an LPF with PPF at $\frac{1}{2\pi R_1C}$.

————— X —————

$$a) R_L = \frac{V_{DD} - V_A}{I_{D2}} = \boxed{27 \text{ k}\Omega}$$

$$V_{E3} = V_A + V_{EB3} = \underline{3V} \Rightarrow R_1 = \frac{V_{DD} - V_{E3}}{I_1} = \boxed{20 \text{ k}\Omega}$$

$$R_2 = \frac{V_0 - V_{SS}}{I_1} = \boxed{50 \text{ k}\Omega}$$

$$b) I_{D2} = \frac{K_N'}{2} \left(\frac{W}{L}\right)_2 (V_{GS2} - V_{TN2})^2 \Rightarrow V_{GS2} = \underline{1.25V} \Rightarrow V_{S2} = \underline{-1.25V}$$

$$\text{Drop across } I_{SS} = V_{S2} - V_{SS} = \boxed{3.75V}$$

$$c) g_{m2} = K_N V_{GT2} = \sqrt{2 K_N I_{D2}} = \underline{8 \times 10^{-4} \text{ S}}$$

$$A_{dm} (\text{NMOS DA}) = -g_{m2} R_L = -\underline{21.6}$$

$$v_A = v_{O2} (\text{for DA}) = -\frac{A_{dm}}{2} v_{id} \quad (\because A_{cm} = 0 \text{ due to } I_{SS} \text{ being ideal})$$

$$\& v_{id} = -v_i \Rightarrow \frac{v_A}{v_i} = \frac{A_{dm}}{2} = \boxed{-10.8}$$

$$r_{E3} = \underline{260 \Omega} \quad r_{\pi3} = \underline{26 \text{ k}\Omega} \Rightarrow R_{i3} = r_{\pi3} + (\beta+1)R_1 = \underline{2.05 \text{ M}\Omega}$$

$$[R_{i3} \gg R_L \Rightarrow \text{It does not load the DA}]$$

$$\frac{v_o}{v_A} = -\frac{R_2}{r_{E3} + R_1} = \boxed{-2.47}$$

$$\Rightarrow \frac{v_o}{v_i} = \boxed{+26.7}$$