

Ghidra - MC68705P3.BIN

```

0121 a4 c0      AND      #0xc0
0123 b7 15      STA      DAT_0015                                = FFh

                                LAB_0125                                XREF[1]:  ProcessData:0151(j)
                                STA      DAT_0012                                = FFh
0125 b7 12      STA      DAT_0013                                = FFh
0129 b7 13

                                LAB_012b                                XREF[2]:  ProcessData:0155(j),
                                ProcessData:0179(j)
012b 3a 10      DEC      DAT_0010                                = FFh
012f 3a 11      DEC      DAT_0011                                = FFh
0133 19 14      BCLR     0x4,DAT_0014                            = FFh
0135 0a 19 07   BRSET    0x5,DAT_0019,LAB_013f                  = FFh
0138 cd 06 18   JSR      FUN_0618                                undefined FUN_0618()
013d b7 11      STA      DAT_0011                                = FFh

                                LAB_013f                                XREF[3]:  012d(j), 0131(j), 0135(j)
013f cc 02 5e   JMP      WaitForData                            undefined WaitForData()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

*****
*                               FUNCTION                               *
*****
undefined ProcessData()
A:1      <RETURN>
ProcessData                                XREF[2]:  WaitForData:0292(c),
FUN_041d:0579(c)
0142 06 20 04   BRSET    0x3,DAT_0020,LAB_0149                    = FFh
0145 1f 14      BCLR     0x7,DAT_0014                            = FFh

                                LAB_0149                                XREF[1]:  0142(j)
0149 1e 14      BSET     0x7,DAT_0014                            = FFh

                                LAB_014b                                XREF[1]:  0147(j)
014b 13 02      BCLR     0x1,PORTC                                = FFh
014f b1 12      CMP      DAT_0012                                = FFh
0151 26 d2      BNE      LAB_0125
0153 3a 13      DEC      DAT_0013                                = FFh
0155 26 d4      BNE      LAB_012b
0159 b8 15      EOR      DAT_0015                                = FFh
015d 0c 17 0c   BRSET    0x6,DAT_0017,LAB_016c                  = FFh
0162 a4 3f      AND      #0x3f
0164 a1 00      CMP      #0x0
0166 27 0d      BEQ      LAB_0175
0168 b7 16      STA      DAT_0016                                = FFh

                                LAB_016c                                XREF[1]:  015d(j)
016c 0c 12 0c   BRSET    0x6,DAT_0012,LAB_017b                    = FFh
0171 ba 14      ORA      DAT_0014                                = FFh

                                LAB_0175                                XREF[3]:  0166(j), 0183(j), 0187(j)
0175 b6 12      LDA      DAT_0012                                = FFh
017d a1 00      CMP      #0x0
017f 26 04      BNE      LAB_0185
0181 1a 14      BSET     0x5,DAT_0014                            = FFh

                                LAB_0185                                XREF[1]:  017f(j)
0185 1b 14      BCLR     0x5,DAT_0014                            = FFh
018b b7 11      STA      DAT_0011                                = FFh
018d ae 80      LDX      #0x80
018f 09 14 02   BRCLR    0x4,DAT_0014,LAB_0194                  = FFh
0192 ae 8b      LDX      #0x8b

                                LAB_0194                                XREF[2]:  018f(j), 019f(j)
0194 f6      LDA      X=>DAT_008b                                = 01h
0195 b1 16      CMP      DAT_0016                                = 02h
0197 27 08      BEQ      LAB_01a1                                = FFh
0199 a1 ff      CMP      #0xff
019b 27 31      BEQ      LAB_01ce
019e 5c      INCX

                                LAB_01a1                                XREF[1]:  0197(j)
01a1 5c      INCX
01a2 fe      LDX      X=>DAT_008c                                = 01h
01a3 58      ASLX

                                switchD_01a4::switchD
01a4 dc 01 a7   JMP      0x1a7,X

                                switchD_01a4::caseD_0                XREF[1]:  01a4(j)
01a7 20 32      BRA      switchD_01a4::caseD_34                  undefined caseD_34()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

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Ghidra - MC68705P3.BIN

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switchD_01a4::caseD_2                                XREF[1]: 01a4(j)
01a9 20 26      BRA      switchD_01a4::caseD_2a        undefined caseD_2a()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

switchD_01a4::caseD_4                                XREF[1]: 01a4(j)
01ab 20 40      BRA      switchD_01a4::caseD_46        undefined caseD_46()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

switchD_01a4::caseD_6                                XREF[1]: 01a4(j)
01ad 20 4e      BRA      switchD_01a4::caseD_56        undefined caseD_56()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

switchD_01a4::caseD_8                                XREF[1]: 01a4(j)
01af 20 50      BRA      switchD_01a4::caseD_5a        undefined caseD_5a()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

switchD_01a4::caseD_a                                XREF[1]: 01a4(j)
01b1 20 52      BRA      switchD_01a4::caseD_5e        undefined caseD_5e()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

switchD_01a4::caseD_c                                XREF[1]: 01a4(j)
01b3 20 5e      BRA      switchD_01a4::caseD_6c        undefined caseD_6c()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

*****
*                               FUNCTION                               *
*****
undefined caseD_10()
A:1      <RETURN>
switchD_01a4::caseD_10                                XREF[4]: ProcessData:01a4(j), 01bd(j),
FUN_01c0:01cb(c),
caseD_46:01f2(c)

01b7 b6 00      LDA      PORTA

switchD_01a4::caseD_12                                XREF[1]: ProcessData:01a4(j)
01b9 a4 3f      AND      #0x3f

switchD_01a4::caseD_14                                XREF[1]: ProcessData:01a4(j)
01bb a1 00      CMP      #0x0

switchD_01a4::caseD_16                                XREF[1]: ProcessData:01a4(j)
01bd 26 f8      BNE      switchD_01a4::caseD_10

switchD_01a4::caseD_18                                XREF[1]: ProcessData:01a4(j)
01bf 81      RTS

*****
*                               FUNCTION                               *
*****
undefined FUN_01c0()
A:1      <RETURN>
caseD_1a (01c0+1)                                XREF[4,1]: caseD_2a:01d9(c),
FUN_01c0      caseD_34:01eb(c),
caseD_56:01ff(c),
caseD_5a:0203(c),
ProcessData:01a4(j)

01c0 cd 02 38      JSR      FUN_0238                                undefined FUN_0238()

caseD_1e (01c3+2)                                XREF[1,1]: ProcessData:01a4(j),
switchD_01a4::caseD_1c      ProcessData:01a4(j)
01c3 cd 02 38      JSR      FUN_0238                                undefined FUN_0238()

caseD_20 (01c6+1)                                XREF[11,1]: caseD_2a:01d4(j),
LAB_01c6      caseD_34:01db(j),
caseD_46:01fb(j),
caseD_5e:0205(j),
caseD_5e:020d(j),
caseD_5e:0211(j),
caseD_6c:021f(j),
caseD_6c:0227(j),
caseD_6c:022b(j),
caseD_6c:0232(j),
caseD_6c:0236(j),
ProcessData:01a4(j)
= FFh

01c6 b6 14      LDA      DAT_0014                                XREF[0,1]: ProcessData:01a4(j)
caseD_22 (01c8+1)                                undefined FUN_023c()
01c8 cd 02 3c      JSR      FUN_023c

caseD_26 (01cb+2)                                XREF[1,1]: ProcessData:01a4(j),
switchD_01a4::caseD_24      ProcessData:01a4(j)
01cb cd 01 b7      JSR      switchD_01a4::caseD_10                                undefined caseD_10()

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Ghidra - MC68705P3.BIN

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caseD_28 (01ce+1)                                XREF[1,1]:  ProcessData:019b(j),
LAB_01ce                                          ProcessData:01a4(j)
01ce cc 01 1f      JMP          FUN_011f          undefined FUN_011f()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

*****
*                               *
*                               *
*****
undefined caseD_2a()
A:1      <RETURN>
caseD_3c (01d1+2)                                XREF[2,1]:  ProcessData:01a4(j),
switchD_01a4::caseD_2a                          ProcessData:01a9(c),
                                                ProcessData:01a4(j)
01d1 08 14 03      BRSET      0x4,DAT_0014,switchD_01a4::caseD_30      = FFh
caseD_2e (01d4+1)                                XREF[0,1]:  ProcessData:01a4(j)
01d4 06 20 ef      BRSET      0x3,DAT_0020,LAB_01c6                      = FFh

switchD_01a4::caseD_30                          XREF[3]:    ProcessData:01a4(j),
                                                ProcessData:01b5(j), 01d1(j)
01d7 a6 01      LDA          #0x1

switchD_01a4::caseD_32                          XREF[1]:    ProcessData:01a4(j)
01d9 20 e5      BRA          FUN_01c0          undefined FUN_01c0()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

*****
*                               *
*                               *
*****
undefined caseD_34()
A:1      <RETURN>
caseD_36 (01db+2)                                XREF[2,1]:  ProcessData:01a4(j),
switchD_01a4::caseD_34                          ProcessData:01a7(c),
                                                ProcessData:01a4(j)
01db 0e 12 e8      BRSET      0x7,DAT_0012,LAB_01c6                      = FFh
caseD_38 (01de+1)                                XREF[0,1]:  ProcessData:01a4(j)
01de a6 02      LDA          #0x2
caseD_3a (01e0+1)                                XREF[0,1]:  ProcessData:01a4(j)
01e0 cd 02 38      JSR          FUN_0238          undefined FUN_0238()

caseD_3e (01e3+2)                                XREF[1,1]:  ProcessData:01a4(j),
switchD_01a4::caseD_3c                          ProcessData:01a4(j)
01e3 cd 02 38      JSR          FUN_0238          undefined FUN_0238()
caseD_40 (01e6+1)                                XREF[0,1]:  ProcessData:01a4(j)
01e6 cd 02 4b      JSR          switchD_01a4::caseD_a4          undefined caseD_a4()

switchD_01a4::caseD_42                          XREF[1]:    ProcessData:01a4(j)
01e9 a6 04      LDA          #0x4

switchD_01a4::caseD_44                          XREF[1]:    ProcessData:01a4(j)
01eb 20 d3      BRA          FUN_01c0          undefined FUN_01c0()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

*****
*                               *
*                               *
*****
undefined caseD_46()
A:1      <RETURN>
switchD_01a4::caseD_46                          XREF[2]:    ProcessData:01a4(j),
                                                ProcessData:01ab(c)
01ed a6 02      LDA          #0x2

caseD_4a (01ef+2)                                XREF[1,1]:  ProcessData:01a4(j),
switchD_01a4::caseD_48                          ProcessData:01a4(j)
01ef cd 02 38      JSR          FUN_0238          undefined FUN_0238()
caseD_4c (01f2+1)                                XREF[0,1]:  ProcessData:01a4(j)
01f2 cd 01 b7      JSR          switchD_01a4::caseD_10          undefined caseD_10()

caseD_50 (01f5+2)                                XREF[1,1]:  ProcessData:01a4(j),
switchD_01a4::caseD_4e                          ProcessData:01a4(j)
01f5 cd 02 38      JSR          FUN_0238          undefined FUN_0238()
caseD_52 (01f8+1)                                XREF[0,1]:  ProcessData:01a4(j)
01f8 cd 02 4b      JSR          switchD_01a4::caseD_a4          undefined caseD_a4()

*****
*                               *
*                               *
*****
undefined caseD_56()
A:1      <RETURN>
switchD_01a4::caseD_56                          XREF[2]:    ProcessData:01a4(j),
                                                ProcessData:01ad(c)
01fd a6 04      LDA          #0x4

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Ghidra - MC68705P3.BIN

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switchD_01a4::caseD_58                                XREF[1]:   ProcessData:01a4(j)
01ff 20 bf      BRA      FUN_01c0                        undefined FUN_01c0()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

*****
*                               FUNCTION                               *
*****

undefined caseD_5a()
A:1      <RETURN>
switchD_01a4::caseD_5a                                XREF[2]:   ProcessData:01a4(j),
                                                    ProcessData:01af(c)

0201 a6 08      LDA      #0x8

switchD_01a4::caseD_5c                                XREF[1]:   ProcessData:01a4(j)
0203 20 bb      BRA      FUN_01c0                        undefined FUN_01c0()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

*****
*                               FUNCTION                               *
*****

undefined caseD_5e()
A:1      <RETURN>
caseD_60 (0205+2)                                XREF[2,1]: ProcessData:01a4(j),
switchD_01a4::caseD_5e                                ProcessData:01b1(c),
                                                    ProcessData:01a4(j)
                                                    = FFh
0205 0d 15 be      BRCLR   0x6,DAT_0015,LAB_01c6
caseD_62 (0208+1)                                XREF[0,1]: ProcessData:01a4(j)
0208 0a 14 04      BRSET   0x5,DAT_0014,switchD_01a4::caseD_68
                                                    = FFh

switchD_01a4::caseD_64                                XREF[1]:   ProcessData:01a4(j)
020b 1a 14      BSET     0x5,DAT_0014
                                                    = FFh

switchD_01a4::caseD_68                                XREF[2]:   ProcessData:01a4(j), 0208(j)
020f 1b 14      BCLR     0x5,DAT_0014
                                                    = FFh

*****
*                               FUNCTION                               *
*****

undefined caseD_6c()
A:1      <RETURN>
switchD_01a4::caseD_6c                                XREF[3]:   ProcessData:01a4(j),
                                                    ProcessData:01b3(c), 021d(j)

0213 b6 00      LDA      PORTA

switchD_01a4::caseD_6e                                XREF[1]:   ProcessData:01a4(j)
0215 a4 3f      AND      #0x3f

switchD_01a4::caseD_70                                XREF[1]:   ProcessData:01a4(j)
0217 a1 21      CMP      #0x21

switchD_01a4::caseD_72                                XREF[1]:   ProcessData:01a4(j)
0219 27 12      BEQ      switchD_01a4::caseD_86

switchD_01a4::caseD_74                                XREF[1]:   ProcessData:01a4(j)
021b a1 00      CMP      #0x0

switchD_01a4::caseD_76                                XREF[1]:   ProcessData:01a4(j)
021d 26 f4      BNE      switchD_01a4::caseD_6c

caseD_7a (021f+2)                                XREF[1,1]: ProcessData:01a4(j),
switchD_01a4::caseD_78                                ProcessData:01a4(j)
021f 0d 15 a4      BRCLR   0x6,DAT_0015,LAB_01c6
                                                    = FFh
caseD_7c (0222+1)                                XREF[0,1]: ProcessData:01a4(j)
0222 0c 14 04      BRSET   0x6,DAT_0014,switchD_01a4::caseD_82
                                                    = FFh

switchD_01a4::caseD_7e                                XREF[1]:   ProcessData:01a4(j)
0225 1c 14      BSET     0x6,DAT_0014
                                                    = FFh

switchD_01a4::caseD_82                                XREF[2]:   ProcessData:01a4(j), 0222(j)
0229 1d 14      BCLR     0x6,DAT_0014
                                                    = FFh

caseD_88 (022d+2)                                XREF[2,1]: ProcessData:01a4(j), 0219(j),
switchD_01a4::caseD_86                                ProcessData:01a4(j)
022d 08 14 04      BRSET   0x4,DAT_0014,LAB_0234
                                                    = FFh
caseD_8a (0230+1)                                XREF[0,1]: ProcessData:01a4(j)
0230 18 14      BSET     0x4,DAT_0014
                                                    = FFh

caseD_8e (0234+1)                                XREF[1,1]: 022d(j), ProcessData:01a4(j)
LAB_0234
0234 19 14      BCLR     0x4,DAT_0014
                                                    = FFh

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Ghidra - MC68705P3.BIN

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*****
*                               FUNCTION                               *
*****

undefined FUN_0238()
A:1      <RETURN>
caseD_92 (0238+1)      XREF[6,1]:  FUN_01c0:01c0(c),
FUN_0238      FUN_01c0:01c3(c),
caseD_34:01e0(c),
caseD_34:01e3(c),
caseD_46:01ef(c),
caseD_46:01f5(c),
ProcessData:01a4(j)
= FFh

0238 ba 14      ORA      DAT_0014
caseD_94 (023a+1)      XREF[0,1]:  ProcessData:01a4(j)
023a a4 7f      AND      #0x7f

*****
*                               FUNCTION                               *
*****

undefined FUN_023c()
A:1      <RETURN>
caseD_96 (023c+1)      XREF[2,1]:  RESET:00f5(c), FUN_01c0:01c8(c),
FUN_023c      ProcessData:01a4(j)
= FFh
023c 11 02      BCLR     0x0,PORTC
caseD_98 (023e+1)      XREF[0,1]:  ProcessData:01a4(j)
023e b7 01      STA      PORTB
= FFh
caseD_9a (0240+1)      XREF[0,1]:  ProcessData:01a4(j)
0240 10 02      BSET     0x0,PORTC
= FFh
caseD_9c (0242+1)      XREF[0,1]:  ProcessData:01a4(j)
0242 a6 20      LDA      #0x20

LAB_0244      XREF[2]:    0245(j), 0248(j)
0244 5a      DECX

switchD_01a4::caseD_9e      XREF[1]:    ProcessData:01a4(j)
0245 26 fd      BNE      LAB_0244

switchD_01a4::caseD_a0      XREF[1]:    ProcessData:01a4(j)
0247 4a      DECA
caseD_a2 (0248+1)      XREF[0,1]:  ProcessData:01a4(j)
0248 26 fa      BNE      LAB_0244
024a 81      RTS

*****
*                               FUNCTION                               *
*****

undefined caseD_a4()
A:1      <RETURN>
switchD_01a4::caseD_a4      XREF[4]:    RESET:0102(c),
ProcessData:01a4(j),
caseD_34:01e6(c),
caseD_46:01f8(c)

024b a6 06      LDA      #0x6

switchD_01a4::caseD_a6      XREF[1]:    ProcessData:01a4(j)
024d b7 1e      STA      DAT_001e
= FFh

switchD_01a4::caseD_a8      XREF[1]:    ProcessData:01a4(j)
024f a6 30      LDA      #0x30

switchD_01a4::caseD_aa      XREF[1]:    ProcessData:01a4(j)
0251 b7 4c      STA      DAT_004c
= FFh

switchD_01a4::caseD_ac      XREF[4]:    ProcessData:01a4(j), 0254(j),
0257(j), 025b(j)
0253 5a      DECX
caseD_ae (0254+1)      XREF[0,1]:  ProcessData:01a4(j)
0254 26 fd      BNE      switchD_01a4::caseD_ac
0256 4a      DECA

switchD_01a4::caseD_b0      XREF[1]:    ProcessData:01a4(j)
0257 26 fa      BNE      switchD_01a4::caseD_ac

switchD_01a4::caseD_b2      XREF[1]:    ProcessData:01a4(j)
0259 3a 1e      DEC      DAT_001e
= FFh

switchD_01a4::caseD_b4      XREF[1]:    ProcessData:01a4(j)
025b 26 f6      BNE      switchD_01a4::caseD_ac

switchD_01a4::caseD_b6      XREF[1]:    ProcessData:01a4(j)
025d 81      RTS

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Ghidra - MC68705P3.BIN

	caseD_c0 (0266+1)	XREF[1,1]: 026e(j), ProcessData:01a4(j)
	LAB_0266	
0266 2f 0a	BIH LAB_0272	
	caseD_c4 (026a+1)	XREF[0,1]: ProcessData:01a4(j)
026a a4 3f	AND #0x3f	
	caseD_c6 (026c+1)	XREF[0,1]: ProcessData:01a4(j)
026c a1 00	CMP #0x0	
	caseD_c8 (026e+1)	XREF[0,1]: ProcessData:01a4(j)
026e 27 f6	BEQ LAB_0266	
	caseD_cc (0272+1)	XREF[1,1]: 0266(j), ProcessData:01a4(j)
	LAB_0272	
0272 a6 00	LDA #0x0	
	caseD_ce (0274+1)	XREF[0,1]: ProcessData:01a4(j)
0274 b7 1a	STA DAT_001a	= FFh
	caseD_d0 (0276+1)	XREF[0,1]: ProcessData:01a4(j)
0276 12 02	BSET 0x1,PORTC	= FFh
	caseD_d2 (0278+1)	XREF[0,1]: ProcessData:01a4(j)
0278 11 02	BCLR 0x0,PORTC	= FFh
	caseD_d4 (027a+1)	XREF[0,1]: ProcessData:01a4(j)
027a 10 02	BSET 0x0,PORTC	= FFh
	caseD_d6 (027c+1)	XREF[0,1]: ProcessData:01a4(j)
027c b6 4c	LDA DAT_004c	= FFh
	caseD_d8 (027e+1)	XREF[0,1]: ProcessData:01a4(j)
027e b7 1e	STA DAT_001e	= FFh
	caseD_da (0280+1)	XREF[0,1]: ProcessData:01a4(j)
0280 a6 05	LDA #0x5	
	caseD_dc (0282+1)	XREF[3,1]: 0285(j), 0288(j), 028c(j), ProcessData:01a4(j)
	LAB_0282	
0282 2e 11	BIL switchD_01a4::caseD_ee	
0284 5a	DECX	
	switchD_01a4::caseD_de	XREF[1]: ProcessData:01a4(j)
0285 26 fb	BNE LAB_0282	
	switchD_01a4::caseD_e0	XREF[1]: ProcessData:01a4(j)
0287 4a	DECA	
	caseD_e2 (0288+1)	XREF[0,1]: ProcessData:01a4(j)
0288 26 f8	BNE LAB_0282	
	caseD_e4 (028a+1)	XREF[0,1]: ProcessData:01a4(j)
028a 3a 1e	DEC DAT_001e	= FFh
	caseD_e6 (028c+1)	XREF[0,1]: ProcessData:01a4(j)
028c 26 f4	BNE LAB_0282	
	caseD_e8 (028e+1)	XREF[0,1]: ProcessData:01a4(j)
028e a6 01	LDA #0x1	
	caseD_ec (0292+1)	XREF[1,1]: 0270(j), ProcessData:01a4(j)
	LAB_0292	
0292 cc 01 42	JMP ProcessData	undefined ProcessData()
	-- Flow Override: CALL_RETURN (CALL_TERMINATOR)	
	switchD_01a4::caseD_ee	XREF[2]: ProcessData:01a4(j), 0282(j)
0295 ae 08	LDX #0x8	
	switchD_01a4::caseD_f0	XREF[3]: ProcessData:01a4(j), 02c6(j), 02dc(j)
0297 b6 00	LDA PORTA	
	switchD_01a4::caseD_f2	XREF[1]: ProcessData:01a4(j)
0299 13 02	BCLR 0x1,PORTC	= FFh
	switchD_01a4::caseD_f4	XREF[1]: ProcessData:01a4(j)
029b 12 02	BSET 0x1,PORTC	= FFh
	switchD_01a4::caseD_f6	XREF[1]: ProcessData:01a4(j)
029d b7 20	STA DAT_0020	= FFh
	switchD_01a4::caseD_f8	XREF[1]: ProcessData:01a4(j)
029f 34 21	LSR DAT_0021	= FFh
	switchD_01a4::caseD_fa	XREF[1]: ProcessData:01a4(j)
02a1 34 22	LSR DAT_0022	= FFh
	switchD_01a4::caseD_fc	XREF[1]: ProcessData:01a4(j)
02a3 34 23	LSR DAT_0023	= FFh
	switchD_01a4::caseD_fe	XREF[1]: ProcessData:01a4(j)
02a5 00 20 04	BRSET 0x0,DAT_0020,LAB_02ac	= FFh
02a8 1e 21	BSET 0x7,DAT_0021	= FFh
	LAB_02ac	XREF[1]: 02a5(j)
02ac 1f 21	BCLR 0x7,DAT_0021	= FFh

Ghidra - MC68705P3.BIN

02ae 02 20 04	LAB_02ae BRSET	0x1,DAT_0020,LAB_02b5	XREF[1]:	02aa(j)	= FFh
02b1 1e 22	BSET	0x7,DAT_0022			= FFh
02b5 1f 22	LAB_02b5 BCLR	0x7,DAT_0022	XREF[1]:	02ae(j)	= FFh
02b7 04 20 04	LAB_02b7 BRSET	0x2,DAT_0020,LAB_02be	XREF[1]:	02b3(j)	= FFh
02ba 1e 23	BSET	0x7,DAT_0023			= FFh
02be 1f 23	LAB_02be BCLR	0x7,DAT_0023	XREF[1]:	02b7(j)	= FFh
02c0 3c 1a	LAB_02c0 INC	DAT_001a	XREF[1]:	02bc(j)	= FFh
02c4 a1 08	CMP	#0x8			
02c6 26 cf	BNE	switchD_01a4::caseD_f0			
02c8 a6 00	LDA	#0x0			
02ca b7 1a	STA	DAT_001a			= FFh
02cc 5a	DECX				
02cd b6 21	LDA	DAT_0021			= FFh
02cf e7 2d	STA	0x2d,X=>DAT_0034			= FFh
02d3 e7 35	STA	0x35,X=>DAT_003c			= FFh
02d7 e7 3d	STA	0x3d,X=>DAT_0044			= FFh
02da a1 00	CMP	#0x0			
02dc 26 b9	BNE	switchD_01a4::caseD_f0			
02de b6 19	LDA	DAT_0019			= FFh
02e0 b1 18	CMP	DAT_0018			= FFh
02e2 27 09	BEQ	LAB_02ed			
02e4 b7 18	STA	DAT_0018			= FFh
02e6 cd 06 18	JSR	FUN_0618			undefined FUN_0618()
02eb b7 47	STA	DAT_0047			= FFh
02ef b7 19	STA	DAT_0019			= FFh
02f5 b7 1b	STA	DAT_001b			= FFh
02fa b7 1c	STA	DAT_001c			= FFh
02fc cd 06 41	JSR	FUN_0641			undefined FUN_0641()
0301 a1 00	CMP	#0x0			
0303 26 03	BNE	LAB_0308			
0305 cc 04 1d	JMP	FUN_041d			undefined FUN_041d()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)					
0308 ae 01	LAB_0308 LDX	#0x1	XREF[1]:	0303(j)	
030c bf 1f	STX	DAT_001f			= FFh
030e 54	LAB_030e LSRX		XREF[1]:	0325(j)	
030f e7 25	STA	DAT_0025,X			= FFh
0313 5c	INCX				
0314 e6 2d	LDA	0x2d,X=>DAT_002f			= FFh
0318 5c	INCX				
031a a1 09	CMP	#0x9			
031c 27 09	BEQ	LAB_0327			
031e e6 2d	LDA	0x2d,X=>DAT_0030			= FFh
0322 cd 06 41	JSR	FUN_0641			undefined FUN_0641()
0327 0a 18 1f	LAB_0327 BRSET	0x5,DAT_0018,LAB_0349	XREF[1]:	031c(j)	= FFh
032a ae 03	LDX	#0x3			
032c e6 25	LAB_032c LDA	0x25,X=>DAT_0028	XREF[1]:	0335(j)	= FFh
032e e7 29	STA	0x29,X=>DAT_002c			= FFh
0330 a1 20	CMP	#0x20			
0332 26 6e	BNE	LAB_03a2			
0334 5a	DECX				
0335 2a f5	BPL	LAB_032c			
0339 b7 47	STA	DAT_0047			= FFh
033d b7 45	STA	DAT_0045			= FFh
033f 1a 19	BSET	0x5,DAT_0019			= FFh
0349 ae 03	LAB_0349 LDX	#0x3	XREF[1]:	0327(j)	
034b 1a 19	BSET	0x5,DAT_0019			= FFh
034d e6 25	LAB_034d LDA	0x25,X=>DAT_0028	XREF[1]:	0354(j)	= FFh
034f e1 29	CMP	0x29,X=>DAT_002c			= FFh
0351 26 08	BNE	LAB_035b			
0353 5a	DECX				
0354 2a f7	BPL	LAB_034d			
0356 0e 3a ed	BRSET	0x7,DAT_003a,LAB_0346			= FFh

Ghidra - MC68705P3.BIN

	LAB_035b		XREF[1]:	0351(j)
035b ae 01	LDX	#0x1		
035d b6 25	LDA	DAT_0025		= FFh
	LAB_035f		XREF[1]:	0367(j)
035f e1 29	CMP	<u>0x29,X=>DAT_002a</u>		= FFh
0361 27 06	BEQ	LAB_0369		
0363 5c	INCX			
0365 a1 04	CMP	#0x4		
0367 26 f6	BNE	LAB_035f		
036d b0 1f	SUB	DAT_001f		= FFh
0372 a1 28	CMP	#0x28		
0374 26 04	BNE	LAB_037a		
0378 b7 47	STA	DAT_0047		= FFh
037c cd 05 fc	JSR	FUN_05fc		undefined FUN_05fc()
037f 3c 47	INC	DAT_0047		= FFh
0383 cd 06 90	JSR	FUN_0690		undefined FUN_0690()
0386 e6 25	LDA	<u>0x25,X=>DAT_0027</u>		= FFh
0388 cd 05 d2	JSR	FUN_05d2		undefined FUN_05d2()
038b 5c	INCX			
038d a1 04	CMP	#0x4		
038f 26 df	BNE	LAB_0370		
0391 ae 00	LDX	#0x0		
	LAB_0393		XREF[1]:	039b(j)
0393 e6 25	LDA	<u>DAT_0025,X</u>		= FFh
0395 e7 29	STA	<u>DAT_0029,X</u>		= FFh
0397 5c	INCX			
0399 a1 04	CMP	#0x4		
039b 26 f6	BNE	LAB_0393		
03a4 b7 45	STA	DAT_0045		= FFh
03a8 cd 06 90	JSR	FUN_0690		undefined FUN_0690()
	LAB_03ab		XREF[3]:	0344(j), 0359(j), 03a0(j)
03ab b6 45	LDA	DAT_0045		= FFh
03ad cd 06 90	JSR	FUN_0690		undefined FUN_0690()
03b0 12 19	BSET	0x1,DAT_0019		= FFh
03b2 0a 19 12	BRSET	0x5,DAT_0019,LAB_03c7		= FFh
03b7 a1 20	CMP	#0x20		
03b9 27 07	BEQ	LAB_03c2		
03bd cd 06 0c	JSR	FUN_060c		undefined FUN_060c()
03c4 cd 06 0c	JSR	FUN_060c		undefined FUN_060c()
03ca cd 03 cf	JSR	FUN_03cf		undefined FUN_03cf()
03cd 20 25	BRA	FUN_03f4		undefined FUN_03f4()
	-- Flow Override: CALL_RETURN (CALL_TERMINATOR)			
04d8 cd 06 90	JSR	FUN_0690		undefined FUN_0690()
	LAB_04e4		XREF[1]:	04e0(j)
04e4 cd 06 0c	JSR	FUN_060c		undefined FUN_060c()
04e9 bf 1f	STX	DAT_001f		= FFh
04ed cd 06 23	JSR	FUN_0623		undefined FUN_0623()
04f0 cd 05 d2	JSR	FUN_05d2		undefined FUN_05d2()
04f5 e6 35	LDA	DAT_0035,X		= FFh
04f7 cd 06 23	JSR	FUN_0623		undefined FUN_0623()
04fa cd 05 d2	JSR	FUN_05d2		undefined FUN_05d2()
04fd 0f 38 0c	BRCLR	0x7,DAT_0038,LAB_050c		= FFh
	LAB_0507		XREF[1]:	0502(j)
0507 18 19	BSET	0x4,DAT_0019		= FFh
0509 cd 06 0c	JSR	FUN_060c		undefined FUN_060c()
050e e6 35	LDA	DAT_0035,X		= FFh
0510 cd 06 23	JSR	FUN_0623		undefined FUN_0623()
0513 cd 05 d2	JSR	FUN_05d2		undefined FUN_05d2()
0518 e6 35	LDA	DAT_0035,X		= FFh
051a cd 06 23	JSR	FUN_0623		undefined FUN_0623()
051d cd 05 d2	JSR	FUN_05d2		undefined FUN_05d2()
0520 0e 38 0b	BRSET	0x7,DAT_0038,LAB_052e		= FFh
0525 cd 06 0c	JSR	FUN_060c		undefined FUN_060c()
052b 5c	INCX			
052c 20 20	BRA	LAB_054e		
0530 cd 05 d2	JSR	FUN_05d2		undefined FUN_05d2()
0535 e6 35	LDA	DAT_0035,X		= FFh
0537 cd 06 23	JSR	FUN_0623		undefined FUN_0623()
053a cd 05 d2	JSR	FUN_05d2		undefined FUN_05d2()
053f e6 35	LDA	DAT_0035,X		= FFh
0541 cd 06 23	JSR	FUN_0623		undefined FUN_0623()
0544 cd 05 d2	JSR	FUN_05d2		undefined FUN_05d2()
0549 cd 05 d2	JSR	FUN_05d2		undefined FUN_05d2()
054c be 1f	LDX	DAT_001f		= FFh
	LAB_054e		XREF[1]:	052c(j)
054e e6 35	LDA	<u>0x35,X=>DAT_0037</u>		= FFh
0550 cd 06 23	JSR	FUN_0623		undefined FUN_0623()
0553 cd 05 d2	JSR	FUN_05d2		undefined FUN_05d2()

Ghidra - MC68705P3.BIN

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0558 e6 35      LDA      DAT_0035,X
055a cd 06 23    JSR      FUN_0623
055d cd 05 d2    JSR      FUN_05d2
0562 cd 06 90    JSR      FUN_0690
0567 cd 05 d2    JSR      FUN_05d2
056a ae 00      LDX      #0x0

                                = FFh
                                undefined FUN_0623()
                                undefined FUN_05d2()
                                undefined FUN_0690()
                                undefined FUN_05d2()

                                LAB_056c
                                XREF[1]: 0577(j)
056c e6 3d      LDA      DAT_003d,X
0570 cd 05 7c    JSR      FUN_057c
                                = FFh
                                undefined FUN_057c()
0573 5c          INCX
0575 a1 08      CMP      #0x8
0577 25 f3      BCS      LAB_056c

                                LAB_0579
                                XREF[1]: FUN_03f4:041a(j)
0579 cc 01 42    JMP      ProcessData
                                undefined ProcessData()
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

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