

Ghidra - MC68705U3_35C.BIN

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//
// RAM
// RAM:0000-RAM:0fff
//

*****
* MC68705U3 - 8-Bit EPROM Microcontroller Unit *
*
* The MC68705U3 (HMOS) Microcontroller Unit (MCU) is an E... *
* The user programmable EPROM allows 'program changes and... *
* This low cost MCU has parallel I/O capability with pins... *
*
* * Internal 8-Bit Timer with 7-Bit Programmable Prescaler *
* * On-chip Oscillator *
* * Memory Mapped I/O *
* * Versatile Interrupt Handling *
* * Bit Manipulation *
* * Bit Test and Branch Instruction *
* * Vectored Interrupts *
* * Bootstrap Program in ROM *
* * 112 Bytes of RAM *
* * 3776 Bytes of EPROM *
* * 24110 Pins *
*****

PORT A I/O Lines

Connected to bus PA(7:0) for READ/WRITE.
PA(7:0) will output signals to IDB(7:0) via CHIP 3B if EPANS ...

PORTA                                     XREF[48]:  Clock_PB3_RMM_Return_Port_A:09b5.
                                                FUN_09c5:09c6(W),
                                                FUN_09ee:09f4(W),
                                                FUN_09ee:09fc(W),
                                                FUN_09ee:0a04(W),
                                                FUN_09ee:0a12(W),
                                                FUN_09ee:0a20(W),
                                                FUN_09ee:0a2c(W),
                                                FUN_09ee:0a3a(W),
                                                FUN_09ee:0a46(W),
                                                FUN_09ee:0a54(W),
                                                FUN_09ee:0a60(W),
                                                FUN_09ee:0a6e(W),
                                                FUN_09ee:0a7a(W),
                                                FUN_09ee:0a88(W),
                                                FUN_09ee:0a94(W),
                                                FUN_09ee:0aa2(W),
                                                FUN_09ee:0aaf(W),
                                                FUN_09ee:0ab7(W),
                                                FUN_0ac0:0ac6(W), [more]

0000 00          ??          00h

PORT B I/O Lines

PB0 = /WMM (clock signal to latch PA(7:0) data on chip 32B / ...
PB1 = /WRCLK (connected to MM58247 RTC pin 3 /WD)
PB2 = /ROCLK (connected to MM58247 RTC pin 2 /RD)
PB3 = /RMM (signal to somewhere(?) on the CPU board)
PB4 = STAT3
PB5 = STAT4
PB6 = READ (IDB:13)
PB7 = STAT7 => INVERTED TO PRES (IDB:15) <= PRESENT SIGNAL if...

PORTB                                     XREF[77]:  RESET:012c(W), RESET:0153(RW),
                                                RESET:0155(RW), RESET:0195(RW),
                                                FUN_06be:06d3(RW),
                                                FUN_06be:06e1(RW),
                                                FUN_06be:06ed(RW),
                                                FUN_06be:06fa(RW),
                                                FUN_06be:0712(RW),
                                                Clock_PB3_RMM_Return_Port_A:09b5.
                                                Clock_PB3_RMM_Return_Port_A:09c1.
                                                FUN_09c5:09cc(RW),
                                                FUN_09c5:09ce(RW),
                                                FUN_09ee:09f6(RW),
                                                FUN_09ee:09f8(RW),
                                                FUN_09ee:09fe(RW),
                                                FUN_09ee:0a00(RW),
                                                FUN_09ee:0a06(RW),
                                                FUN_09ee:0a08(RW),
                                                FUN_09ee:0a14(RW), [more]

0001 00          ??          00h
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PORT C I/O Lines

STAT 0-4 goew to IDB(8:11)

PC0 = STAT0
PC1 = STAT1
PC2 = STAT2

DISP1-5 is to control display ? Goes to bus /DP(5:1) via inve...

PC3 = DISP1
PC4 = DISP2
PC5 = DISP3
PC6 = DISP4
PC7 = DISP5

PORTC
XREF[13]: RESET:0134(W),
Read_Write_Port_C:04a7(R),
Read_Write_Port_C:04b0(W),
Read_Write_Port_C:04b2(W),
Read_Write_Port_C:04b6(W),
Read_Write_Port_C:04b8(RW),
Read_Write_Port_C:04be(W),
Read_Write_Port_C:04c0(RW),
Read_Write_Port_C:04c6(W),
Read_Write_Port_C:04cc(RW),
Read_Write_Port_C:04d5(W),
FUN_06be:06c5(R),
FUN_06be:06cb(W)

0002 00      ??      00h

PORT D *INPUT* lines
PIN PD6 can be /INT2

PD0 = PCR0
PD1 = PCR1
PD2 = PONI
PD3 = IONI
PD4 = LHIT
PD5 = LEV0 (Level 0. Meaning CPU inactive)
PD6 = HIGH (To avoid external interrupt to be triggered)
PD7 = /EMP
PORTD
XREF[5]: RESET:0158(R), RESET:01a2(R),
Read_Port_D:09af(R),
Read_Port_D:09b4(R),
Read_Port_D_Update_CLR:09d4(R)

0003 00      ??      00h

DDRA
XREF[8]: RESET:0128(W),
Read_Bytes_from_Port_A:0643(W),
FUN_09c5:09ca(W),
FUN_09c5:09d0(W),
FUN_09ee:09f0(W),
FUN_09ee:0abd(W),
FUN_0ac0:0ac2(W),
FUN_0ac0:0bdb(W)

0004 00      ??      00h

DDRB
XREF[2]: RESET:0130(W),
Read_Bytes_from_Port_A:0645(W)

0005 00      ??      00h

DDRC
XREF[2]: RESET:0138(W),
Read_Bytes_from_Port_A:0647(W)

0006 00      ??      00h

DDRD
XREF[1]: RESET:013a(W)

0007 00      ??      00h

TDR_Timer_Data_Register
XREF[3]: RESET:0145(W), RESET:0197(R),
TIMER_INTERRUPT:08ca(W)

0008 00      ??      00h
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Timer Control Register Bits

b7 - TIR - Timer Interrupt Request Status (Set to 1 when time...
b6 - TIM - Timer Interrupt MASK (1=Interrupt inhibited, 0=Int...
b5 - TIN - Timer Input Select (1=External clock, 0=Internal c...
b4 - TIE - Timer External Input Enable (1=Enable external tim...
b3- PSC - Prescaler Clear (Write only. Writing 1 resets the p...
b2 - PS2 - Prescaler select 2
b1 - PS1 - Prescaler select 1
b0 - PS0 - Prescaler select 0

PS2 PS1 PS0 - Divide by
0 0 0 1
0 0 1 2
0 1 0 4
0 1 1 8
1 0 0 16
1 0 1 32
1 1 0 64
1 1 1 128

MR_Misc_register (0009+1)
PCR_Program_Control_Register (0009+2)
TCR_Timer_Control_Register

0009 00 00 00 00 float 0.0
000d 00 ?? 00h
000e 00 ?? 00h
000f 00 ?? 00h

0x010 RAM START (112 bytes)
0x07F RAM END
RAM_0010

0010 00 ?? 00h

RAM_0011

0011 00 ?? 00h

RAM_0012

0012 00 ?? 00h

RAM_0013

0013 00 ?? 00h

RAM_0014

0014 00 ?? 00h

RAM_0015

0015 00 ?? 00h

XREF[3,3]: RESET:0141(W),
Read_Bytes_from_Port_A:063d(W),
TIMER_INTERRUPT:08cd(RW),
RESET:0149(W),
Read_Bytes_from_Port_A:0639(W),
Read_Bytes_from_Port_A:0641(W)

XREF[5]: FUN_01b4:01de(R),
Read_Bytes_from_Port_A:053d(W),
Read_Bytes_from_Port_A:0603(W),
Read_Bytes_from_Port_A:0618(W),
Read_Port_D:09ad(W)

XREF[6]: FUN_01b4:01d0(R),
Read_Bytes_from_Port_A:0543(W),
Read_Bytes_from_Port_A:0568(W),
Read_Bytes_from_Port_A:05fd(W),
Read_Bytes_from_Port_A:0613(W),
Read_Port_D:099c(W)

XREF[7]: FUN_01b4:01c2(R),
Read_Bytes_from_Port_A:0558(W),
Read_Bytes_from_Port_A:0561(W),
Read_Bytes_from_Port_A:056d(W),
Read_Bytes_from_Port_A:05b2(W),
Read_Bytes_from_Port_A:05f1(W),
Read_Bytes_from_Port_A:0622(W)

XREF[7]: FUN_01b4:01b4(R),
Read_Bytes_from_Port_A:0556(W),
Read_Bytes_from_Port_A:055f(W),
Read_Bytes_from_Port_A:0570(W),
Read_Bytes_from_Port_A:05be(W),
Read_Bytes_from_Port_A:05eb(W),
Read_Bytes_from_Port_A:061d(W)

XREF[6]: FUN_01ed:025f(R),
FUN_01ed:026d(R),
FUN_01ed:027b(R),
Read_Bytes_from_Port_A:051e(W),
Read_Bytes_from_Port_A:0589(W),
Read_Bytes_from_Port_A:05d0(W)

XREF[8]: FUN_01ed:0230(R),
FUN_01ed:0240(R),
FUN_01ed:024d(R),
FUN_01ed:025d(R),
FUN_01ed:02c8(R),
Read_Bytes_from_Port_A:0528(W),
Read_Bytes_from_Port_A:0587(W),
Read_Bytes_from_Port_A:05ce(W)

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	RAM_0016		XREF[11]:	FUN_01ed:0213(R), FUN_01ed:021f(R), FUN_01ed:022e(R), FUN_01ed:02aa(R), Read_CHR_OCHR:03ff(R), Read_CHR_OCHR:040d(R), Read_CHR_OCHR:041b(R), Read_Bytes_from_Port_A:0523(W), Read_Bytes_from_Port_A:0582(W), Read_Bytes_from_Port_A:05cd(W), FUN_066b:0670(R)
0016 00	??	00h		
	RAM_0017		XREF[13]:	Read_CHR_OCHR:03a9(R), Read_CHR_OCHR:03b9(R), Read_CHR_OCHR:03c6(R), Read_CHR_OCHR:03d9(R), Read_CHR_OCHR:03fd(R), Read_CHR_OCHR:045a(R), Read_Bytes_from_Port_A:0532(W), Read_Bytes_from_Port_A:057d(W), Read_Bytes_from_Port_A:059d(W), Read_Bytes_from_Port_A:05da(W), FUN_066b:067b(RW), FUN_066b:0687(W), FUN_066b:068e(RW)
0017 00	??	00h		
	DAT_0018		XREF[12]:	Read_CHR_OCHR:038c(R), Read_CHR_OCHR:0398(R), Read_CHR_OCHR:03a7(R), Read_CHR_OCHR:043c(R), Read_Bytes_from_Port_A:052d(W), Read_Bytes_from_Port_A:0578(W), Read_Bytes_from_Port_A:0597(W), Read_Bytes_from_Port_A:05d5(W), FUN_066b:067d(RW), FUN_066b:0682(R), FUN_066b:0689(W), FUN_066b:068c(RW)
0018 00	??	00h		
	PortD_PONI_IONI_bit_4_5		XREF[7]:	Read_Bytes_from_Port_A:05ad(R), Read_Bytes_from_Port_A:05b4(R), FUN_06b2:06b7(R), FUN_06b2:06bb(W), Read_Port_D_Update_CLR:09dd(W), Read_Port_D_Update_CLR:09e9(R), Read_Port_D_Update_CLR:09eh(W)
0019 00	??	00h		
	RAM_001a		XREF[5]:	RESET:014c(W), Read_Bytes_from_Port_A:053d(R), Read_Bytes_from_Port_A:0545(R), Read_Bytes_from_Port_A:054d(R), Read_Bytes_from_Port_A:060h(W)
001a 00	??	00h		
	DAT_001b		XREF[22]:	FUN_01ed:01ed(R), FUN_01ed:01f0(R), FUN_01ed:0202(R), FUN_01ed:029b(R), Read_CHR_OCHR:037b(R), Read_CHR_OCHR:03d6(R), Read_Bytes_from_Port_A:0534(RW), Read_Bytes_from_Port_A:0536(RW), Read_Bytes_from_Port_A:0538(RW), Read_Bytes_from_Port_A:058h(RW), Read_Bytes_from_Port_A:058d(RW), Read_Bytes_from_Port_A:058f(RW), Read_Bytes_from_Port_A:059e(RW), Read_Bytes_from_Port_A:05a0(RW), Read_Bytes_from_Port_A:05a2(RW), Read_Bytes_from_Port_A:05c3(RW), Read_Bytes_from_Port_A:05c5(RW), Read_Bytes_from_Port_A:05c7(RW), Read_Bytes_from_Port_A:064d(RW), Read_Bytes_from_Port_A:0652(RW), [more]
001b 00	??	00h		

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			RAM_001c		XREF[10]:	Read_Bytes_from_Port_A:04db(W), Read_Bytes_from_Port_A:04dd(R), Read_Bytes_from_Port_A:04e3(R), FUN_06be:06be(R), FUN_06be:06c3(W), FUN_06be:06c9(R), FUN_06be:06da(W), FUN_06be:06e6(R), FUN_06be:06ff(R), FUN_06be:070b(R)
001c	00	??		00h		
			RAM_001d		XREF[10]:	FUN_01ed:01f9(R), Read_CHR_OCHR:037e(R), Read_Bytes_from_Port_A:04ed(R), Read_Bytes_from_Port_A:05a6(W), Read_Bytes_from_Port_A:062a(W), Read_Bytes_from_Port_A:0654(R), Read_Bytes_from_Port_A:0662(R), Read_Bytes_from_Port_A:0666(W), FUN_0694:0695(R), FUN_0694:06a1(R)
001d	00	??		00h		
			RAM_001e		XREF[7]:	Read_Bytes_from_Port_A:05aa(W), Read_Bytes_from_Port_A:062f(W), Read_Bytes_from_Port_A:0657(RW), Read_Bytes_from_Port_A:065a(RW), Read_Bytes_from_Port_A:065d(R), Read_Bytes_from_Port_A:0660(W), FUN_066b:0674(R)
001e	00	??		00h		
			RAM_001f		XREF[2]:	RESET:0150(W), RESET:015b(RW)
001f	00	??		00h		
			DAT_0020		XREF[15]:	FUN_01ed:0323(R), FUN_01ed:0348(R), FUN_0715:074a(RW), FUN_0715:0761(R), FUN_0715:0765(W), FUN_0803:0803(R), FUN_0803:0807(W), FUN_0803:081f(R), FUN_0803:08c1(R), FUN_0803:08c5(W), FUN_08f9:093b(RW), FUN_09ee:0a18(R), FUN_0ac0:0ae8(W), FUN_0ac0:0aff(R), FUN_0ac0:0b01(W)
0020	00	??		00h		
			DAT_0021		XREF[14]:	FUN_01ed:035d(R), FUN_01ed:036b(R), FUN_0715:0767(RW), FUN_0715:0785(RW), FUN_0715:0787(R), FUN_0803:0840(R), FUN_08f9:0917(R), FUN_08f9:0931(RW), FUN_08f9:0933(R), FUN_08f9:0939(W), FUN_09ee:0a32(R), FUN_0ac0:0b0f(W), FUN_0ac0:0b26(R), FUN_0ac0:0b28(W)
0021	00	??		00h		
			DAT_0022		XREF[9]:	FUN_0715:07aa(W), FUN_0803:085a(R), FUN_08f9:0925(RW), FUN_08f9:0927(R), FUN_08f9:092d(W), FUN_09ee:0a4c(R), FUN_0ac0:0b36(W), FUN_0ac0:0b4d(R), FUN_0ac0:0b4f(W)
0022	00	??		00h		

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	DAT_0023			XREF[10]:	FUN_0715:07a7(W), FUN_0715:07d2(RW), FUN_0803:086e(R), FUN_08f9:090f(RW), FUN_08f9:0911(R), FUN_08f9:0915(W), FUN_09ee:0a66(R), FUN_0ac0:0b5d(W), FUN_0ac0:0b74(R), FUN_0ac0:0b76(W)
0023	00	??	00h		
	DAT_0024			XREF[12]:	FUN_01ed:02ef(R), FUN_01ed:0302(R), FUN_0715:0717(W), FUN_0715:07fa(RW), FUN_0803:0894(R), FUN_08f9:0905(RW), FUN_08f9:0907(R), FUN_08f9:090b(W), FUN_09ee:0a80(R), FUN_0ac0:0b84(W), FUN_0ac0:0b9b(R), FUN_0ac0:0b9d(W)
0024	00	undefined1	00h		
	DAT_0025			XREF[13]:	FUN_01ed:02ef(R), FUN_01ed:0302(R), FUN_0715:0717(W), FUN_0715:0800(W), FUN_0803:08af(R), FUN_08f9:08fb(RW), FUN_08f9:08fd(R), FUN_08f9:0901(W), FUN_09ee:0a9a(R), FUN_09ee:0aa8(R), FUN_0ac0:0bab(W), FUN_0ac0:0bc2(R), FUN_0ac0:0bc4(W)
0025	00	undefined1	00h		
	DAT_0026			XREF[16]:	FUN_0715:0726(W), FUN_0715:074c(RW), FUN_0715:074e(R), FUN_0715:0755(W), FUN_0715:0790(R), FUN_0803:0811(W), FUN_0803:0833(RW), FUN_0803:0835(R), FUN_0803:083c(W), FUN_0803:0842(R), FUN_08f9:0920(R), FUN_08f9:093d(RW), FUN_08f9:093f(R), FUN_08f9:0945(W), FUN_09ee:0a0a(R), FUN_0ac0:0ada(W)
0026	00	??	00h		
0027	00	??	00h		
0028	00	??	00h		
0029	00	??	00h		
002a	00	??	00h		
002b	00	??	00h		
002c	00	??	00h		
002d	00	??	00h		
002e	00	??	00h		
002f	00	??	00h		
0030	00	??	00h		
0031	00	??	00h		
0032	00	??	00h		
0033	00	??	00h		
0034	00	??	00h		
0035	00	??	00h		
0036	00	??	00h		
0037	00	??	00h		
0038	00	??	00h		
0039	00	??	00h		
003a	00	??	00h		
003b	00	??	00h		
003c	00	??	00h		
003d	00	??	00h		
003e	00	??	00h		
003f	00	??	00h		
0040	00	??	00h		

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0041 00	??	00h		
0042 00	??	00h		
	DAT_0043		XREF[1]:	Read_Write_Port_C:04bcd(R)
0043 00	undefined1	00h		
	DAT_0044		XREF[1]:	Read_Write_Port_C:04bcd(R)
0044 00	undefined1	00h		
	DAT_0045		XREF[3]:	RESET:0166(W), Read_Write_Port_C:04b4(R), Read_Write_Port_C:04c8(R)
0045 00	undefined1	00h		
	PortC_ValueToWrite		XREF[5]:	RESET:0166(W), Something_RAM_050_051:0496(R), Something_RAM_050_051:0498(W), Read_Write_Port_C:04ab(R), Read_Write_Port_C:04c8(R)
0046 00	undefined1	00h		
	DAT_0047		XREF[3]:	FUN_0715:0728(R), FUN_0803:0868(W), FUN_0803:087c(RW)
0047 00	??	00h		
	DAT_0048		XREF[4]:	FUN_0715:072c(R), FUN_0803:086c(W), FUN_0803:0876(RW), FUN_0803:0878(R)
0048 00	??	00h		
	DAT_0049		XREF[2]:	FUN_0715:07ac(R), FUN_0803:08bb(W)
0049 00	??	00h		
	DAT_004a		XREF[2]:	FUN_0715:07b0(R), FUN_0803:08bf(W)
004a 00	??	00h		
	DAT_004b		XREF[17]:	RESET:0170(W), RESET:017e(W), RESET:018c(W), Something_RAM_050_051:048a(R), FUN_0715:071e(W), FUN_0715:0740(R), FUN_0715:0769(W), FUN_0715:077f(R), FUN_0715:07b6(W), FUN_0715:07cc(R), FUN_0715:07de(W), FUN_0715:07f4(R), FUN_0803:0815(W), FUN_0803:082b(R), FUN_0803:088e(W), FUN_0803:08a4(R), FUN_0803:08aa(RW)
004b 00	??	00h		
	DAT_004c		XREF[24]:	RESET:0174(W), RESET:0182(W), RESET:0190(W), Something_RAM_050_051:0490(R), FUN_0715:0722(W), FUN_0715:073a(R), FUN_0715:0746(W), FUN_0715:0751(RW), FUN_0715:0753(RW), FUN_0715:076d(W), FUN_0715:0779(R), FUN_0715:0796(W), FUN_0715:07ba(W), FUN_0715:07c6(R), FUN_0715:07e2(W), FUN_0715:07ee(R), FUN_0803:0819(W), FUN_0803:0825(R), FUN_0803:0831(W), FUN_0803:0838(RW), [more]
004c 00	??	00h		

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	DAT_004d			XREF[29]:	FUN_0715:072a(W), FUN_0715:0730(R), FUN_0715:073e(R), FUN_0715:0742(W), FUN_0715:075b(W), FUN_0715:076f(R), FUN_0715:077d(R), FUN_0715:0781(W), FUN_0715:079c(W), FUN_0715:07ae(W), FUN_0715:07bc(R), FUN_0715:07ca(R), FUN_0715:07ce(W), FUN_0715:07d8(W), FUN_0715:07e4(R), FUN_0715:07f2(R), FUN_0715:07f6(W), FUN_0803:0809(W), FUN_0803:0829(R), FUN_0803:082d(W), [more]
004d 00	??	00h			
	DAT_004e			XREF[31]:	FUN_0715:072e(W), FUN_0715:0734(R), FUN_0715:0738(R), FUN_0715:073c(W), FUN_0715:075f(W), FUN_0715:0773(R), FUN_0715:0777(R), FUN_0715:077b(W), FUN_0715:07a0(W), FUN_0715:07b2(W), FUN_0715:07c0(R), FUN_0715:07c4(R), FUN_0715:07c8(W), FUN_0715:07dc(W), FUN_0715:07e8(R), FUN_0715:07ec(R), FUN_0715:07f0(W), FUN_0803:080b(W), FUN_0803:0823(R), FUN_0803:0827(W), [more]
004e 00	??	00h			
	DAT_004f			XREF[11]:	FUN_01ed:02e9(W), FUN_01ed:02fc(R), FUN_01ed:030e(RW), FUN_01ed:0310(R), FUN_01ed:0314(RW), Something_RAM_050_051:047b(W), Something_RAM_050_051:0492(R), Something_RAM_050_051:0494(RW), FUN_0803:080d(W), FUN_0803:081b(RW), FUN_0803:081d(R)
004f 00	??	00h			
	DAT_0050			XREF[2]:	Something_RAM_050_051:0483(W), Something_RAM_050_051:049c(RW)
0050 00	??	00h			
	DAT_0051			XREF[20]:	FUN_01ed:02b0(W), FUN_01ed:02bc(R), FUN_01ed:02c4(RW), FUN_01ed:02ce(W), FUN_01ed:02da(R), FUN_01ed:02e2(RW), FUN_01ed:02ed(W), FUN_01ed:0300(R), FUN_01ed:0316(RW), FUN_01ed:0318(R), Read_CHR_OCHR:0442(W), Read_CHR_OCHR:044e(R), Read_CHR_OCHR:0456(RW), Read_CHR_OCHR:0460(W), Read_CHR_OCHR:046c(R), Read_CHR_OCHR:0474(RW), Something_RAM_050_051:0475(W), Something_RAM_050_051:049a(R), Something_RAM_050_051:04a0(RW), Something_RAM_050_051:04a2(R)
0051 00	??	00h			

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	DAT_0052			XREF[37]:	FUN_01b4:01ea(W), FUN_01ed:0289(W), FUN_01ed:02a1(W), FUN_01ed:032b(W), FUN_01ed:0335(W), Read_CHR_OCHR:03e9(W), Read_CHR_OCHR:0429(W), FUN_0715:0732(W), FUN_0715:0759(R), FUN_0715:0771(W), FUN_0715:079a(R), FUN_0715:07be(W), FUN_0715:07d6(R), FUN_0715:07e6(W), FUN_0803:0896(W), FUN_0803:0898(RW), FUN_0ac0:0afa(W), FUN_0ac0:0afd(R), FUN_0ac0:0b21(W), FUN_0ac0:0b24(R), [more]
0052 00	??	00h			
	DAT_0053			XREF[15]:	FUN_01b4:01e4(W), FUN_01ed:0279(W), FUN_01ed:02a5(W), FUN_01ed:032f(W), FUN_01ed:0339(W), Read_CHR_OCHR:03e7(W), Read_CHR_OCHR:0419(W), FUN_0715:0736(W), FUN_0715:075d(R), FUN_0715:0775(W), FUN_0715:079e(R), FUN_0715:07c2(W), FUN_0715:07da(R), FUN_0715:07ea(W), FUN_0715:07fe(R)
0053 00	??	00h			
	DAT_0054			XREF[9]:	FUN_01b4:01dc(W), FUN_01ed:026b(W), FUN_01ed:02dc(W), FUN_01ed:0346(W), Read_CHR_OCHR:03e5(W), Read_CHR_OCHR:03ef(R), Read_CHR_OCHR:03f3(W), Read_CHR_OCHR:040b(W), Read_CHR_OCHR:046e(W)
0054 00	undefined1	00h			
	DAT_0055			XREF[11]:	FUN_01b4:01d6(W), FUN_01ed:020e(W), FUN_01ed:025b(W), FUN_01ed:02a7(RW), FUN_01ed:02dc(W), FUN_01ed:0320(RW), FUN_01ed:0355(W), Read_CHR_OCHR:03d4(W), Read_CHR_OCHR:03ef(R), Read_CHR_OCHR:046e(W), Something_RAM_050_051:048d(RW)
0055 00	undefined1	00h			
	DAT_0056			XREF[6]:	FUN_01b4:01ce(W), FUN_01ed:0210(W), FUN_01ed:024b(W), FUN_01ed:0312(W), FUN_01ed:0357(W), Read_CHR_OCHR:03c4(W)
0056 00	undefined1	00h			
	DAT_0057			XREF[6]:	FUN_01b4:01c8(W), FUN_01ed:023e(W), FUN_01ed:02fe(W), FUN_01ed:031e(RW), FUN_01ed:035b(W), Read_CHR_OCHR:03b7(W)
0057 00	undefined1	00h			

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	DAT_0058			XREF[9]:	FUN_01b4:01c0(W), FUN_01ed:0207(W), FUN_01ed:022c(W), FUN_01ed:02be(W), FUN_01ed:0312(W), FUN_01ed:0369(W), Read_CHR_OCHR:03a5(W), Read_CHR_OCHR:0450(W), Something_RAM_050_051:0486(RW)
0058 00	undefined1	00h			
	DAT_0059			XREF[10]:	FUN_01b4:01ba(W), FUN_01ed:0207(W), FUN_01ed:021d(W), FUN_01ed:02be(W), FUN_01ed:02fe(W), FUN_01ed:031c(RW), FUN_01ed:0378(W), Read_CHR_OCHR:0396(W), Read_CHR_OCHR:0450(W), Something_RAM_050_051:0486(RW)
0059 00	undefined1	00h			
	PortC_SavedValue			XREF[18]:	FUN_01ed:02ac(W), FUN_01ed:02b2(R), FUN_01ed:02c0(RW), FUN_01ed:02c2(RW), FUN_01ed:02ca(W), FUN_01ed:02d0(R), FUN_01ed:02de(RW), FUN_01ed:02e0(RW), Read_CHR_OCHR:043e(W), Read_CHR_OCHR:0444(R), Read_CHR_OCHR:0452(RW), Read_CHR_OCHR:0454(RW), Read_CHR_OCHR:045c(W), Read_CHR_OCHR:0462(R), Read_CHR_OCHR:0470(RW), Read_CHR_OCHR:0472(RW), Read_Write_Port_C:04a9(W), Read_Write_Port_C:04d3(R)
005a 00	??	00h			
	RAM_005b			XREF[3]:	RESET:011a(W), TIMER_INTERRUPT:08da(RW), TIMER_INTERRUPT:08e3(W)
005b 00	??	00h			
	RAM_005c			XREF[3]:	RESET:011e(W), TIMER_INTERRUPT:08e5(RW), TIMER_INTERRUPT:08eb(W)
005c 00	??	00h			
	RAM_005D			XREF[3]:	RESET:0122(W), TIMER_INTERRUPT:08ed(RW), TIMER_INTERRUPT:08f3(W)
005d 00	??	00h			
	DAT_005e			XREF[1]:	FUN_0948:0980(R)
005e 00	??	00h			
	DAT_005f			XREF[1]:	FUN_0948:097c(R)
005f 00	??	00h			
	DAT_0060			XREF[3]:	FUN_06b2:06b5(R), FUN_06b2:06b9(W), FUN_0948:0978(R)
0060 00	??	00h			
	DAT_0061			XREF[2]:	FUN_0948:0974(R), FUN_0948:0982(W)
0061 00	??	00h			
	DAT_0062			XREF[2]:	FUN_0948:0970(R), FUN_0948:097e(W)
0062 00	??	00h			
	DAT_0063			XREF[2]:	FUN_0948:096c(R), FUN_0948:097a(W)
0063 00	??	00h			
	DAT_0064			XREF[2]:	FUN_0948:0968(R), FUN_0948:0976(W)
0064 00	??	00h			

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		DAT_0065		XREF[2]:	FUN_0948:0964(R), FUN_0948:0972(W)
0065	00	??	00h		
		DAT_0066		XREF[2]:	FUN_0948:0960(R), FUN_0948:096e(W)
0066	00	??	00h		
		DAT_0067		XREF[2]:	FUN_0948:095c(R), FUN_0948:096a(W)
0067	00	??	00h		
		DAT_0068		XREF[2]:	FUN_0948:0958(R), FUN_0948:0966(W)
0068	00	??	00h		
		DAT_0069		XREF[2]:	FUN_0948:0954(R), FUN_0948:0962(W)
0069	00	??	00h		
		DAT_006a		XREF[2]:	FUN_0948:0950(R), FUN_0948:095e(W)
006a	00	??	00h		
		DAT_006b		XREF[2]:	FUN_0948:094c(R), FUN_0948:095a(W)
006b	00	??	00h		
		DAT_006c		XREF[2]:	FUN_0948:0948(R), FUN_0948:0956(W)
006c	00	??	00h		
		DAT_006d		XREF[1]:	FUN_0948:0952(W)
006d	00	??	00h		
		DAT_006e		XREF[1]:	FUN_0948:094e(W)
006e	00	??	00h		
		DAT_006f		XREF[2]:	FUN_06b2:06b2(R), FUN_0948:094a(W)
006f	00	??	00h		
		PortD_SavedValue		XREF[3]:	Read_Port_D_Update_CLR:09d6(W), Read_Port_D_Update_CLR:09de(R), Read_Port_D_Update_CLR:09e1(R)
0070	00	??	00h		
		RAM_0071		XREF[3]:	RESET:0126(W), Read_Port_D:0985(RW), Read_Port_D:098b(W)
0071	00	??	00h		
		DAT_0072		XREF[3]:	Read_Port_D:098d(R), Read_Port_D:0993(W), Read_Port_D:09b2(RW)
0072	00	??	00h		
		DAT_0073		XREF[3]:	Read_Port_D:099e(R), Read_Port_D:09a4(W), Read_Port_D:09b7(RW)
0073	00	??	00h		
0074	00	??	00h		
0075	00	??	00h		
0076	00	??	00h		
0077	00	??	00h		
0078	00	??	00h		
0079	00	??	00h		
007a	00	??	00h		
007b	00	??	00h		
007c	00	??	00h		
007d	00	??	00h		
007e	00	??	00h		
007f	00	??	00h		
		RAM ENDS AT 0x007F			

		ROM START AT 0x0080			
		ENDS AT 0x00FF			
		DAT_0080		XREF[2]:	FUN_0715:071c(R), FUN_0803:0813(R)
0080	02	??	02h		

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	DAT_0081			XREF[2]:	FUN_0715:0720(R), FUN_0803:0817(R)
0081	da	??	DAh		
	DAT_0082			XREF[1]:	FUN_0715:07b4(R)
0082	0e	??	0Eh		
	DAT_0083			XREF[1]:	FUN_0715:07b8(R)
0083	10	??	10h		
0084	07	??	07h		
0085	bb	??	BBh		
0086	00	??	00h		
0087	10	??	10h		
0088	20	??	20h		
0089	30	??	30h		0
008a	40	??	40h		@
008b	50	??	50h		P
008c	60	??	60h		`
008d	70	??	70h		p
	DAT_008e			XREF[1]:	RESET:0113(W)
008e	00	undefined1	00h		
	DAT_008f			XREF[1]:	RESET:0113(W)
008f	10	undefined1	10h		
0090	20	??	20h		
0091	30	??	30h		0
0092	40	??	40h		@
0093	50	??	50h		P
0094	60	??	60h		`
0095	70	??	70h		p
0096	00	??	00h		
0097	10	??	10h		
0098	20	??	20h		
0099	30	??	30h		0
009a	40	??	40h		@
009b	50	??	50h		P
009c	60	??	60h		`
009d	70	??	70h		p
009e	00	??	00h		
009f	10	??	10h		
00a0	20	??	20h		
00a1	30	??	30h		0
00a2	40	??	40h		@
00a3	50	??	50h		P
00a4	60	??	60h		`
00a5	70	??	70h		p
00a6	00	??	00h		
00a7	10	??	10h		
00a8	20	??	20h		
00a9	30	??	30h		0
00aa	40	??	40h		@
00ab	50	??	50h		P
00ac	60	??	60h		`
00ad	70	??	70h		p
00ae	00	??	00h		
00af	10	??	10h		
00b0	20	??	20h		
00b1	30	??	30h		0
00b2	40	??	40h		@
00b3	50	??	50h		P
00b4	60	??	60h		`
00b5	70	??	70h		p
00b6	00	??	00h		
00b7	10	??	10h		
00b8	20	??	20h		
00b9	30	??	30h		0
00ba	40	??	40h		@
00bb	50	??	50h		P
00bc	60	??	60h		`
00bd	70	??	70h		p
00be	00	??	00h		
00bf	10	??	10h		
00c0	20	??	20h		
00c1	30	??	30h		0
00c2	40	??	40h		@
00c3	50	??	50h		P
00c4	60	??	60h		`
00c5	70	??	70h		p
00c6	00	??	00h		
00c7	10	??	10h		
00c8	20	??	20h		
00c9	30	??	30h		0
00ca	40	??	40h		@

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```

00cb 50    ??    50h  P
00cc 60    ??    60h  `
00cd 70    ??    70h  p
00ce 00    ??    00h
00cf 10    ??    10h
00d0 20    ??    20h
00d1 30    ??    30h  0
00d2 40    ??    40h  @
00d3 50    ??    50h  P
00d4 60    ??    60h  `
00d5 70    ??    70h  p
00d6 00    ??    00h
00d7 10    ??    10h
00d8 20    ??    20h
00d9 30    ??    30h  0
00da 40    ??    40h  @
00db 50    ??    50h  P
00dc 60    ??    60h  `
00dd 70    ??    70h  p
00de 00    ??    00h
00df 10    ??    10h
00e0 20    ??    20h
00e1 30    ??    30h  0
00e2 40    ??    40h  @
00e3 50    ??    50h  P
00e4 60    ??    60h  `
00e5 70    ??    70h  p
00e6 00    ??    00h
00e7 10    ??    10h
00e8 20    ??    20h
00e9 30    ??    30h  0
00ea 40    ??    40h  @
00eb 50    ??    50h  P
00ec 60    ??    60h  `
00ed 70    ??    70h  p
00ee 00    ??    00h
00ef 10    ??    10h
00f0 20    ??    20h
00f1 30    ??    30h  0
00f2 40    ??    40h  @
00f3 50    ??    50h  P
00f4 60    ??    60h  `
00f5 70    ??    70h  p
00f6 00    ??    00h
00f7 10    ??    10h
00f8 20    ??    20h
00f9 30    ??    30h  0
00fa 40    ??    40h  @
00fb 50    ??    50h  P
00fc 60    ??    60h  `
00fd 70    ??    70h  p

RAM_00fe

XREF[9]:  RESET:013c(W), RESET:0161(W),
          RESET:016b(W), RESET:0176(W),
          RESET:0179(W), RESET:0184(W),
          RESET:0187(W), RESET:0192(W),
          RESET:019e(W)

00fe 00 10    undefined2 0010h
0100 20    ??    20h
0101 30    ??    30h  0
0102 40    ??    40h  @
0103 50    ??    50h  P
0104 60    ??    60h  `
0105 70    ??    70h  p
0106 00    ??    00h
0107 00    ??    00h
0108 00    ??    00h
0109 00    ??    00h
010a 00    ??    00h
010b 00    ??    00h
010c 00    ??    00h
010d 00    ??    00h
010e 00    ??    00h
010f 00    ??    00h

*****
*                               *
*                               *
*****
undefined RESET()
A:1    <RETURN>
RESET
XREF[2]:  Read_Bytes_from_Port_A:0649(c),
          0ffe(*)

0110 9c    RSP
0111 ae 7f    LDX    #0x7f

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0113 6f 10	CLR	<u>0x10,X=>DAT_008f</u>	XREF[1]:	0116(j)
0115 5a	DECX			= 10h
0116 2a fb	BPL	LAB_0113		
0118 a6 10	LDA	#0x10		
011a b7 5b	STA	RAM_005b		
011c a6 c8	LDA	#0xc8		
011e b7 5c	STA	RAM_005c		
0120 a6 02	LDA	#0x2		
0122 b7 5d	STA	RAM_005D		
0124 a6 80	LDA	#0x80		
0126 b7 71	STA	RAM_0071		
0128 3f 04	CLR	DDRA		
012a a6 2f	LDA	#0x2f		
012c b7 01	STA	PORTB		
012e a6 ff	LDA	#0xff		
0130 b7 05	STA	DDRB		
0132 a6 f8	LDA	#0xf8		
0134 b7 02	STA	PORTC		
0136 a6 ff	LDA	#0xff		
0138 b7 06	STA	DDRC		
013a 3f 07	CLR	DDRD		
013c cd 0a c0	JSR	<u>FUN_0ac0</u>		= 0010h undefined FUN_0ac0()
013f a6 35	LDA	#0x35		
0141 b7 09	STA	TCR_Timer_Control_Register		= 0.0
0143 a6 03	LDA	#0x3		
0145 b7 08	STA	TDR_Timer_Data_Register		
0147 a6 7f	LDA	#0x7f		
0149 b7 0a	STA	MR_Misc_register		
014b 9a	CLI			
014c 3f 1a	CLR	RAM_001a		
014e a6 ff	LDA	#0xff	XREF[2]:	015f(j), 01b1(j)
0150 b7 1f	STA	RAM_001f		
0152 9b	SEI			
0153 18 01	BSET	0x4,PORTB		
0155 19 01	BCLR	0x4,PORTB		
0157 9a	CLI			
0158 0e 03 06	BRSET	0x7,PORTD,LAB_0161	XREF[1]:	015d(j)
015b 3a 1f	DEC	RAM_001f		
015d 26 f9	BNE	LAB_0158		
015f 20 ed	BRA	LAB_014e		
0161 cd 04 d8	JSR	<u>Read_Bytes_from_Port_A</u>	XREF[2]:	0158(j), 01a2(j) = 0010h undefined Read_Bytes_from_Port_A()
0164 ae 1f	LDX	#0x1f		
0166 6f 27	CLR	<u>0x27,X=>PortC_ValueToWrite</u>	XREF[1]:	0169(j)
0168 5a	DECX			
0169 2a fb	BPL	LAB_0166		
016b cd 01 b4	JSR	<u>FUN_01b4</u>		= 0010h undefined FUN_01b4()
016e a6 40	LDA	#0x40		
0170 b7 4b	STA	DAT_004b		
0172 a6 04	LDA	#0x4		
0174 b7 4c	STA	DAT_004c		
0176 cd 04 79	JSR	<u>Something_RAM_050_051</u>		= 0010h undefined Something_RAM_050_051()
0179 cd 01 ed	JSR	<u>FUN_01ed</u>		= 0010h undefined FUN_01ed()
017c a6 20	LDA	#0x20		
017e b7 4b	STA	DAT_004b		
0180 a6 02	LDA	#0x2		
0182 b7 4c	STA	DAT_004c		
0184 cd 04 79	JSR	<u>Something_RAM_050_051</u>		= 0010h undefined Something_RAM_050_051()
0187 cd 03 7b	JSR	<u>Read_CHR_OCHR</u>		= 0010h undefined Read_CHR_OCHR()
018a a6 10	LDA	#0x10		
018c b7 4b	STA	DAT_004b		
018e a6 01	LDA	#0x1		
0190 b7 4c	STA	DAT_004c		
0192 cd 04 79	JSR	<u>Something_RAM_050_051</u>		= 0010h undefined Something_RAM_050_051()
0195 1b 01	BCLR	0x5,PORTB		
0197 b6 08	LDA	TDR_Timer_Data_Register	XREF[1]:	019b(j)

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0199 a1 03      CMP      #0x3
019b 26 fa      BNE      LAB_0197
019d 9b         SEI
019e cd 04 a7   JSR      Read_Write_Port_C           = 0010h
                                                    voidRead_Write_Port_C(void)

01a1 9a         CLI
01a2 0e 03 bc   BRSET    0x7,PORTD,LAB_0161
01a5 ae 20      LDX      #0x20

                                LAB_01a7
                                XREF[1]: 01af(j)
01a7 9f         TXA
01a8 ae 0a      LDX      #0xa

                                LAB_01aa
                                XREF[1]: 01ab(j)
01aa 5a         DECX
01ab 26 fd      BNE      LAB_01aa
01ad 97         TAX
01ae 5a         DECX
01af 26 f6      BNE      LAB_01a7
01b1 cc 01 4e   JMP      LAB_014e

*****
*                               FUNCTION                               *
*****
undefined FUN_01b4()
A:1      <RETURN>
FUN_01b4      XREF[1]: RESET:016b(c)
01b4 be 13      LDX      RAM_0013
01b6 58         ASLX
01b7 d6 0d 22   LDA      DAT_0d22,X
01ba b7 59      STA      DAT_0059
01bc 5c         INCX
01bd d6 0d 22   LDA      DAT_0d22,X
01c0 b7 58      STA      DAT_0058
01c2 be 12      LDX      RAM_0012
01c4 58         ASLX
01c5 d6 0d 22   LDA      DAT_0d22,X
01c8 b7 57      STA      DAT_0057
01ca 5c         INCX
01cb d6 0d 22   LDA      DAT_0d22,X
01ce b7 56      STA      DAT_0056
01d0 be 11      LDX      RAM_0011
01d2 58         ASLX
01d3 d6 0d 22   LDA      DAT_0d22,X
01d6 b7 55      STA      DAT_0055
01d8 5c         INCX
01d9 d6 0d 22   LDA      DAT_0d22,X
01dc b7 54      STA      DAT_0054
01de be 10      LDX      RAM_0010
01e0 58         ASLX
01e1 d6 0d 22   LDA      DAT_0d22,X
01e4 b7 53      STA      DAT_0053
01e6 5c         INCX
01e7 d6 0d 22   LDA      DAT_0d22,X
01ea b7 52      STA      DAT_0052
01ec 81         RTS

*****
*                               FUNCTION                               *
*****
undefined FUN_01ed()
A:1      <RETURN>
FUN_01ed      XREF[1]: RESET:0179(c)
01ed 03 1b 09   BRCLR    0x1,DAT_001b,LAB_01f9
01f0 04 1b 03   BRSET    0x2,DAT_001b,LAB_01f6
01f3 cc 02 e7   JMP      LAB_02e7

                                LAB_01f6
                                XREF[1]: 01f0(j)
01f6 cc 03 23   JMP      LAB_0323

                                LAB_01f9
                                XREF[1]: 01ed(j)
01f9 b6 1d      LDA      RAM_001d
01fb a4 03      AND      #0x3
01fd 27 03      BEQ      LAB_0202
01ff cc 02 aa   JMP      LAB_02aa

                                LAB_0202
                                XREF[1]: 01fd(j)
0202 07 1b 0e   BRCLR    0x3,DAT_001b,LAB_0213
0205 ae 07      LDX      #0x7

                                LAB_0207
                                XREF[1]: 020a(j)
0207 6f 52      CLR      0x52,X=>DAT_0059
0209 5a         DECX
020a 2a fb      BPL      LAB_0207
020c a6 08      LDA      #0x8

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020e	b7 55	STA	DAT_0055	
0210	b7 56	STA	DAT_0056	
0212	81	RTS		
		LAB_0213		XREF[1]: 0202(j)
0213	b6 16	LDA	RAM_0016	
0215	a4 07	AND	#0x7	
0217	ab 10	ADD	#0x10	
0219	97	TAX		
021a	d6 0c f8	LDA	DAT_0cf8,X	
021d	b7 59	STA	DAT_0059	
021f	b6 16	LDA	RAM_0016	
0221	44	LSRA		
0222	44	LSRA		
0223	44	LSRA		
0224	a4 07	AND	#0x7	
0226	ab 10	ADD	#0x10	
0228	97	TAX		
0229	d6 0c f8	LDA	DAT_0cf8,X	
022c	b7 58	STA	DAT_0058	
022e	be 16	LDX	RAM_0016	
0230	b6 15	LDA	RAM_0015	
0232	58	ASLX		
0233	49	ROLA		
0234	58	ASLX		
0235	49	ROLA		
0236	a4 07	AND	#0x7	
0238	ab 10	ADD	#0x10	
023a	97	TAX		
023b	d6 0c f8	LDA	DAT_0cf8,X	
023e	b7 57	STA	DAT_0057	
0240	b6 15	LDA	RAM_0015	
0242	44	LSRA		
0243	a4 07	AND	#0x7	
0245	ab 10	ADD	#0x10	
0247	97	TAX		
0248	d6 0c f8	LDA	DAT_0cf8,X	
024b	b7 56	STA	DAT_0056	
024d	b6 15	LDA	RAM_0015	
024f	44	LSRA		
0250	44	LSRA		
0251	44	LSRA		
0252	44	LSRA		
0253	a4 07	AND	#0x7	
0255	ab 10	ADD	#0x10	
0257	97	TAX		
0258	d6 0c f8	LDA	DAT_0cf8,X	
025b	b7 55	STA	DAT_0055	
025d	be 15	LDX	RAM_0015	
025f	b6 14	LDA	RAM_0014	
0261	58	ASLX		
0262	49	ROLA		
0263	a4 07	AND	#0x7	
0265	ab 10	ADD	#0x10	
0267	97	TAX		
0268	d6 0c f8	LDA	DAT_0cf8,X	
026b	b7 54	STA	DAT_0054	
026d	b6 14	LDA	RAM_0014	
026f	44	LSRA		
0270	44	LSRA		
0271	a4 07	AND	#0x7	
0273	ab 10	ADD	#0x10	
0275	97	TAX		
0276	d6 0c f8	LDA	DAT_0cf8,X	
0279	b7 53	STA	DAT_0053	
027b	b6 14	LDA	RAM_0014	
027d	48	ASLA		
027e	49	ROLA		
027f	49	ROLA		
0280	49	ROLA		
0281	a4 07	AND	#0x7	
0283	ab 10	ADD	#0x10	
0285	97	TAX		
0286	d6 0c f8	LDA	DAT_0cf8,X	
0289	b7 52	STA	DAT_0052	
028b	5f	CLRX		
028c	a6 77	LDA	#0x77	
		LAB_028e		XREF[1]: 0299(j)
028e	e1 52	CMP	0x52,X	
0290	26 09	BNE	LAB_029b	
0292	6f 52	CLR	0x52,X	
0294	5c	INCX		
0295	a3 07	CPX	#0x7	
0297	27 02	BEQ	LAB_029b	

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0299 20 f3	BRA	LAB_028e		
		LAB_029b		
029b 08 1b 01	BRSET	0x4,DAT_001b,LAB_029f	XREF[2]:	0290(j), 0297(j)
029e 81	RTS			
		LAB_029f	XREF[1]:	029b(j)
029f a6 4f	LDA	#0x4f		
02a1 b7 52	STA	DAT_0052		
02a3 a6 66	LDA	#0x66		
02a5 b7 53	STA	DAT_0053		
02a7 1e 55	BSET	0x7,DAT_0055		
02a9 81	RTS			
		LAB_02aa	XREF[1]:	01ff(j)
02aa b6 16	LDA	RAM_0016		
02ac b7 5a	STA	PortC_SavedValue		
02ae ae 03	LDX	#0x3		
02b0 bf 51	STX	DAT_0051		
		LAB_02b2	XREF[1]:	02c6(j)
02b2 b6 5a	LDA	PortC_SavedValue		
02b4 a4 03	AND	#0x3		
02b6 ab 04	ADD	#0x4		
02b8 97	TAX			
02b9 d6 0c f8	LDA	DAT_0cf8,X		
02bc be 51	LDX	DAT_0051		
02be e7 56	STA	0x56,X=>DAT_0059		
02c0 34 5a	LSR	PortC_SavedValue		
02c2 34 5a	LSR	PortC_SavedValue		
02c4 3a 51	DEC	DAT_0051		
02c6 2a ea	BPL	LAB_02b2		
02c8 b6 15	LDA	RAM_0015		
02ca b7 5a	STA	PortC_SavedValue		
02cc ae 03	LDX	#0x3		
02ce bf 51	STX	DAT_0051		
		LAB_02d0	XREF[1]:	02e4(j)
02d0 b6 5a	LDA	PortC_SavedValue		
02d2 a4 03	AND	#0x3		
02d4 ab 04	ADD	#0x4		
02d6 97	TAX			
02d7 d6 0c f8	LDA	DAT_0cf8,X		
02da be 51	LDX	DAT_0051		
02dc e7 52	STA	0x52,X=>DAT_0055		
02de 34 5a	LSR	PortC_SavedValue		
02e0 34 5a	LSR	PortC_SavedValue		
02e2 3a 51	DEC	DAT_0051		
02e4 2a ea	BPL	LAB_02d0		
02e6 81	RTS			
		LAB_02e7	XREF[1]:	01f3(j)
02e7 ae 07	LDX	#0x7		
02e9 bf 4f	STX	DAT_004f		
02eb ae 03	LDX	#0x3		
02ed bf 51	STX	DAT_0051		
		LAB_02ef	XREF[1]:	031a(j)
02ef ee 22	LDX	0x22,X=>DAT_0025		
02f1 58	ASLX			
02f2 5c	INCX			
02f3 d6 0e 44	LDA	DAT_0e44,X		
02f6 ab 10	ADD	#0x10		
02f8 97	TAX			
02f9 d6 0c f8	LDA	DAT_0cf8,X		
02fc be 4f	LDX	DAT_004f		
02fe e7 52	STA	0x52,X=>DAT_0059		
0300 be 51	LDX	DAT_0051		
0302 ee 22	LDX	0x22,X=>DAT_0025		
0304 58	ASLX			
0305 d6 0e 44	LDA	DAT_0e44,X		
0308 ab 10	ADD	#0x10		
030a 97	TAX			
030b d6 0c f8	LDA	DAT_0cf8,X		
030e 3a 4f	DEC	DAT_004f		
0310 be 4f	LDX	DAT_004f		
0312 e7 52	STA	0x52,X=>DAT_0058		
0314 3a 4f	DEC	DAT_004f		
0316 3a 51	DEC	DAT_0051		
0318 be 51	LDX	DAT_0051		
031a 2a d3	BPL	LAB_02ef		
031c 1e 59	BSET	0x7,DAT_0059		
031e 1e 57	BSET	0x7,DAT_0057		
0320 1e 55	BSET	0x7,DAT_0055		
0322 81	RTS			

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```

LAB_0323
0323 b6 20      LDA      DAT_0020
0325 a1 4f      CMP      #0x4f
0327 25 0a      BCS      LAB_0333
0329 ae 11      LD      #0x11
032b bf 52      STX      DAT_0052
032d ae 3f      LD      #0x3f
032f bf 53      STX      DAT_0053
0331 20 08      BRA      LAB_033b

LAB_0333
0333 ae 6b      LD      #0x6b
0335 bf 52      STX      DAT_0052
0337 ae 77      LD      #0x77
0339 bf 53      STX      DAT_0053

LAB_033b
033b 97      TAX
033c 58      ASLX
033d d6 0e 44   LDA      DAT_0e44,X
0340 ab 10      ADD      #0x10
0342 97      TAX
0343 d6 0c f8   LDA      DAT_0cf8,X
0346 b7 54      STA      DAT_0054
0348 be 20      LD      DAT_0020
034a 58      ASLX
034b 5c      INCX
034c d6 0e 44   LDA      DAT_0e44,X
034f ab 10      ADD      #0x10
0351 97      TAX
0352 d6 0c f8   LDA      DAT_0cf8,X
0355 b7 55      STA      DAT_0055
0357 3f 56      CLR      DAT_0056
0359 a6 80      LDA      #0x80
035b b7 57      STA      DAT_0057
035d be 21      LD      DAT_0021
035f 58      ASLX
0360 d6 0e 44   LDA      DAT_0e44,X
0363 ab 10      ADD      #0x10
0365 97      TAX
0366 d6 0c f8   LDA      DAT_0cf8,X
0369 b7 58      STA      DAT_0058
036b be 21      LD      DAT_0021
036d 58      ASLX
036e 5c      INCX
036f d6 0e 44   LDA      DAT_0e44,X
0372 ab 10      ADD      #0x10
0374 97      TAX
0375 d6 0c f8   LDA      DAT_0cf8,X
0378 b7 59      STA      DAT_0059
037a 81      RTS

*****
*                               *
*                               *
*****
undefined Read_CHR_OCHR()
undefined      A:1      <RETURN>
Read_CHR_OCHR
037b 02 1b 0b   BRSET    0x1,DAT_001b,LAB_0389
037e b6 1d      LDA      RAM_001d
0380 a4 03      AND      #0x3
0382 a1 00      CMP      #0x0
0384 26 03      BNE      LAB_0389
0386 cc 03 8c   JMP      LAB_038c

LAB_0389
0389 cc 04 3c   JMP      LAB_043c

LAB_038c
038c b6 18      LDA      DAT_0018
038e a4 07      AND      #0x7
0390 ab 10      ADD      #0x10
0392 97      TAX
0393 d6 0c f8   LDA      DAT_0cf8,X
0396 b7 59      STA      DAT_0059
0398 b6 18      LDA      DAT_0018
039a 44      LSRA
039b 44      LSRA
039c 44      LSRA
039d a4 07      AND      #0x7
039f ab 10      ADD      #0x10
03a1 97      TAX
03a2 d6 0c f8   LDA      DAT_0cf8,X
03a5 b7 58      STA      DAT_0058

XREF[1]:      01f6(j)
XREF[1]:      0327(j)
XREF[1]:      0331(j)
XREF[1]:      RESET:0187(c)
XREF[2]:      037b(j), 0384(j)
XREF[1]:      0386(j)

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```

03a7 be 18      LDX      DAT_0018
03a9 b6 17      LDA      RAM_0017
03ab 58         ASLX
03ac 49         ROLA
03ad 58         ASLX
03ae 49         ROLA
03af a4 07      AND      #0x7
03b1 ab 10      ADD      #0x10
03b3 97         TAX
03b4 d6 0c f8   LDA      DAT_0cf8,X
03b7 b7 57      STA      DAT_0057
03b9 b6 17      LDA      RAM_0017
03bb 44         LSRA
03bc a4 07      AND      #0x7
03be ab 10      ADD      #0x10
03c0 97         TAX
03c1 d6 0c f8   LDA      DAT_0cf8,X
03c4 b7 56      STA      DAT_0056
03c6 b6 17      LDA      RAM_0017
03c8 44         LSRA
03c9 44         LSRA
03ca 44         LSRA
03cb 44         LSRA
03cc a4 07      AND      #0x7
03ce ab 10      ADD      #0x10
03d0 97         TAX
03d1 d6 0c f8   LDA      DAT_0cf8,X
03d4 b7 55      STA      DAT_0055
03d6 06 1b 24   BRSET    0x3,DAT_001b,LAB_03fd
03d9 b6 17      LDA      RAM_0017
03db 49         ROLA
03dc 49         ROLA
03dd a4 01      AND      #0x1
03df ab 10      ADD      #0x10
03e1 97         TAX
03e2 d6 0c f8   LDA      DAT_0cf8,X
03e5 b7 54      STA      DAT_0054
03e7 3f 53      CLR      DAT_0053
03e9 3f 52      CLR      DAT_0052
03eb ae 02      LDX      #0x2
03ed a6 77      LDA      #0x77

                                LAB_03ef
03ef e1 52      CMP      0x52,X=>DAT_0054
03f1 26 09      BNE      LAB_03fc
03f3 6f 52      CLR      0x52,X=>DAT_0054
03f5 5c         INCX
03f6 a3 07      CPX      #0x7
03f8 27 02      BEQ      LAB_03fc
03fa 20 f3      BRA      LAB_03ef

                                LAB_03fc
03fc 81         RTS

                                LAB_03fd
03fd be 17      LDX      RAM_0017
03ff b6 16      LDA      RAM_0016
0401 58         ASLX
0402 49         ROLA
0403 a4 07      AND      #0x7
0405 ab 10      ADD      #0x10
0407 97         TAX
0408 d6 0c f8   LDA      DAT_0cf8,X
040b b7 54      STA      DAT_0054
040d b6 16      LDA      RAM_0016
040f 44         LSRA
0410 44         LSRA
0411 a4 07      AND      #0x7
0413 ab 10      ADD      #0x10
0415 97         TAX
0416 d6 0c f8   LDA      DAT_0cf8,X
0419 b7 53      STA      DAT_0053
041b b6 16      LDA      RAM_0016
041d 49         ROLA
041e 49         ROLA
041f 49         ROLA
0420 49         ROLA
0421 a4 07      AND      #0x7
0423 ab 10      ADD      #0x10
0425 97         TAX
0426 d6 0c f8   LDA      DAT_0cf8,X
0429 b7 52      STA      DAT_0052
042b 5f         CLRX
042c a6 77      LDA      #0x77

```

XREF[1]: 03fa(j)

XREF[2]: 03f1(j), 03f8(j)

XREF[1]: 03d6(j)

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```

LAB_042e
042e e1 52      CMP      0x52,X
0430 26 09      BNE      LAB_043b
0432 6f 52      CLR      0x52,X
0434 5c         INCX
0435 a3 07      CPX      #0x7
0437 27 02      BEQ      LAB_043b
0439 20 f3      BRA      LAB_042e

LAB_043b
043b 81         RTS

LAB_043c
043c b6 18      LDA      DAT_0018
043e b7 5a      STA      PortC_SavedValue
0440 ae 03      LDX      #0x3
0442 bf 51      STX      DAT_0051

LAB_0444
0444 b6 5a      LDA      PortC_SavedValue
0446 a4 03      AND      #0x3
0448 ab 04      ADD      #0x4
044a 97         TAX
044b d6 0c f8   LDA      DAT_0cf8,X
044e be 51      LDX      DAT_0051
0450 e7 56      STA      0x56,X=>DAT_0059
0452 34 5a      LSR      PortC_SavedValue
0454 34 5a      LSR      PortC_SavedValue
0456 3a 51      DEC      DAT_0051
0458 2a ea      BPL      LAB_0444
045a b6 17      LDA      RAM_0017
045c b7 5a      STA      PortC_SavedValue
045e ae 03      LDX      #0x3
0460 bf 51      STX      DAT_0051

LAB_0462
0462 b6 5a      LDA      PortC_SavedValue
0464 a4 03      AND      #0x3
0466 ab 04      ADD      #0x4
0468 97         TAX
0469 d6 0c f8   LDA      DAT_0cf8,X
046c be 51      LDX      DAT_0051
046e e7 52      STA      0x52,X=>DAT_0055
0470 34 5a      LSR      PortC_SavedValue
0472 34 5a      LSR      PortC_SavedValue
0474 3a 51      DEC      DAT_0051
0476 2a ea      BPL      LAB_0462
0478 81         RTS

*****
*                               *
*                               *
*****
undefined Something_RAM_050_051()
undefined      A:1      <RETURN>
Something_RAM_050_051
0479 a6 1f      LDA      #0x1f
047b b7 4f      STA      DAT_004f
047d ae 03      LDX      #0x3
047f bf 51      STX      DAT_0051

LAB_0481
0481 a6 07      LDA      #0x7
0483 b7 50      STA      DAT_0050

LAB_0485
0485 4f         CLRA
0486 64 56      LSR      0x56,X=>DAT_0059
0488 24 02      BCC      LAB_048c
048a ba 4b      ORA      DAT_004b

LAB_048c
048c 64 52      LSR      0x52,X=>DAT_0055
048e 24 02      BCC      LAB_0492
0490 ba 4c      ORA      DAT_004c

LAB_0492
0492 be 4f      LDX      DAT_004f
0494 3a 4f      DEC      DAT_004f
0496 ea 27      ORA      0x27,X=>PortC_ValueToWrite
0498 e7 27      STA      0x27,X=>PortC_ValueToWrite
049a be 51      LDX      DAT_0051
049c 3a 50      DEC      DAT_0050
049e 2a e5      BPL      LAB_0485

XREF[1]: 0439(j)
XREF[2]: 0430(j), 0437(j)
XREF[1]: 0389(j)
XREF[1]: 0458(j)
XREF[1]: 0476(j)
XREF[3]: RESET:0176(c), RESET:0184(c),
          RESET:0192(c)
XREF[1]: 04a4(j)
XREF[1]: 049e(j)
XREF[1]: 0488(j)
XREF[1]: 048e(j)

```

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```

04a0 3a 51      DEC      DAT_0051
04a2 be 51      LD      DAT_0051
04a4 2a db      BPL     LAB_0481
04a6 81         RTS

*****
*                               *
*                               *
*****

void Read_Write_Port_C(void)
    <VOID>          <RETURN>
    Read_Write_Port_C
04a7 b6 02      LDA     PORTC
04a9 b7 5a      STA     PortC_SavedValue
04ab b6 46      LDA     PortC_ValueToWrite
04ad 97         TAX
04ae aa 80      ORA     #0x80
04b0 bf 02      STX     PORTC
04b2 b7 02      STA     PORTC
04b4 b6 45      LDA     DAT_0045
04b6 b7 02      STA     PORTC
04b8 1e 02      BSET    0x7,PORTC
04ba ae 1d      LD      #0x1d

LAB_04bc
04bc e6 27      LDA     0x27,X=>DAT_0044
04be b7 02      STA     PORTC
04c0 1e 02      BSET    0x7,PORTC
04c2 5a         DECC
04c3 2a f7      BPL     LAB_04bc
04c5 a6 1f      LDA     #0x1f

LAB_04c7
04c7 97         TAX
04c8 ee 27      LD      0x27,X=>PortC_ValueToWrite
04ca ee 86      LD      0x86,X
04cc bf 02      STX     PORTC
04ce 1e 02      BSET    0x7,PORTC
04d0 4a         DECA
04d1 2a f4      BPL     LAB_04c7
04d3 b6 5a      LDA     PortC_SavedValue
04d5 b7 02      STA     PORTC
04d7 81         RTS

*****
*                               *
*                               *
*****

undefined Read_Bytes_from_Port_A()
    A:1          <RETURN>
    Read_Bytes_from_Port_A
04d8 cd 09 bc   JSR     Clock_PB3_RMM_Return_Port_A
04db b7 1c      STA     RAM_001c
04dd 06 1c 03   BRSET   0x3,RAM_001c,LAB_04e3
04e0 cc 06 be   JMP     FUN_06be
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

LAB_04e3
04e3 b6 1c      LDA     RAM_001c
04e5 a4 07      AND     #0x7
04e7 48         ASLA
04e8 48         ASLA
04e9 97         TAX

switchD_04ea::switchD
04ea dc 04 fc   JMP     0x4fc,X

LAB_04ed
04ed 01 1d 03   BRCLR   0x0,RAM_001d,LAB_04f3
04f0 cd 06 6b   JSR     FUN_066b

LAB_04f3
04f3 5f         CLRX
04f4 cd 06 94   JSR     FUN_0694
04f7 5c         INCX
04f8 cd 06 94   JSR     FUN_0694
04fb 81         RTS

switchD_04ea::caseD_0
04fc cc 05 1b   JMP     LAB_051b
04ff 9d         ??      9Dh

switchD_04ea::caseD_1
0500 cc 05 75   JMP     LAB_0575

```

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```

0503 9d      ??      9Dh

                                switchD_04ea::caseD_2
0504 cc 05 94      JMP      LAB_0594      XREF[1]: 04ea(j)
0507 9d      ??      9Dh

                                switchD_04ea::caseD_3
0508 cc 05 c3      JMP      LAB_05c3      XREF[1]: 04ea(j)
050b 9d      ??      9Dh

                                switchD_04ea::caseD_4
050c cc 05 df      JMP      LAB_05df      XREF[1]: 04ea(j)
050f 9d      ??      9Dh

                                switchD_04ea::caseD_5
0510 cc 06 08      JMP      LAB_0608      XREF[1]: 04ea(j)
0513 9d      ??      9Dh

                                switchD_04ea::caseD_6
0514 cc 06 10      JMP      LAB_0610      XREF[1]: 04ea(j)
0517 9d      ??      9Dh

                                switchD_04ea::caseD_7
0518 cc 06 27      JMP      LAB_0627      XREF[1]: 04ea(j)

                                LAB_051b
051b cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A      XREF[1]: 04fc(j)
051e b7 14      STA      RAM_0014
0520 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A      undefinedClock_PB3_RMM_Return_P...
0523 b7 16      STA      RAM_0016
0525 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A      undefinedClock_PB3_RMM_Return_P...
0528 b7 15      STA      RAM_0015
052a cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A      undefinedClock_PB3_RMM_Return_P...
052d b7 18      STA      DAT_0018
052f cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A      undefinedClock_PB3_RMM_Return_P...
0532 b7 17      STA      RAM_0017
0534 13 1b      BCLR     0x1,DAT_001b
0536 17 1b      BCLR     0x3,DAT_001b
0538 19 1b      BCLR     0x4,DAT_001b
053a ae 50      LDX      #0x50
053c bf 10      STX      RAM_0010
053e 05 1a 25      BRCLR   0x2,RAM_001a,LAB_0566
0541 ae 54      LDX      #0x54
0543 bf 11      STX      RAM_0011
0545 b6 1a      LDA      RAM_001a
0547 a4 18      AND      #0x18
0549 44      LSRA
054a b7 52      STA      DAT_0052
054c b6 1a      LDA      RAM_001a
054e a4 03      AND      #0x3
0550 ba 52      ORA      DAT_0052
0552 a1 08      CMP      #0x8
0554 25 07      BCS      LAB_055d
0556 b7 13      STA      RAM_0013
0558 3f 12      CLR      RAM_0012
055a cc 04 ed      JMP      LAB_04ed

                                LAB_055d
055d ab 30      ADD      #0x30      XREF[1]: 0554(j)
055f b7 13      STA      RAM_0013
0561 3f 12      CLR      RAM_0012
0563 cc 04 ed      JMP      LAB_04ed

                                LAB_0566
0566 ae 45      LDX      #0x45      XREF[1]: 053e(j)
0568 bf 11      STX      RAM_0011
056a ae 58      LDX      #0x58
056c bf 12      STX      RAM_0012
056e ae 4d      LDX      #0x4d
0570 bf 13      STX      RAM_0013
0572 cc 04 ed      JMP      LAB_04ed

                                LAB_0575
0575 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A      XREF[1]: 0500(j)
0578 b7 18      STA      DAT_0018
057a cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A      undefinedClock_PB3_RMM_Return_P...
057d b7 17      STA      RAM_0017
057f cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A      undefinedClock_PB3_RMM_Return_P...
0582 b7 16      STA      RAM_0016
0584 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A      undefinedClock_PB3_RMM_Return_P...
0587 b7 15      STA      RAM_0015
0589 3f 14      CLR      RAM_0014
058b 18 1b      BSET     0x4,DAT_001b
058d 13 1b      BCLR     0x1,DAT_001b
058f 17 1b      BCLR     0x3,DAT_001b

```

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```

0591 cc 04 ed      JMP      LAB_04ed

                                LAB_0594
0594 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A
0597 b7 18         STA      DAT_0018
0599 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A
059c b7 17         STA      RAM_0017
059e 12 1b         BSET     0x1,DAT_001b
05a0 19 1b         BCLR     0x4,DAT_001b
05a2 17 1b         BCLR     0x3,DAT_001b
05a4 a6 10         LDA      #0x10
05a6 b7 1d         STA      RAM_001d
05a8 a6 00         LDA      #0x0
05aa b7 1e         STA      RAM_001e
05ac b6 19         LDA      PortD_PONI_IONI_bit_4_5
05ae a4 0f         AND      #0xf
05b0 ab 10         ADD      #0x10
05b2 b7 12         STA      RAM_0012
05b4 b6 19         LDA      PortD_PONI_IONI_bit_4_5
05b6 a4 30         AND      #0x30
05b8 44           LSRA
05b9 44           LSRA
05ba 44           LSRA
05bb 44           LSRA
05bc ab 3a         ADD      #0x3a
05be b7 13         STA      RAM_0013
05c0 cc 04 ed      JMP      LAB_04ed

                                LAB_05c3
05c3 16 1b         BSET     0x3,DAT_001b
05c5 13 1b         BCLR     0x1,DAT_001b
05c7 19 1b         BCLR     0x4,DAT_001b
05c9 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A
05cc b7 16         STA      RAM_0016
05ce 3f 15         CLR      RAM_0015
05d0 3f 14         CLR      RAM_0014
05d2 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A
05d5 b7 18         STA      DAT_0018
05d7 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A
05da b7 17         STA      RAM_0017
05dc cc 04 ed      JMP      LAB_04ed

                                LAB_05df
05df cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A
05e2 b7 52         STA      DAT_0052
05e4 a4 03         AND      #0x3
05e6 48           ASLA
05e7 97           TAX
05e8 d6 0d 12      LDA      DAT_0d12,X
05eb b7 13         STA      RAM_0013
05ed 5c           INCX
05ee d6 0d 12      LDA      DAT_0d12,X
05f1 b7 12         STA      RAM_0012
05f3 b6 52         LDA      DAT_0052
05f5 a4 18         AND      #0x18
05f7 44           LSRA
05f8 44           LSRA
05f9 97           TAX
05fa d6 0d 1a      LDA      DAT_0d1a,X
05fd b7 11         STA      RAM_0011
05ff 5c           INCX
0600 d6 0d 1a      LDA      DAT_0d1a,X
0603 b7 10         STA      RAM_0010
0605 cc 04 ed      JMP      LAB_04ed

                                LAB_0608
0608 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A
060b b7 1a         STA      RAM_001a
060d cc 04 ed      JMP      LAB_04ed

                                LAB_0610
0610 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A
0613 b7 11         STA      RAM_0011
0615 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A
0618 b7 10         STA      RAM_0010
061a cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A
061d b7 13         STA      RAM_0013
061f cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A
0622 b7 12         STA      RAM_0012
0624 cc 04 ed      JMP      LAB_04ed

                                LAB_0627
0627 cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A
062a b7 1d         STA      RAM_001d
062c cd 09 bc      JSR      Clock_PB3_RMM_Return_Port_A

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062f b7 1e      STA      RAM_001e
0631 a4 f0      AND      #0xf0
0633 a1 40      CMP      #0x40
0635 26 15      BNE      LAB_064c
0637 a6 40      LDA      #0x40
0639 b7 0a      STA      MR_Misc_register
063b a6 47      LDA      #0x47
063d b7 09      STA      TCR_Timer_Control_Register      = 0.0
063f a6 01      LDA      #0x1
0641 b7 0b      STA      PCR_Program_Control_Register
0643 3f 04      CLR      DDRA
0645 3f 05      CLR      DDRB
0647 3f 06      CLR      DDRC
0649 cc 01 10    JMP      RESET      undefined RESET()

-- Flow Override: CALL_RETURN (CALL_TERMINATOR)

LAB_064c
064c 15 1b      BCLR      0x2,DAT_001b      XREF[1]: 0635(j)
064e a1 10      CMP      #0x10
0650 26 02      BNE      LAB_0654
0652 14 1b      BSET      0x2,DAT_001b

LAB_0654
0654 b6 1d      LDA      RAM_001d      XREF[1]: 0650(j)
0656 48        ASLA
0657 39 1e      ROL      RAM_001e
0659 48        ASLA
065a 39 1e      ROL      RAM_001e
065c b6 1e      LDA      RAM_001e
065e a4 3f      AND      #0x3f
0660 b7 1e      STA      RAM_001e
0662 b6 1d      LDA      RAM_001d
0664 a4 3f      AND      #0x3f
0666 b7 1d      STA      RAM_001d
0668 cc 04 ed    JMP      LAB_04ed

*****
*                      FUNCTION                      *
*****
undefined FUN_066b()
undefined      A:1      <RETURN>
FUN_066b      XREF[1]:  Read_Bytes_from_Port_A:04f0(c)
066b 3f 52      CLR      DAT_0052
066d 07 1b 04    BRCLR      0x3,DAT_001b,LAB_0674
0670 b6 16      LDA      RAM_0016
0672 b7 52      STA      DAT_0052

LAB_0674
0674 be 1e      LDX      RAM_001e      XREF[1]: 066d(j)
0676 5d        TSTX
0677 27 09      BEQ      LAB_0682

LAB_0679
0679 34 52      LSR      DAT_0052      XREF[1]: 0680(j)
067b 36 17      ROR      RAM_0017
067d 36 18      ROR      DAT_0018
067f 5a        DECX
0680 26 f7      BNE      LAB_0679

LAB_0682
0682 b6 18      LDA      DAT_0018      XREF[1]: 0677(j)
0684 a4 0f      AND      #0xf
0686 97        TAX
0687 3f 17      CLR      RAM_0017
0689 3f 18      CLR      DAT_0018
068b 99        SEC

LAB_068c
068c 39 18      ROL      DAT_0018      XREF[1]: 0691(j)
068e 39 17      ROL      RAM_0017
0690 5a        DECX
0691 2a f9      BPL      LAB_068c
0693 81        RTS

*****
*                      FUNCTION                      *
*****
undefined FUN_0694()
undefined      A:1      <RETURN>
FUN_0694      XREF[2]:  Read_Bytes_from_Port_A:04f4(c),
                      Read_Bytes_from_Port_A:04f8(c)
0694 4f        CLRA
0695 09 1d 05    BRCLR      0x4,RAM_001d,LAB_069d
0698 e6 6d      LDA      0x6d,X
069a 43        COMA

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069b e4 5e      AND      0x5e,X

                                LAB_069d                                XREF[1]: 0695(j)
069d b7 52      STA      DAT_0052
069f e6 17      LDA      0x17,X
06a1 07 1d 07   BRCLR   0x3,RAM_001d,LAB_06ab
06a4 e6 6d      LDA      0x6d,X
06a6 43        COMA
06a7 ea 5e      ORA      0x5e,X
06a9 e4 17      AND      0x17,X

                                LAB_06ab                                XREF[1]: 06a1(j)
06ab ba 52      ORA      DAT_0052
06ad e7 5e      STA      0x5e,X
06af e7 17      STA      0x17,X
06b1 81        RTS

*****
*                                FUNCTION                                *
*****
undefined FUN_06b2()
A:1             <RETURN>
                                XREF[1]: 06b2(j)
06b2 b6 6f      LDA      DAT_006f
06b4 43        COMA
06b5 b4 60      AND      DAT_0060
06b7 ba 19      ORA      PortD_PONI_IONI_bit_4_5
06b9 b7 60      STA      DAT_0060
06bb b7 19      STA      PortD_PONI_IONI_bit_4_5
06bd 81        RTS

*****
*                                FUNCTION                                *
*****
undefined FUN_06be()
A:1             <RETURN>
                                XREF[1]: 06be(j)
06be be 1c      LDX      RAM_001c
06c0 9f        TXA
06c1 a4 07      AND      #0x7
06c3 b7 1c      STA      RAM_001c
06c5 b6 02      LDA      PORTC
06c7 a4 f8      AND      #0xf8
06c9 ba 1c      ORA      RAM_001c
06cb b7 02      STA      PORTC
06cd 9f        TXA
06ce a4 04      AND      #0x4
06d0 26 01      BNE      LAB_06d3
06d2 81        RTS

                                LAB_06d3                                XREF[1]: 06d0(j)
06d3 1b 01      BCLR    0x5,PORTB
06d5 9f        TXA
06d6 a4 03      AND      #0x3
06d8 a8 03      EOR      #0x3
06da b7 1c      STA      RAM_001c
06dc 9f        TXA
06dd a4 20      AND      #0x20
06df 26 19      BNE      LAB_06fa
06e1 1d 01      BCLR    0x6,PORTB
06e3 cd 09 bc   JSR      Clock_PB3_RMM_Return_Port_A      undefinedClock_PB3_RMM_Return_P...
06e6 be 1c      LDX      RAM_001c
06e8 e7 47      STA      0x47,X
06ea cd 09 c5   JSR      FUN_09c5      undefined FUN_09c5()
06ed 1a 01      BSET    0x5,PORTB
06ef a3 00      CPX      #0x0
06f1 26 06      BNE      LAB_06f9
06f3 cd 07 15   JSR      FUN_0715      undefined FUN_0715()
06f6 cd 09 ee   JSR      FUN_09ee      undefined FUN_09ee()

                                LAB_06f9                                XREF[1]: 06f1(j)
06f9 81        RTS

                                LAB_06fa                                XREF[1]: 06df(j)
06fa 1c 01      BSET    0x6,PORTB
06fc cd 09 bc   JSR      Clock_PB3_RMM_Return_Port_A      undefinedClock_PB3_RMM_Return_P...
06ff be 1c      LDX      RAM_001c
0701 a3 03      CPX      #0x3
0703 26 06      BNE      LAB_070b
0705 cd 0a c0   JSR      FUN_0ac0      undefined FUN_0ac0()
0708 cd 08 03   JSR      FUN_0803      undefined FUN_0803()

                                LAB_070b                                XREF[1]: 0703(j)
070b be 1c      LDX      RAM_001c
070d e6 47      LDA      0x47,X

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```

070f cd 09 c5 JSR FUN_09c5 undefined FUN_09c5()
0712 1a 01 BSET 0x5,FORTB
0714 81 RTS

*****
* FUNCTION *
*****
undefined FUN_0715()
A:1 <RETURN>
FUN_0715 XREF[1]: FUN_06be:06f3(c)
0715 ae 05 LDX #0x5

LAB_0717 XREF[1]: 071a(j)
0717 6f 20 CLR 0x20,X=>DAT_0025
0719 5a DECK
071a 2a fb BPL LAB_0717
071c b6 80 LDA DAT_0080 = 02h
071e b7 4b STA DAT_004b
0720 b6 81 LDA DAT_0081 = DAh
0722 b7 4c STA DAT_004c
0724 a6 03 LDA #0x3
0726 b7 26 STA DAT_0026
0728 b6 47 LDA DAT_0047
072a b7 4d STA DAT_004d
072c b6 48 LDA DAT_0048
072e b7 4e STA DAT_004e

LAB_0730 XREF[1]: 0757(j)
0730 b6 4d LDA DAT_004d
0732 b7 52 STA DAT_0052
0734 b6 4e LDA DAT_004e
0736 b7 53 STA DAT_0053
0738 b6 4e LDA DAT_004e
073a b0 4c SUB DAT_004c
073c b7 4e STA DAT_004e
073e b6 4d LDA DAT_004d
0740 b2 4b SBC DAT_004b
0742 b7 4d STA DAT_004d
0744 a6 da LDA #0xda
0746 b7 4c STA DAT_004c
0748 25 0f BCS LAB_0759
074a 3c 20 INC DAT_0020
074c 3c 26 INC DAT_0026
074e 05 26 06 BRCLR 0x2,DAT_0026,LAB_0757
0751 3c 4c INC DAT_004c
0753 3c 4c INC DAT_004c
0755 3f 26 CLR DAT_0026

LAB_0757 XREF[1]: 074e(j)
0757 20 d7 BRA LAB_0730

LAB_0759 XREF[1]: 0748(j)
0759 b6 52 LDA DAT_0052
075b b7 4d STA DAT_004d
075d b6 53 LDA DAT_0053
075f b7 4e STA DAT_004e
0761 b6 20 LDA DAT_0020
0763 ab 4f ADD #0x4f
0765 b7 20 STA DAT_0020
0767 3c 21 INC DAT_0021
0769 3f 4b CLR DAT_004b
076b a6 3e LDA #0x3e
076d b7 4c STA DAT_004c

LAB_076f XREF[1]: 0798(j)
076f b6 4d LDA DAT_004d
0771 b7 52 STA DAT_0052
0773 b6 4e LDA DAT_004e
0775 b7 53 STA DAT_0053
0777 b6 4e LDA DAT_004e
0779 b0 4c SUB DAT_004c
077b b7 4e STA DAT_004e
077d b6 4d LDA DAT_004d
077f b2 4b SBC DAT_004b
0781 b7 4d STA DAT_004d
0783 25 15 BCS LAB_079a
0785 3c 21 INC DAT_0021
0787 be 21 LDX DAT_0021
0789 d6 0d de LDA DAT_0dde,X
078c a3 02 CPX #0x2
078e 26 06 BNE LAB_0796
0790 3d 26 TST DAT_0026
0792 26 02 BNE LAB_0796
0794 ab 02 ADD #0x2

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0796 b7 4c	LAB_0796	STA	DAT_004c	XREF[2]:	078e(j), 0792(j)
0798 20 d5		BRA	LAB_076f		
079a b6 52	LAB_079a	LDA	DAT_0052	XREF[1]:	0783(j)
079c b7 4d		STA	DAT_004d		
079e b6 53		LDA	DAT_0053		
07a0 b7 4e		STA	DAT_004e		
07a2 44		LSRA			
07a3 24 04		BCC	LAB_07a9		
07a5 ae 0c		LDX	#0xc		
07a7 bf 23		STX	DAT_0023		
07a9 4c	LAB_07a9	INCA		XREF[1]:	07a3(j)
07aa b7 22		STA	DAT_0022		
07ac b6 49		LDA	DAT_0049		
07ae b7 4d		STA	DAT_004d		
07b0 b6 4a		LDA	DAT_004a		
07b2 b7 4e		STA	DAT_004e		
07b4 b6 82		LDA	DAT_0082		= 0Eh
07b6 b7 4b		STA	DAT_004b		
07b8 b6 83		LDA	DAT_0083		= 10h
07ba b7 4c		STA	DAT_004c		
07bc b6 4d	LAB_07bc	LDA	DAT_004d	XREF[1]:	07d4(j)
07be b7 52		STA	DAT_0052		
07c0 b6 4e		LDA	DAT_004e		
07c2 b7 53		STA	DAT_0053		
07c4 b6 4e		LDA	DAT_004e		
07c6 b0 4c		SUB	DAT_004c		
07c8 b7 4e		STA	DAT_004e		
07ca b6 4d		LDA	DAT_004d		
07cc b2 4b		SBC	DAT_004b		
07ce b7 4d		STA	DAT_004d		
07d0 25 04		BCS	LAB_07d6		
07d2 3c 23		INC	DAT_0023		
07d4 20 e6		BRA	LAB_07bc		
07d6 b6 52	LAB_07d6	LDA	DAT_0052	XREF[1]:	07d0(j)
07d8 b7 4d		STA	DAT_004d		
07da b6 53		LDA	DAT_0053		
07dc b7 4e		STA	DAT_004e		
07de 3f 4b		CLR	DAT_004b		
07e0 a6 3c		LDA	#0x3c		
07e2 b7 4c		STA	DAT_004c		
07e4 b6 4d	LAB_07e4	LDA	DAT_004d	XREF[1]:	07fc(j)
07e6 b7 52		STA	DAT_0052		
07e8 b6 4e		LDA	DAT_004e		
07ea b7 53		STA	DAT_0053		
07ec b6 4e		LDA	DAT_004e		
07ee b0 4c		SUB	DAT_004c		
07f0 b7 4e		STA	DAT_004e		
07f2 b6 4d		LDA	DAT_004d		
07f4 b2 4b		SBC	DAT_004b		
07f6 b7 4d		STA	DAT_004d		
07f8 25 04		BCS	LAB_07fe		
07fa 3c 24		INC	DAT_0024		
07fc 20 e6		BRA	LAB_07e4		
07fe b6 53	LAB_07fe	LDA	DAT_0053	XREF[1]:	07f8(j)
0800 b7 25		STA	DAT_0025		
0802 81		RTS			

* FUNCTION *					

undefined FUN_0803()					
undefined	A:1	<RETURN>		XREF[1]:	FUN_06be:0708(c)
0803 b6 20	FUN_0803	LDA	DAT_0020		
0805 a0 4f		SUB	#0x4f		
0807 b7 20		STA	DAT_0020		
0809 3f 4d		CLR	DAT_004d		
080b 3f 4e		CLR	DAT_004e		
080d 3f 4f		CLR	DAT_004f		
080f a6 03		LDA	#0x3		
0811 b7 26		STA	DAT_0026		

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0813 b6 80	LDA	DAT_0080	= 02h
0815 b7 4b	STA	DAT_004b	
0817 b6 81	LDA	DAT_0081	= DAh
0819 b7 4c	STA	DAT_004c	
	LAB_081b		XREF[1]: 083e(j)
081b 3c 4f	INC	DAT_004f	
081d b6 4f	LDA	DAT_004f	
081f b1 20	CMP	DAT_0020	
0821 22 1d	BHI	LAB_0840	
0823 b6 4e	LDA	DAT_004e	
0825 bb 4c	ADD	DAT_004c	
0827 b7 4e	STA	DAT_004e	
0829 b6 4d	LDA	DAT_004d	
082b b9 4b	ADC	DAT_004b	
082d b7 4d	STA	DAT_004d	
082f a6 da	LDA	#0xda	
0831 b7 4c	STA	DAT_004c	
0833 3c 26	INC	DAT_0026	
0835 05 26 06	BRCLR	0x2,DAT_0026,LAB_083e	
0838 3c 4c	INC	DAT_004c	
083a 3c 4c	INC	DAT_004c	
083c 3f 26	CLR	DAT_0026	
	LAB_083e		XREF[1]: 0835(j)
083e 20 db	BRA	LAB_081b	
	LAB_0840		XREF[1]: 0821(j)
0840 b6 21	LDA	DAT_0021	
0842 3d 26	TST	DAT_0026	
0844 26 02	BNE	LAB_0848	
0846 ab 0d	ADD	#0xd	
	LAB_0848		XREF[1]: 0844(j)
0848 48	ASLA		
0849 97	TAX		
084a 5c	INCX		
084b d6 0d eb	LDA	DAT_0deb,X	
084e bb 4e	ADD	DAT_004e	
0850 b7 4e	STA	DAT_004e	
0852 5a	DECX		
0853 d6 0d eb	LDA	DAT_0deb,X	
0856 b9 4d	ADC	DAT_004d	
0858 b7 4d	STA	DAT_004d	
085a b6 22	LDA	DAT_0022	
085c 4a	DECA		
085d 48	ASLA		
085e bb 4e	ADD	DAT_004e	
0860 b7 4e	STA	DAT_004e	
0862 24 02	BCC	LAB_0866	
0864 3c 4d	INC	DAT_004d	
	LAB_0866		XREF[1]: 0862(j)
0866 b6 4d	LDA	DAT_004d	
0868 b7 47	STA	DAT_0047	
086a b6 4e	LDA	DAT_004e	
086c b7 48	STA	DAT_0048	
086e b6 23	LDA	DAT_0023	
0870 a1 0c	CMP	#0xc	
0872 25 0a	BCS	LAB_087e	
0874 a0 0c	SUB	#0xc	
0876 3c 48	INC	DAT_0048	
0878 3d 48	TST	DAT_0048	
087a 26 02	BNE	LAB_087e	
087c 3c 47	INC	DAT_0047	
	LAB_087e		XREF[2]: 0872(j), 087a(j)
087e 48	ASLA		
087f 97	TAX		
0880 5c	INCX		
0881 d6 0e 1f	LDA	DAT_0e1f,X	
0884 b7 4e	STA	DAT_004e	
0886 5a	DECX		
0887 d6 0e 1f	LDA	DAT_0e1f,X	
088a b7 4d	STA	DAT_004d	
088c ae 05	LDX	#0x5	
088e 3f 4b	CLR	DAT_004b	
0890 a6 3c	LDA	#0x3c	
0892 b7 4c	STA	DAT_004c	
0894 b6 24	LDA	DAT_0024	
0896 b7 52	STA	DAT_0052	
	LAB_0898		XREF[1]: 08ad(j)
0898 34 52	LSR	DAT_0052	
089a 24 0c	BCC	LAB_08a8	

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089c b6 4e    LDA    DAT_004e
089e bb 4c    ADD    DAT_004c
08a0 b7 4e    STA    DAT_004e
08a2 b6 4d    LDA    DAT_004d
08a4 b9 4b    ADC    DAT_004b
08a6 b7 4d    STA    DAT_004d

LAB_08a8
08a8 38 4c    ASL    DAT_004c
08aa 39 4b    ROL    DAT_004b
08ac 5a      DECX
08ad 2a e9    BPL    LAB_0898
08af b6 25    LDA    DAT_0025
08b1 bb 4e    ADD    DAT_004e
08b3 b7 4e    STA    DAT_004e
08b5 24 02    BCC    LAB_08b9
08b7 3c 4d    INC    DAT_004d

LAB_08b9
08b9 b6 4d    LDA    DAT_004d
08bb b7 49    STA    DAT_0049
08bd b6 4e    LDA    DAT_004e
08bf b7 4a    STA    DAT_004a
08c1 b6 20    LDA    DAT_0020
08c3 ab 4f    ADD    #0x4f
08c5 b7 20    STA    DAT_0020
08c7 81      RTS

*****
*                      FUNCTION                      *
*****

undefined TIMER_INTERRUPT()
A:1          <RETURN>
undefined    INT2 or TIMER interrupt

TIMER_INTERRUPT
08c8 a6 03    LDA    #0x3
08ca b7 08    STA    TDR_Timer_Data_Register
08cc 1f 09    BCLR    0x7,TCR_Timer_Control_Register    = 0.0
08ce cd 09 d4 JSR    Read_Port_D_Update_CLR    undefinedRead_Port_D_Update_CLR()
08d1 cd 06 b2 JSR    FUN_06b2    undefined FUN_06b2()
08d4 03 1b 03 BRCLR    0x1,DAT_001b,LAB_08da
08d7 cd 09 85 JSR    Read_Port_D    undefined Read_Port_D()

LAB_08da
08da 3a 5b    DEC    RAM_005b
08dc 26 07    BNE    LAB_08e5
08de cd 09 48 JSR    FUN_0948    undefined FUN_0948()
08e1 a6 10    LDA    #0x10
08e3 b7 5b    STA    RAM_005b

LAB_08e5
08e5 3a 5c    DEC    RAM_005c
08e7 26 0f    BNE    LAB_08f8
08e9 a6 c8    LDA    #0xc8
08eb b7 5c    STA    RAM_005c
08ed 3a 5d    DEC    RAM_005d
08ef 26 07    BNE    LAB_08f8
08f1 a6 02    LDA    #0x2
08f3 b7 5d    STA    RAM_005d
08f5 cd 08 f9 JSR    FUN_08f9    undefined FUN_08f9()

LAB_08f8
08f8 80      RTI

*****
*                      FUNCTION                      *
*****

undefined FUN_08f9()
A:1          <RETURN>
undefined    FUN_08f9
08f9 a6 3c    LDA    #0x3c
08fb 3c 25    INC    DAT_0025
08fd b1 25    CMP    DAT_0025
08ff 22 46    BHI    LAB_0947
0901 3f 25    CLR    DAT_0025
0903 a6 3c    LDA    #0x3c
0905 3c 24    INC    DAT_0024
0907 b1 24    CMP    DAT_0024
0909 22 3c    BHI    LAB_0947
090b 3f 24    CLR    DAT_0024
090d a6 18    LDA    #0x18
090f 3c 23    INC    DAT_0023
0911 b1 23    CMP    DAT_0023
0913 22 32    BHI    LAB_0947
0915 3f 23    CLR    DAT_0023

XREF[1]: 089a(j)
XREF[1]: 08b5(j)
XREF[1]: 0ff8(*)
XREF[1]: 08d4(j)
XREF[1]: 08dc(j)
XREF[2]: 08e7(j), 08ef(j)
XREF[1]: TIMER_INTERRUPT:08f5(c)

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```

0917 be 21      LDX      DAT_0021
0919 d6 0e 37    LDA      DAT_0e37,X
091c a1 1c      CMP      #0x1c
091e 26 05      BNE      LAB_0925
0920 3d 26      TST      DAT_0026
0922 26 01      BNE      LAB_0925
0924 4c         INCA

LAB_0925
XREF[2]:      091e(j), 0922(j)

0925 3c 22      INC      DAT_0022
0927 b1 22      CMP      DAT_0022
0929 24 1c      BCC      LAB_0947
092b a6 01      LDA      #0x1
092d b7 22      STA      DAT_0022
092f a6 0c      LDA      #0xc
0931 3c 21      INC      DAT_0021
0933 b1 21      CMP      DAT_0021
0935 22 10      BHI      LAB_0947
0937 a6 01      LDA      #0x1
0939 b7 21      STA      DAT_0021
093b 3c 20      INC      DAT_0020
093d 3c 26      INC      DAT_0026
093f b6 26      LDA      DAT_0026
0941 a1 03      CMP      #0x3
0943 23 02      BLS      LAB_0947
0945 3f 26      CLR      DAT_0026

LAB_0947
XREF[6]:      08ff(j), 0909(j), 0913(j),
              0929(j), 0935(j), 0943(j)

0947 81         RTS

*****
*                               *
*                               FUNCTION                               *
*****
undefined FUN_0948()
A:1      <RETURN>
XREF[1]:      TIMER_INTERRUPT:08de(c)

0948 b6 6c      LDA      DAT_006c
094a b7 6f      STA      DAT_006f
094c b6 6b      LDA      DAT_006b
094e b7 6e      STA      DAT_006e
0950 b6 6a      LDA      DAT_006a
0952 b7 6d      STA      DAT_006d
0954 b6 69      LDA      DAT_0069
0956 b7 6c      STA      DAT_006c
0958 b6 68      LDA      DAT_0068
095a b7 6b      STA      DAT_006b
095c b6 67      LDA      DAT_0067
095e b7 6a      STA      DAT_006a
0960 b6 66      LDA      DAT_0066
0962 b7 69      STA      DAT_0069
0964 b6 65      LDA      DAT_0065
0966 b7 68      STA      DAT_0068
0968 b6 64      LDA      DAT_0064
096a b7 67      STA      DAT_0067
096c b6 63      LDA      DAT_0063
096e b7 66      STA      DAT_0066
0970 b6 62      LDA      DAT_0062
0972 b7 65      STA      DAT_0065
0974 b6 61      LDA      DAT_0061
0976 b7 64      STA      DAT_0064
0978 b6 60      LDA      DAT_0060
097a b7 63      STA      DAT_0063
097c b6 5f      LDA      DAT_005f
097e b7 62      STA      DAT_0062
0980 b6 5e      LDA      DAT_005e
0982 b7 61      STA      DAT_0061
0984 81         RTS

*****
*                               *
*                               FUNCTION                               *
*****
undefined Read_Port_D()
A:1      <RETURN>
XREF[1]:      TIMER_INTERRUPT:08d7(c)

0985 3a 71      DEC      RAM_0071
0987 26 26      BNE      LAB_09af
0989 a6 80      LDA      #0x80
098b b7 71      STA      RAM_0071
098d b6 72      LDA      DAT_0072
098f 4d         TSTA
0990 27 08      BEQ      LAB_099a
0992 44         LSRA
0993 b7 72      STA      DAT_0072
0995 44         LSRA

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09d3 81      RTS

*****
*                      FUNCTION                      *
*****
undefined Read_Port_D_Update_CLR()
undefined      A:1      <RETURN>
Read_Port_D_Update_CLR                                XREF[1]:  TIMER_INTERRUPT:08ce(c)
09d4 b6 03      LDA      PORTD
09d6 b7 70      STA      PortD_SavedValue
09d8 a4 0c      AND      #0xc
09da 48        ASLA
09db 48        ASLA
09dc b7 19      STA      PortD_PONI_IONI_bit_4_5
09de 05 70 0c   BRCLR    0x2,PortD_SavedValue,LAB_09ed
09e1 b6 70      LDA      PortD_SavedValue
09e3 a4 03      AND      #0x3
09e5 97        TAX
09e6 d6 0f 0c   LDA      DAT_0f0c,X                      = 01h
09e9 ba 19      ORA      PortD_PONI_IONI_bit_4_5
09eb b7 19      STA      PortD_PONI_IONI_bit_4_5

LAB_09ed
09ed 81      RTS

*****
*                      FUNCTION                      *
*****
undefined FUN_09ee()
undefined      A:1      <RETURN>
FUN_09ee                                                XREF[1]:  FUN_06be:06f6(c)
09ee a6 ff      LDA      #0xff
09f0 b7 04      STA      DDRA
09f2 a6 0f      LDA      #0xf
09f4 b7 00      STA      PORTA
09f6 13 01      BCLR    0x1,PORTB
09f8 12 01      BSET    0x1,PORTB
09fa a6 f0      LDA      #0xf0
09fc b7 00      STA      PORTA
09fe 13 01      BCLR    0x1,PORTB
0a00 12 01      BSET    0x1,PORTB
0a02 a6 05      LDA      #0x5
0a04 b7 00      STA      PORTA
0a06 13 01      BCLR    0x1,PORTB
0a08 12 01      BSET    0x1,PORTB
0a0a b6 26      LDA      DAT_0026
0a0c a4 03      AND      #0x3
0a0e 48        ASLA
0a0f 48        ASLA
0a10 aa f1      ORA      #0xf1
0a12 b7 00      STA      PORTA
0a14 13 01      BCLR    0x1,PORTB
0a16 12 01      BSET    0x1,PORTB
0a18 be 20      LDX      DAT_0020
0a1a 58        ASLX
0a1b d6 0e 44   LDA      DAT_0e44,X
0a1e aa d0      ORA      #0xd0
0a20 b7 00      STA      PORTA
0a22 13 01      BCLR    0x1,PORTB
0a24 12 01      BSET    0x1,PORTB
0a26 5c        INCX
0a27 d6 0e 44   LDA      DAT_0e44,X
0a2a aa c0      ORA      #0xc0
0a2c b7 00      STA      PORTA
0a2e 13 01      BCLR    0x1,PORTB
0a30 12 01      BSET    0x1,PORTB
0a32 be 21      LDX      DAT_0021
0a34 58        ASLX
0a35 d6 0e 44   LDA      DAT_0e44,X
0a38 aa b0      ORA      #0xb0
0a3a b7 00      STA      PORTA
0a3c 13 01      BCLR    0x1,PORTB
0a3e 12 01      BSET    0x1,PORTB
0a40 5c        INCX
0a41 d6 0e 44   LDA      DAT_0e44,X
0a44 aa a0      ORA      #0xa0
0a46 b7 00      STA      PORTA
0a48 13 01      BCLR    0x1,PORTB
0a4a 12 01      BSET    0x1,PORTB
0a4c be 22      LDX      DAT_0022
0a4e 58        ASLX
0a4f d6 0e 44   LDA      DAT_0e44,X
0a52 aa 90      ORA      #0x90
0a54 b7 00      STA      PORTA
0a56 13 01      BCLR    0x1,PORTB

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0a58 12 01      BSET      0x1,PORTB
0a5a 5c          INCX
0a5b d6 0e 44   LDA       DAT_0e44,X
0a5e aa 80      ORA       #0x80
0a60 b7 00      STA       PORTA
0a62 13 01      BCLR     0x1,PORTB
0a64 12 01      BSET     0x1,PORTB
0a66 be 23      LDX       DAT_0023
0a68 58          ASLX
0a69 d6 0e 44   LDA       DAT_0e44,X
0a6c aa 70      ORA       #0x70
0a6e b7 00      STA       PORTA
0a70 13 01      BCLR     0x1,PORTB
0a72 12 01      BSET     0x1,PORTB
0a74 5c          INCX
0a75 d6 0e 44   LDA       DAT_0e44,X
0a78 aa 60      ORA       #0x60
0a7a b7 00      STA       PORTA
0a7c 13 01      BCLR     0x1,PORTB
0a7e 12 01      BSET     0x1,PORTB
0a80 be 24      LDX       DAT_0024
0a82 58          ASLX
0a83 d6 0e 44   LDA       DAT_0e44,X
0a86 aa 50      ORA       #0x50
0a88 b7 00      STA       PORTA
0a8a 13 01      BCLR     0x1,PORTB
0a8c 12 01      BSET     0x1,PORTB
0a8e 5c          INCX
0a8f d6 0e 44   LDA       DAT_0e44,X
0a92 aa 40      ORA       #0x40
0a94 b7 00      STA       PORTA
0a96 13 01      BCLR     0x1,PORTB
0a98 12 01      BSET     0x1,PORTB
0a9a be 25      LDX       DAT_0025
0a9c 58          ASLX
0a9d d6 0e 44   LDA       DAT_0e44,X
0aa0 aa 30      ORA       #0x30
0aa2 b7 00      STA       PORTA
0aa4 13 01      BCLR     0x1,PORTB
0aa6 12 01      BSET     0x1,PORTB
0aa8 be 25      LDX       DAT_0025
0aaa d6 0e 44   LDA       DAT_0e44,X
0aad aa 20      ORA       #0x20
0aaf b7 00      STA       PORTA
0ab1 13 01      BCLR     0x1,PORTB
0ab3 12 01      BSET     0x1,PORTB
0ab5 a6 01      LDA       #0x1
0ab7 b7 00      STA       PORTA
0ab9 13 01      BCLR     0x1,PORTB
0abb 12 01      BSET     0x1,PORTB
0abd 3f 04      CLR       DDRA
0abf 81          RTS

*****
*                               *
*                               *
*****
undefined FUN_0ac0()
undefined      A:1      <RETURN>
FUN_0ac0      XREF[3]:      RESET:013c(c), FUN_06be:0705(c),
                                0bd8(j)

0ac0 a6 f0      LDA       #0xf0
0ac2 b7 04      STA       DDRA
0ac4 a6 00      LDA       #0x0
0ac6 b7 00      STA       PORTA
0ac8 15 01      BCLR     0x2,PORTB
0aca 14 01      BSET     0x2,PORTB
0acc a6 f0      LDA       #0xf0
0ace b7 00      STA       PORTA
0ad0 15 01      BCLR     0x2,PORTB
0ad2 b6 00      LDA       PORTA
0ad4 14 01      BSET     0x2,PORTB
0ad6 a4 0f      AND       #0xf
0ad8 44          LSRA
0ad9 44          LSRA
0ada b7 26      STA       DAT_0026
0adc a6 c0      LDA       #0xc0
0ade b7 00      STA       PORTA
0ae0 15 01      BCLR     0x2,PORTB
0ae2 b6 00      LDA       PORTA
0ae4 14 01      BSET     0x2,PORTB
0ae6 a4 0f      AND       #0xf
0ae8 b7 20      STA       DAT_0020
0aea a6 d0      LDA       #0xd0
0aec b7 00      STA       PORTA
0aee 15 01      BCLR     0x2,PORTB

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0af0 b6 00    LDA    PORTA
0af2 14 01    BSET   0x2,PORTB
0af4 a4 0f    AND    #0xf
0af6 97       TAX
0af7 58       ASLX
0af8 58       ASLX
0af9 58       ASLX
0afa bf 52    STX    DAT_0052
0afc 48       ASLA
0afd bb 52    ADD    DAT_0052
0aff bb 20    ADD    DAT_0020
0b01 b7 20    STA    DAT_0020
0b03 a6 a0    LDA    #0xa0
0b05 b7 00    STA    PORTA
0b07 15 01    BCLR   0x2,PORTB
0b09 b6 00    LDA    PORTA
0b0b 14 01    BSET   0x2,PORTB
0b0d a4 0f    AND    #0xf
0b0f b7 21    STA    DAT_0021
0b11 a6 b0    LDA    #0xb0
0b13 b7 00    STA    PORTA
0b15 15 01    BCLR   0x2,PORTB
0b17 b6 00    LDA    PORTA
0b19 14 01    BSET   0x2,PORTB
0b1b a4 0f    AND    #0xf
0b1d 97       TAX
0b1e 58       ASLX
0b1f 58       ASLX
0b20 58       ASLX
0b21 bf 52    STX    DAT_0052
0b23 48       ASLA
0b24 bb 52    ADD    DAT_0052
0b26 bb 21    ADD    DAT_0021
0b28 b7 21    STA    DAT_0021
0b2a a6 80    LDA    #0x80
0b2c b7 00    STA    PORTA
0b2e 15 01    BCLR   0x2,PORTB
0b30 b6 00    LDA    PORTA
0b32 14 01    BSET   0x2,PORTB
0b34 a4 0f    AND    #0xf
0b36 b7 22    STA    DAT_0022
0b38 a6 90    LDA    #0x90
0b3a b7 00    STA    PORTA
0b3c 15 01    BCLR   0x2,PORTB
0b3e b6 00    LDA    PORTA
0b40 14 01    BSET   0x2,PORTB
0b42 a4 0f    AND    #0xf
0b44 97       TAX
0b45 58       ASLX
0b46 58       ASLX
0b47 58       ASLX
0b48 bf 52    STX    DAT_0052
0b4a 48       ASLA
0b4b bb 52    ADD    DAT_0052
0b4d bb 22    ADD    DAT_0022
0b4f b7 22    STA    DAT_0022
0b51 a6 60    LDA    #0x60
0b53 b7 00    STA    PORTA
0b55 15 01    BCLR   0x2,PORTB
0b57 b6 00    LDA    PORTA
0b59 14 01    BSET   0x2,PORTB
0b5b a4 0f    AND    #0xf
0b5d b7 23    STA    DAT_0023
0b5f a6 70    LDA    #0x70
0b61 b7 00    STA    PORTA
0b63 15 01    BCLR   0x2,PORTB
0b65 b6 00    LDA    PORTA
0b67 14 01    BSET   0x2,PORTB
0b69 a4 0f    AND    #0xf
0b6b 97       TAX
0b6c 58       ASLX
0b6d 58       ASLX
0b6e 58       ASLX
0b6f bf 52    STX    DAT_0052
0b71 48       ASLA
0b72 bb 52    ADD    DAT_0052
0b74 bb 23    ADD    DAT_0023
0b76 b7 23    STA    DAT_0023
0b78 a6 40    LDA    #0x40
0b7a b7 00    STA    PORTA
0b7c 15 01    BCLR   0x2,PORTB
0b7e b6 00    LDA    PORTA
0b80 14 01    BSET   0x2,PORTB
0b82 a4 0f    AND    #0xf
0b84 b7 24    STA    DAT_0024

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0b86	a6 50	LDA	#0x50
0b88	b7 00	STA	PORTA
0b8a	15 01	BCLR	0x2,PORTB
0b8c	b6 00	LDA	PORTA
0b8e	14 01	BSET	0x2,PORTB
0b90	a4 0f	AND	#0xf
0b92	97	TAX	
0b93	58	ASLX	
0b94	58	ASLX	
0b95	58	ASLX	
0b96	bf 52	STX	DAT_0052
0b98	48	ASLA	
0b99	bb 52	ADD	DAT_0052
0b9b	bb 24	ADD	DAT_0024
0b9d	b7 24	STA	DAT_0024
0b9f	a6 20	LDA	#0x20
0ba1	b7 00	STA	PORTA
0ba3	15 01	BCLR	0x2,PORTB
0ba5	b6 00	LDA	PORTA
0ba7	14 01	BSET	0x2,PORTB
0ba9	a4 0f	AND	#0xf
0bab	b7 25	STA	DAT_0025
0bad	a6 30	LDA	#0x30
0bae	b7 00	STA	PORTA
0bb1	15 01	BCLR	0x2,PORTB
0bb3	b6 00	LDA	PORTA
0bb5	14 01	BSET	0x2,PORTB
0bb7	a4 0f	AND	#0xf
0bb9	97	TAX	
0bba	58	ASLX	
0bbb	58	ASLX	
0bbc	58	ASLX	
0bbd	bf 52	STX	DAT_0052
0bbf	48	ASLA	
0bc0	bb 52	ADD	DAT_0052
0bc2	bb 25	ADD	DAT_0025
0bc4	b7 25	STA	DAT_0025
0bc6	a6 00	LDA	#0x0
0bc8	b7 00	STA	PORTA
0bca	15 01	BCLR	0x2,PORTB
0bcc	b6 00	LDA	PORTA
0bce	14 01	BSET	0x2,PORTB
0bd0	a4 0f	AND	#0xf
0bd2	a4 08	AND	#0x8
0bd4	a1 08	CMP	#0x8
0bd6	26 03	BNE	LAB_0bdb
0bd8	cc 0a c0	JMP	FUN_0ac0
LAB_0bdb		XREF[1]:	0bd6(j)
0bdb	3f 04	CLR	DDRA
0bdd	81	RTS	
0bde	00	??	00h
0bdf	00	??	00h
0be0	00	??	00h
0be1	00	??	00h
0be2	00	??	00h
0be3	00	??	00h
0be4	00	??	00h
0be5	00	??	00h
0be6	00	??	00h
0be7	00	??	00h
0be8	00	??	00h
0be9	00	??	00h
0bea	00	??	00h
0beb	00	??	00h
0bec	00	??	00h
0bed	00	??	00h
0bee	00	??	00h
0bef	00	??	00h
0bf0	00	??	00h
0bf1	00	??	00h
0bf2	00	??	00h
0bf3	00	??	00h
0bf4	00	??	00h
0bf5	00	??	00h
0bf6	00	??	00h
0bf7	00	??	00h
0bf8	00	??	00h
0bf9	00	??	00h
0bfa	00	??	00h
0bfb	00	??	00h
0bfc	00	??	00h
0bfd	00	??	00h
0bfe	00	??	00h
0bff	00	??	00h

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0c00	00	??	00h
0c01	00	??	00h
0c02	00	??	00h
0c03	00	??	00h
0c04	00	??	00h
0c05	00	??	00h
0c06	00	??	00h
0c07	00	??	00h
0c08	00	??	00h
0c09	00	??	00h
0c0a	00	??	00h
0c0b	00	??	00h
0c0c	00	??	00h
0c0d	00	??	00h
0c0e	00	??	00h
0c0f	00	??	00h
0c10	00	??	00h
0c11	00	??	00h
0c12	00	??	00h
0c13	00	??	00h
0c14	00	??	00h
0c15	00	??	00h
0c16	00	??	00h
0c17	00	??	00h
0c18	00	??	00h
0c19	00	??	00h
0c1a	00	??	00h
0c1b	00	??	00h
0c1c	00	??	00h
0c1d	00	??	00h
0c1e	00	??	00h
0c1f	00	??	00h
0c20	00	??	00h
0c21	00	??	00h
0c22	00	??	00h
0c23	00	??	00h
0c24	00	??	00h
0c25	00	??	00h
0c26	00	??	00h
0c27	00	??	00h
0c28	00	??	00h
0c29	00	??	00h
0c2a	00	??	00h
0c2b	00	??	00h
0c2c	00	??	00h
0c2d	00	??	00h
0c2e	00	??	00h
0c2f	00	??	00h
0c30	00	??	00h
0c31	00	??	00h
0c32	00	??	00h
0c33	00	??	00h
0c34	00	??	00h
0c35	00	??	00h
0c36	00	??	00h
0c37	00	??	00h
0c38	00	??	00h
0c39	00	??	00h
0c3a	00	??	00h
0c3b	00	??	00h
0c3c	00	??	00h
0c3d	00	??	00h
0c3e	00	??	00h
0c3f	00	??	00h
0c40	00	??	00h
0c41	00	??	00h
0c42	00	??	00h
0c43	00	??	00h
0c44	00	??	00h
0c45	00	??	00h
0c46	00	??	00h
0c47	00	??	00h
0c48	00	??	00h
0c49	00	??	00h
0c4a	00	??	00h
0c4b	00	??	00h
0c4c	00	??	00h
0c4d	00	??	00h
0c4e	00	??	00h
0c4f	00	??	00h
0c50	00	??	00h
0c51	00	??	00h
0c52	00	??	00h
0c53	00	??	00h
0c54	00	??	00h

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0c55 00	??	00h
0c56 00	??	00h
0c57 00	??	00h
0c58 00	??	00h
0c59 00	??	00h
0c5a 00	??	00h
0c5b 00	??	00h
0c5c 00	??	00h
0c5d 00	??	00h
0c5e 00	??	00h
0c5f 00	??	00h
0c60 00	??	00h
0c61 00	??	00h
0c62 00	??	00h
0c63 00	??	00h
0c64 00	??	00h
0c65 00	??	00h
0c66 00	??	00h
0c67 00	??	00h
0c68 00	??	00h
0c69 00	??	00h
0c6a 00	??	00h
0c6b 00	??	00h
0c6c 00	??	00h
0c6d 00	??	00h
0c6e 00	??	00h
0c6f 00	??	00h
0c70 00	??	00h
0c71 00	??	00h
0c72 00	??	00h
0c73 00	??	00h
0c74 00	??	00h
0c75 00	??	00h
0c76 00	??	00h
0c77 00	??	00h
0c78 00	??	00h
0c79 00	??	00h
0c7a 00	??	00h
0c7b 00	??	00h
0c7c 00	??	00h
0c7d 00	??	00h
0c7e 00	??	00h
0c7f 00	??	00h
0c80 00	??	00h
0c81 00	??	00h
0c82 00	??	00h
0c83 00	??	00h
0c84 00	??	00h
0c85 00	??	00h
0c86 00	??	00h
0c87 00	??	00h
0c88 00	??	00h
0c89 00	??	00h
0c8a 00	??	00h
0c8b 00	??	00h
0c8c 00	??	00h
0c8d 00	??	00h
0c8e 00	??	00h
0c8f 00	??	00h
0c90 00	??	00h
0c91 00	??	00h
0c92 00	??	00h
0c93 00	??	00h
0c94 00	??	00h
0c95 00	??	00h
0c96 00	??	00h
0c97 00	??	00h
0c98 00	??	00h
0c99 00	??	00h
0c9a 00	??	00h
0c9b 00	??	00h
0c9c 00	??	00h
0c9d 00	??	00h
0c9e 00	??	00h
0c9f 00	??	00h
0ca0 00	??	00h
0ca1 00	??	00h
0ca2 00	??	00h
0ca3 00	??	00h
0ca4 00	??	00h
0ca5 00	??	00h
0ca6 00	??	00h
0ca7 00	??	00h
0ca8 00	??	00h
0ca9 00	??	00h

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0caa	00	??	00h
0cab	00	??	00h
0cac	00	??	00h
0cad	00	??	00h
0cae	00	??	00h
0caf	00	??	00h
0cb0	00	??	00h
0cb1	00	??	00h
0cb2	00	??	00h
0cb3	00	??	00h
0cb4	00	??	00h
0cb5	00	??	00h
0cb6	00	??	00h
0cb7	00	??	00h
0cb8	00	??	00h
0cb9	00	??	00h
0cba	00	??	00h
0cbb	00	??	00h
0cbc	00	??	00h
0cbd	00	??	00h
0cbe	00	??	00h
0cbf	00	??	00h
0cc0	00	??	00h
0cc1	00	??	00h
0cc2	00	??	00h
0cc3	00	??	00h
0cc4	00	??	00h
0cc5	00	??	00h
0cc6	00	??	00h
0cc7	00	??	00h
0cc8	00	??	00h
0cc9	00	??	00h
0cca	00	??	00h
0ccb	00	??	00h
0ccc	00	??	00h
0ccd	00	??	00h
0cce	00	??	00h
0ccf	00	??	00h
0cd0	00	??	00h
0cd1	00	??	00h
0cd2	00	??	00h
0cd3	00	??	00h
0cd4	00	??	00h
0cd5	00	??	00h
0cd6	00	??	00h
0cd7	00	??	00h
0cd8	00	??	00h
0cd9	00	??	00h
0cda	00	??	00h
0cdb	00	??	00h
0cdc	00	??	00h
0cdd	00	??	00h
0cde	00	??	00h
0cdf	00	??	00h
0ce0	00	??	00h
0ce1	00	??	00h
0ce2	00	??	00h
0ce3	00	??	00h
0ce4	00	??	00h
0ce5	00	??	00h
0ce6	00	??	00h
0ce7	00	??	00h
0ce8	00	??	00h
0ce9	00	??	00h
0cea	00	??	00h
0ceb	00	??	00h
0cec	00	??	00h
0ced	00	??	00h
0cee	00	??	00h
0cef	00	??	00h
0cf0	00	??	00h
0cf1	00	??	00h
0cf2	00	??	00h
0cf3	00	??	00h
0cf4	00	??	00h
0cf5	00	??	00h
0cf6	00	??	00h
0cf7	00	??	00h

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	DAT_0cf8				XREF[27]:	FUN_01ed:021a(*), FUN_01ed:0229(*), FUN_01ed:02b9(*), FUN_01ed:02d7(*), FUN_01ed:02f9(*), FUN_01ed:030b(*), FUN_01ed:0343(*), FUN_01ed:0352(*), FUN_01ed:0366(*), FUN_01ed:0375(*), Read_CHR_OCHR:0393(*), Read_CHR_OCHR:03a2(*), Read_CHR_OCHR:03b4(*), Read_CHR_OCHR:03c1(*), Read_CHR_OCHR:03d1(*), Read_CHR_OCHR:03e2(*), Read_CHR_OCHR:0408(*), Read_CHR_OCHR:0416(*), Read_CHR_OCHR:0426(*), Read_CHR_OCHR:044b(*), [more]
0cf8	00	??	00h			
0cf9	20	??	20h			
0cfa	28	??	28h	(
0cfb	2a	??	2Ah	*		
0cfc	50	??	50h	P		
0cfd	51	??	51h	Q		
0cfe	54	??	54h	T		
0cff	55	??	55h	U		
0d00	5f	??	5Fh	_		
0d01	66	??	66h	f		
0d02	6e	??	6Eh	n		
0d03	4e	??	4Eh	N		
0d04	5d	??	5Dh]		
0d05	08	??	08h			
0d06	44	??	44h	D		
0d07	4f	??	4Fh	O		
0d08	77	??	77h	w		
0d09	11	??	11h			
0d0a	6b	??	6Bh	k		
0d0b	3b	??	3Bh	;		
0d0c	1d	??	1Dh			
0d0d	3e	??	3Eh	>		
0d0e	7e	??	7Eh	~		
0d0f	13	??	13h			
0d10	7f	??	7Fh			
0d11	3f	??	3Fh	?		
	DAT_0d12				XREF[2]:	Read_Bytes_from_Port_A:05e8*), Read_Bytes_from_Port_A:05ed*)
0d12	3a	??	3Ah	:		
0d13	3a	??	3Ah	:		
0d14	52	??	52h	R		
0d15	3a	??	3Ah	:		
0d16	3a	??	3Ah	:		
0d17	57	??	57h	W		
0d18	52	??	52h	R		
0d19	57	??	57h	W		
	DAT_0d1a				XREF[2]:	Read_Bytes_from_Port_A:05fa*), Read_Bytes_from_Port_A:060Q*)
0d1a	43	??	43h	C		
0d1b	44	??	44h	D		
0d1c	44	??	44h	D		
0d1d	44	??	44h	D		
0d1e	43	??	43h	C		
0d1f	41	??	41h	A		
0d20	44	??	44h	D		
0d21	41	??	41h	A		
	DAT_0d22				XREF[8]:	FUN_01b4:01b7(*), FUN_01b4:01bd(*), FUN_01b4:01c5(*), FUN_01b4:01cb(*), FUN_01b4:01d3(*), FUN_01b4:01d9(*), FUN_01b4:01e1(*), FUN_01b4:01e7(*)
0d22	00	??	00h			
0d23	00	??	00h			
0d24	55	??	55h	U		
0d25	11	??	11h			
0d26	55	??	55h	U		
0d27	11	??	11h			
0d28	55	??	55h	U		

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0d29	11	??	11h	
0d2a	55	??	55h	U
0d2b	11	??	11h	
0d2c	55	??	55h	U
0d2d	11	??	11h	
0d2e	55	??	55h	U
0d2f	11	??	11h	
0d30	55	??	55h	U
0d31	11	??	11h	
0d32	4d	??	4Dh	M
0d33	9a	??	9Ah	
0d34	41	??	41h	A
0d35	82	??	82h	
0d36	45	??	45h	E
0d37	99	??	99h	
0d38	45	??	45h	E
0d39	8b	??	8Bh	
0d3a	49	??	49h	I
0d3b	83	??	83h	
0d3c	4c	??	4Ch	L
0d3d	83	??	83h	
0d3e	4c	??	4Ch	L
0d3f	9b	??	9Bh	
0d40	45	??	45h	E
0d41	82	??	82h	
0d42	88	??	88h	
0d43	11	??	11h	
0d44	8d	??	8Dh	
0d45	11	??	11h	
0d46	88	??	88h	
0d47	1b	??	1Bh	
0d48	8d	??	8Dh	
0d49	1b	??	1Bh	
0d4a	88	??	88h	
0d4b	b1	??	B1h	
0d4c	8d	??	8Dh	
0d4d	b1	??	B1h	
0d4e	88	??	88h	
0d4f	bb	??	BBh	
0d50	8d	??	8Dh	
0d51	bb	??	BBh	
0d52	d8	??	D8h	
0d53	11	??	11h	
0d54	dd	??	DDh	
0d55	11	??	11h	
0d56	d8	??	D8h	
0d57	1b	??	1Bh	
0d58	dd	??	DDh	
0d59	1b	??	1Bh	
0d5a	d8	??	D8h	
0d5b	b1	??	B1h	
0d5c	dd	??	DDh	
0d5d	b1	??	B1h	
0d5e	d8	??	D8h	
0d5f	bb	??	BBh	
0d60	dd	??	DDh	
0d61	bb	??	BBh	
0d62	00	??	00h	
0d63	00	??	00h	
0d64	08	??	08h	
0d65	00	??	00h	
0d66	0e	??	0Eh	
0d67	00	??	00h	
0d68	0f	??	0Fh	
0d69	01	??	01h	
0d6a	0f	??	0Fh	
0d6b	07	??	07h	
0d6c	0f	??	0Fh	
0d6d	1f	??	1Fh	
0d6e	0f	??	0Fh	
0d6f	7f	??	7Fh	
0d70	8f	??	8Fh	
0d71	ff	??	FFh	
0d72	ef	??	EFh	
0d73	ff	??	FFh	
0d74	ff	??	FFh	
0d75	ff	??	FFh	
0d76	aa	??	AAh	
0d77	55	??	55h	U
0d78	88	??	88h	
0d79	11	??	11h	
0d7a	00	??	00h	
0d7b	40	??	40h	@
0d7c	80	??	80h	
0d7d	01	??	01h	

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0d7e 00	??	00h	
0d7f 20	??	20h	
0d80 02	??	02h	
0d81 40	??	40h	@
0d82 0d	??	0Dh	
0d83 1a	??	1Ah	
0d84 03	??	03h	
0d85 02	??	02h	
0d86 05	??	05h	
0d87 19	??	19h	
0d88 05	??	05h	
0d89 0b	??	0Bh	
0d8a 09	??	09h	
0d8b 03	??	03h	
0d8c 0c	??	0Ch	
0d8d 0b	??	0Bh	
0d8e 0c	??	0Ch	
0d8f 1b	??	1Bh	
0d90 05	??	05h	
0d91 02	??	02h	
0d92 0d	??	0Dh	
0d93 1b	??	1Bh	
0d94 0d	??	0Dh	
0d95 0b	??	0Bh	
0d96 00	??	00h	
0d97 00	??	00h	
0d98 0d	??	0Dh	
0d99 11	??	11h	
0d9a 40	??	40h	@
0d9b 80	??	80h	
0d9c 4d	??	4Dh	M
0d9d 91	??	91h	
0d9e 00	??	00h	
0d9f 00	??	00h	
0da0 55	??	55h	U
0da1 11	??	11h	
0da2 95	??	95h	
0da3 ba	??	BAh	
0da4 d5	??	D5h	
0da5 83	??	83h	
0da6 1d	??	1Dh	
0da7 3b	??	3Bh	;
0da8 54	??	54h	T
0da9 a8	??	A8h	
0daa 1d	??	1Dh	
0dab 3a	??	3Ah	:
0dac d4	??	D4h	
0dad a8	??	A8h	
0dae d4	??	D4h	
0daf 80	??	80h	
0db0 54	??	54h	T
0db1 ab	??	ABh	
0db2 c1	??	C1h	
0db3 83	??	83h	
0db4 1c	??	1Ch	
0db5 38	??	38h	8
0db6 1c	??	1Ch	
0db7 b0	??	B0h	
0db8 c2	??	C2h	
0db9 84	??	84h	
0dba 40	??	40h	@
0dbb a8	??	A8h	
0dbc 63	??	63h	c
0dbd 82	??	82h	
0dbe 61	??	61h	a
0dbf 86	??	86h	
0dc0 55	??	55h	U
0dc1 aa	??	AAh	
0dc2 d5	??	D5h	
0dc3 81	??	81h	
0dc4 55	??	55h	U
0dc5 ae	??	AEnh	
0dc6 d5	??	D5h	
0dc7 85	??	85h	
0dc8 d4	??	D4h	
0dc9 2b	??	2Bh	+
0dca 1c	??	1Ch	
0dcb 10	??	10h	
0dcc 41	??	41h	A
0dcd aa	??	AAh	
0dce 42	??	42h	B
0dcf c0	??	C0h	
0dd0 41	??	41h	A
0dd1 c6	??	C6h	
0dd2 22	??	22h	"

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Odd3	44	??	44h	D
Odd4	22	??	22h	"
Odd5	10	??	10h	
Odd6	16	??	16h	
Odd7	68	??	68h	h
Odd8	dc	??	DCh	
Odd9	99	??	99h	
Odda	57	??	57h	W
Oddb	ea	??	EAh	
Oddc	13	??	13h	
Oddd	43	??	43h	C
		DAT_0dde	XREF[1]: FUN_0715:0789(*)	
Odde	00	??	00h	
Oddf	00	??	00h	
Ode0	38	??	38h	8
Ode1	3e	??	3Eh	>
Ode2	3c	??	3Ch	<
Ode3	3e	??	3Eh	>
Ode4	3c	??	3Ch	<
Ode5	3e	??	3Eh	>
Ode6	3e	??	3Eh	>
Ode7	3c	??	3Ch	<
Ode8	3e	??	3Eh	>
Ode9	3c	??	3Ch	<
Odea	3e	??	3Eh	>
		DAT_0deb	XREF[2]: FUN_0803:084b(*), FUN_0803:0853(*)	
Odeb	00	??	00h	
Odec	00	??	00h	
Oded	00	??	00h	
Odee	00	??	00h	
Odef	00	??	00h	
Odf0	3e	??	3Eh	>
Odf1	00	??	00h	
Odf2	76	??	76h	v
Odf3	00	??	00h	
Odf4	b4	??	B4h	
Odf5	00	??	00h	
Odf6	f0	??	F0h	
Odf7	01	??	01h	
Odf8	2e	??	2Eh	.
Odf9	01	??	01h	
Odfa	6a	??	6Ah	j
Odfb	01	??	01h	
Odfc	a8	??	A8h	
Odfd	01	??	01h	
Odfe	e6	??	E6h	
Odff	02	??	02h	
Oe00	22	??	22h	"
Oe01	02	??	02h	
Oe02	60	??	60h	`
Oe03	02	??	02h	
Oe04	9c	??	9Ch	
Oe05	00	??	00h	
Oe06	00	??	00h	
Oe07	00	??	00h	
Oe08	00	??	00h	
Oe09	00	??	00h	
Oe0a	3e	??	3Eh	>
Oe0b	00	??	00h	
Oe0c	78	??	78h	x
Oe0d	00	??	00h	
Oe0e	b6	??	B6h	
Oe0f	00	??	00h	
Oe10	f2	??	F2h	
Oe11	01	??	01h	
Oe12	30	??	30h	0
Oe13	01	??	01h	
Oe14	6c	??	6Ch	l
Oe15	01	??	01h	
Oe16	aa	??	AAh	
Oe17	01	??	01h	
Oe18	e8	??	E8h	
Oe19	02	??	02h	
Oe1a	24	??	24h	\$
Oe1b	02	??	02h	
Oe1c	62	??	62h	b
Oe1d	02	??	02h	
Oe1e	9e	??	9Eh	
		DAT_0e1f	XREF[2]: FUN_0803:0881(*), FUN_0803:0887(*)	
Oe1f	00	??	00h	

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0e20	00	??	00h	
0e21	0e	??	0Eh	
0e22	10	??	10h	
0e23	1c	??	1Ch	
0e24	20	??	20h	
0e25	2a	??	2Ah	*
0e26	30	??	30h	0
0e27	38	??	38h	8
0e28	40	??	40h	@
0e29	46	??	46h	F
0e2a	50	??	50h	P
0e2b	54	??	54h	T
0e2c	60	??	60h	`
0e2d	62	??	62h	b
0e2e	70	??	70h	p
0e2f	70	??	70h	p
0e30	80	??	80h	
0e31	7e	??	7Eh	~
0e32	90	??	90h	
0e33	8c	??	8Ch	
0e34	a0	??	A0h	
0e35	9a	??	9Ah	
0e36	b0	??	B0h	
		DAT_0e37		XREF[1]: FUN_08f9:0919(*)
0e37	00	??	00h	
0e38	1f	??	1Fh	
0e39	1c	??	1Ch	
0e3a	1f	??	1Fh	
0e3b	1e	??	1Eh	
0e3c	1f	??	1Fh	
0e3d	1e	??	1Eh	
0e3e	1f	??	1Fh	
0e3f	1f	??	1Fh	
0e40	1e	??	1Eh	
0e41	1f	??	1Fh	
0e42	1e	??	1Eh	
0e43	1f	??	1Fh	
		DAT_0e44		XREF[18]: FUN_01ed:02f3(*), FUN_01ed:0305(*), FUN_01ed:033d(*), FUN_01ed:034c(*), FUN_01ed:0360(*), FUN_01ed:036f(*), FUN_09ee:0a1b(*), FUN_09ee:0a27(*), FUN_09ee:0a35(*), FUN_09ee:0a41(*), FUN_09ee:0a4f(*), FUN_09ee:0a5b(*), FUN_09ee:0a69(*), FUN_09ee:0a75(*), FUN_09ee:0a83(*), FUN_09ee:0a8f(*), FUN_09ee:0a9d(*), FUN_09ee:0aaa(*)
0e44	00	??	00h	
0e45	00	??	00h	
0e46	00	??	00h	
0e47	01	??	01h	
0e48	00	??	00h	
0e49	02	??	02h	
0e4a	00	??	00h	
0e4b	03	??	03h	
0e4c	00	??	00h	
0e4d	04	??	04h	
0e4e	00	??	00h	
0e4f	05	??	05h	
0e50	00	??	00h	
0e51	06	??	06h	
0e52	00	??	00h	
0e53	07	??	07h	
0e54	00	??	00h	
0e55	08	??	08h	
0e56	00	??	00h	
0e57	09	??	09h	
0e58	01	??	01h	
0e59	00	??	00h	
0e5a	01	??	01h	
0e5b	01	??	01h	
0e5c	01	??	01h	
0e5d	02	??	02h	
0e5e	01	??	01h	
0e5f	03	??	03h	

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0e60	01	??	01h
0e61	04	??	04h
0e62	01	??	01h
0e63	05	??	05h
0e64	01	??	01h
0e65	06	??	06h
0e66	01	??	01h
0e67	07	??	07h
0e68	01	??	01h
0e69	08	??	08h
0e6a	01	??	01h
0e6b	09	??	09h
0e6c	02	??	02h
0e6d	00	??	00h
0e6e	02	??	02h
0e6f	01	??	01h
0e70	02	??	02h
0e71	02	??	02h
0e72	02	??	02h
0e73	03	??	03h
0e74	02	??	02h
0e75	04	??	04h
0e76	02	??	02h
0e77	05	??	05h
0e78	02	??	02h
0e79	06	??	06h
0e7a	02	??	02h
0e7b	07	??	07h
0e7c	02	??	02h
0e7d	08	??	08h
0e7e	02	??	02h
0e7f	09	??	09h
0e80	03	??	03h
0e81	00	??	00h
0e82	03	??	03h
0e83	01	??	01h
0e84	03	??	03h
0e85	02	??	02h
0e86	03	??	03h
0e87	03	??	03h
0e88	03	??	03h
0e89	04	??	04h
0e8a	03	??	03h
0e8b	05	??	05h
0e8c	03	??	03h
0e8d	06	??	06h
0e8e	03	??	03h
0e8f	07	??	07h
0e90	03	??	03h
0e91	08	??	08h
0e92	03	??	03h
0e93	09	??	09h
0e94	04	??	04h
0e95	00	??	00h
0e96	04	??	04h
0e97	01	??	01h
0e98	04	??	04h
0e99	02	??	02h
0e9a	04	??	04h
0e9b	03	??	03h
0e9c	04	??	04h
0e9d	04	??	04h
0e9e	04	??	04h
0e9f	05	??	05h
0ea0	04	??	04h
0ea1	06	??	06h
0ea2	04	??	04h
0ea3	07	??	07h
0ea4	04	??	04h
0ea5	08	??	08h
0ea6	04	??	04h
0ea7	09	??	09h
0ea8	05	??	05h
0ea9	00	??	00h
0eaa	05	??	05h
0eab	01	??	01h
0eac	05	??	05h
0ead	02	??	02h
0eae	05	??	05h
0eaf	03	??	03h
0eb0	05	??	05h
0eb1	04	??	04h
0eb2	05	??	05h
0eb3	05	??	05h
0eb4	05	??	05h

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0eb5	06	??	06h
0eb6	05	??	05h
0eb7	07	??	07h
0eb8	05	??	05h
0eb9	08	??	08h
0eba	05	??	05h
0ebb	09	??	09h
0ebc	06	??	06h
0ebd	00	??	00h
0ebe	06	??	06h
0ebf	01	??	01h
0ec0	06	??	06h
0ec1	02	??	02h
0ec2	06	??	06h
0ec3	03	??	03h
0ec4	06	??	06h
0ec5	04	??	04h
0ec6	06	??	06h
0ec7	05	??	05h
0ec8	06	??	06h
0ec9	06	??	06h
0eca	06	??	06h
0ecb	07	??	07h
0ecc	06	??	06h
0ecd	08	??	08h
0ece	06	??	06h
0ecf	09	??	09h
0ed0	07	??	07h
0ed1	00	??	00h
0ed2	07	??	07h
0ed3	01	??	01h
0ed4	07	??	07h
0ed5	02	??	02h
0ed6	07	??	07h
0ed7	03	??	03h
0ed8	07	??	07h
0ed9	04	??	04h
0eda	07	??	07h
0edb	05	??	05h
0edc	07	??	07h
0edd	06	??	06h
0ede	07	??	07h
0edf	07	??	07h
0ee0	07	??	07h
0ee1	08	??	08h
0ee2	07	??	07h
0ee3	09	??	09h
0ee4	08	??	08h
0ee5	00	??	00h
0ee6	08	??	08h
0ee7	01	??	01h
0ee8	08	??	08h
0ee9	02	??	02h
0eea	08	??	08h
0eeb	03	??	03h
0eec	08	??	08h
0eed	04	??	04h
0eee	08	??	08h
0eeF	05	??	05h
0ef0	08	??	08h
0ef1	06	??	06h
0ef2	08	??	08h
0ef3	07	??	07h
0ef4	08	??	08h
0ef5	08	??	08h
0ef6	08	??	08h
0ef7	09	??	09h
0ef8	09	??	09h
0ef9	00	??	00h
0efa	09	??	09h
0efb	01	??	01h
0efc	09	??	09h
0efd	02	??	02h
0efe	09	??	09h
0eff	03	??	03h
0f00	09	??	09h
0f01	04	??	04h
0f02	09	??	09h
0f03	05	??	05h
0f04	09	??	09h
0f05	06	??	06h
0f06	09	??	09h
0f07	07	??	07h
0f08	09	??	09h
0f09	08	??	08h

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```

0f0a 09      ??      09h
0f0b 09      ??      09h

                                DAT_0f0c                                XREF[1]:      Read_Port_D_Update_CLR:09e8*)
0f0c 01      ??      01h
0f0d 02      ??      02h
0f0e 04      ??      04h
0f0f 08      ??      08h
0f10 00      ??      00h
0f11 00      ??      00h
0f12 00      ??      00h
0f13 00      ??      00h
0f14 00      ??      00h
0f15 00      ??      00h
0f16 00      ??      00h
0f17 00      ??      00h
0f18 00      ??      00h
0f19 00      ??      00h
0f1a 00      ??      00h
0f1b 00      ??      00h
0f1c 00      ??      00h
0f1d 00      ??      00h
0f1e 00      ??      00h
0f1f 00      ??      00h
0f20 00      ??      00h
0f21 00      ??      00h
0f22 00      ??      00h
0f23 00      ??      00h
0f24 00      ??      00h
0f25 00      ??      00h
0f26 00      ??      00h
0f27 00      ??      00h
0f28 00      ??      00h
0f29 00      ??      00h
0f2a 00      ??      00h
0f2b 00      ??      00h
0f2c 00      ??      00h
0f2d 00      ??      00h
0f2e 00      ??      00h
0f2f 00      ??      00h
0f30 00      ??      00h
0f31 00      ??      00h
0f32 00      ??      00h
0f33 00      ??      00h
0f34 00      ??      00h
0f35 00      ??      00h
0f36 00      ??      00h
0f37 00      ??      00h

Mask bits 0x25 = 0010_0101
CLK = 0 (Clock Oscilation type) 0=Crystal (1=resistor capacit...
TOPT = 0 (Timer Mask/Programmable option) (0=All TCR bits are...
CLS =1 (Timer Clock Source) (1=External TIMER PIN. 0=Internal...
TIE = 0 (Timer External Input Enable)
SNM = 0 (Secure/Non-Secure Mode Option)
P2 = 1
P1 = 0
P0 = 1

P2-P0 = (Prescaler option) => 101 => Divide by 32.

MOR_Mask_Option_Register
0f38 25      db      25h                                Mask Option Register
*** START: Unused hole from 0xF39 to 0xF7F ***
0f39 00      db      0h
0f3a 00      db      0h
0f3b 00      db      0h
0f3c 00      db      0h
0f3d 00      db      0h
0f3e 00      db      0h
0f3f 00      db      0h
0f40 00      db      0h
0f41 00      db      0h
0f42 00      db      0h
0f43 00      db      0h
0f44 00      db      0h
0f45 00      db      0h
0f46 00      db      0h
0f47 00      db      0h
0f48 00      db      0h
0f49 00      db      0h
0f4a 00      db      0h
0f4b 00      db      0h
0f4c 00      db      0h
0f4d 00      db      0h

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0f4e 00	db	0h
0f4f 00	db	0h
0f50 00	db	0h
0f51 00	db	0h
0f52 00	db	0h
0f53 00	db	0h
0f54 00	db	0h
0f55 00	db	0h
0f56 00	db	0h
0f57 00	db	0h
0f58 00	db	0h
0f59 00	db	0h
0f5a 00	db	0h
0f5b 00	db	0h
0f5c 00	db	0h
0f5d 00	db	0h
0f5e 00	db	0h
0f5f 00	db	0h
0f60 00	db	0h
0f61 00	db	0h
0f62 00	db	0h
0f63 00	db	0h
0f64 00	db	0h
0f65 00	db	0h
0f66 00	db	0h
0f67 00	db	0h
0f68 00	db	0h
0f69 00	db	0h
0f6a 00	db	0h
0f6b 00	db	0h
0f6c 00	db	0h
0f6d 00	db	0h
0f6e 00	db	0h
0f6f 00	db	0h
0f70 00	db	0h
0f71 00	db	0h
0f72 00	db	0h
0f73 00	db	0h
0f74 00	db	0h
0f75 00	db	0h
0f76 00	db	0h
0f77 00	db	0h
0f78 00	db	0h
0f79 00	db	0h
0f7a 00	db	0h
0f7b 00	db	0h
0f7c 00	db	0h
0f7d 00	db	0h
0f7e 00	db	0h
0f7f 00	db	0h
*** END: Unused hole from 0xF39 to 0xF7F ***		
0f80 00	??	00h
0f81 00	??	00h
0f82 00	??	00h
0f83 00	??	00h
0f84 00	??	00h
0f85 00	??	00h
0f86 00	??	00h
0f87 00	??	00h
0f88 00	??	00h
0f89 00	??	00h
0f8a 00	??	00h
0f8b 00	??	00h
0f8c 00	??	00h
0f8d 00	??	00h
0f8e 00	??	00h
0f8f 00	??	00h
0f90 00	??	00h
0f91 00	??	00h
0f92 00	??	00h
0f93 00	??	00h
0f94 00	??	00h
0f95 00	??	00h
0f96 00	??	00h
0f97 00	??	00h
0f98 00	??	00h
0f99 00	??	00h
0f9a 00	??	00h
0f9b 00	??	00h
0f9c 00	??	00h
0f9d 00	??	00h
0f9e 00	??	00h
0f9f 00	??	00h
0fa0 00	??	00h
0fa1 00	??	00h

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0fa2	00	??	00h
0fa3	00	??	00h
0fa4	00	??	00h
0fa5	00	??	00h
0fa6	00	??	00h
0fa7	00	??	00h
0fa8	00	??	00h
0fa9	00	??	00h
0faa	00	??	00h
0fab	00	??	00h
0fac	00	??	00h
0fad	00	??	00h
0fae	00	??	00h
0faf	00	??	00h
0fb0	00	??	00h
0fb1	00	??	00h
0fb2	00	??	00h
0fb3	00	??	00h
0fb4	00	??	00h
0fb5	00	??	00h
0fb6	00	??	00h
0fb7	00	??	00h
0fb8	00	??	00h
0fb9	00	??	00h
0fba	00	??	00h
0fbb	00	??	00h
0fbc	00	??	00h
0fbd	00	??	00h
0fbe	00	??	00h
0fbf	00	??	00h
0fc0	00	??	00h
0fc1	00	??	00h
0fc2	00	??	00h
0fc3	00	??	00h
0fc4	00	??	00h
0fc5	00	??	00h
0fc6	00	??	00h
0fc7	00	??	00h
0fc8	00	??	00h
0fc9	00	??	00h
0fca	00	??	00h
0fcb	00	??	00h
0fcc	00	??	00h
0fcd	00	??	00h
0fce	00	??	00h
0fcf	00	??	00h
0fd0	00	??	00h
0fd1	00	??	00h
0fd2	00	??	00h
0fd3	00	??	00h
0fd4	00	??	00h
0fd5	00	??	00h
0fd6	00	??	00h
0fd7	00	??	00h
0fd8	00	??	00h
0fd9	00	??	00h
0fda	00	??	00h
0fdb	00	??	00h
0fdc	00	??	00h
0fdd	00	??	00h
0fde	00	??	00h
0fdf	00	??	00h
0fe0	00	??	00h
0fe1	00	??	00h
0fe2	00	??	00h
0fe3	00	??	00h
0fe4	00	??	00h
0fe5	00	??	00h
0fe6	00	??	00h
0fe7	00	??	00h
0fe8	00	??	00h
0fe9	00	??	00h
0fea	00	??	00h
0feb	00	??	00h
0fec	00	??	00h
0fed	00	??	00h
0fee	00	??	00h
0fef	00	??	00h
0ff0	00	??	00h
0ff1	00	??	00h
0ff2	00	??	00h
0ff3	00	??	00h
0ff4	00	??	00h
0ff5	00	??	00h
0ff6	00 00	addr	0000

BOOTSTRAP Vector (Not used in th...

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0ff8 08 c8	addr	TIMER_INTERRUPT
0ffa 00 00	addr	0000
0ffc 09 ba	addr	SWI_INTERRUPT
0ffe 01 10	addr	RESET

Timer Interrupt Vector
External Interrupt Vector
SWI Vector
RESET Vector