

11 OCT 87 1615 / DI/OSER/DEL/MAS2E2/CPU/PNX 1.D.DRA



ID NO: 35000200	DESIGN: JLB-CJTIC
PRINT: 3202D	DRAWN: JLB-CJTIC
	DATE: 5-10-87
	PPN TO IDB
	REV: *

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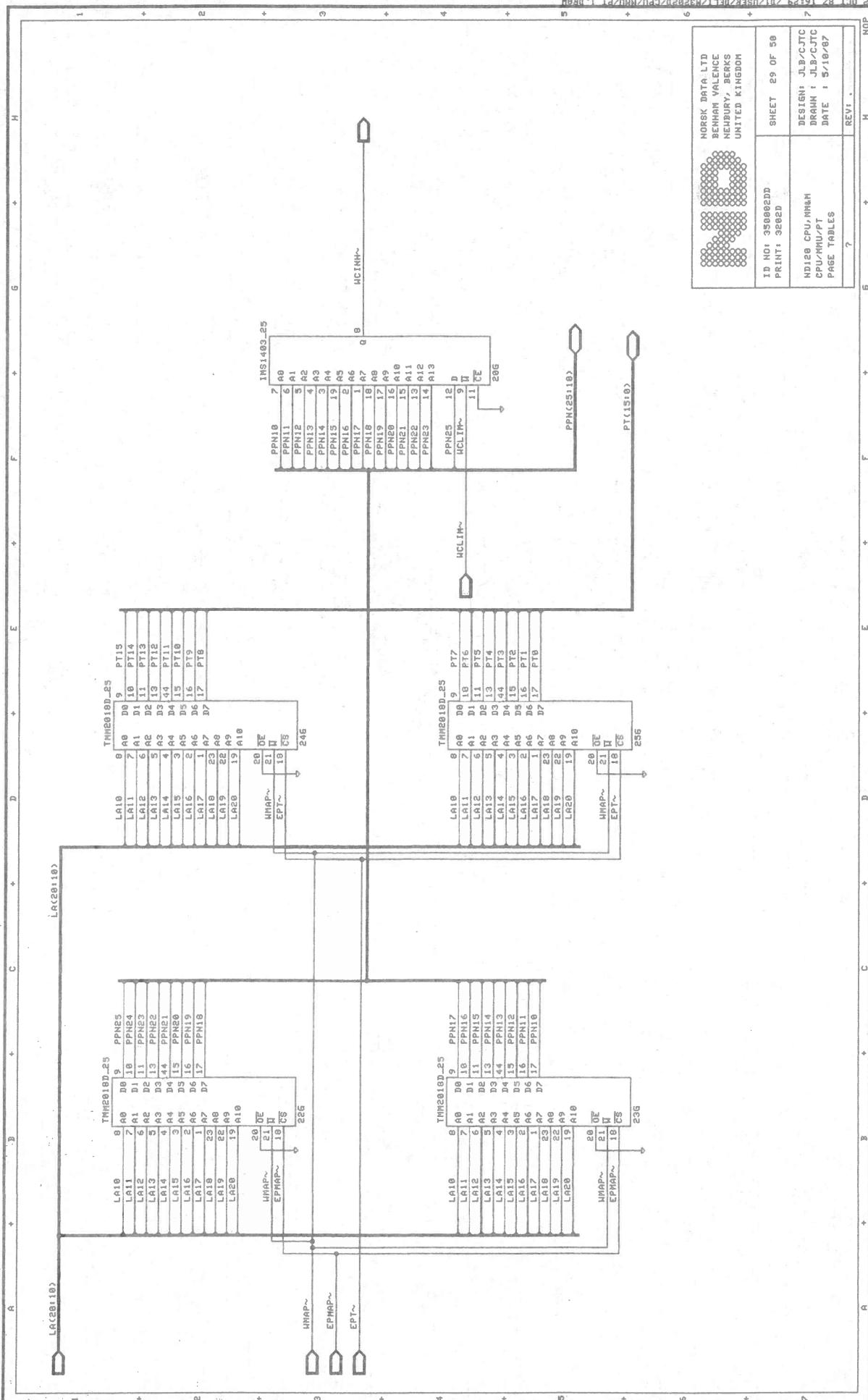
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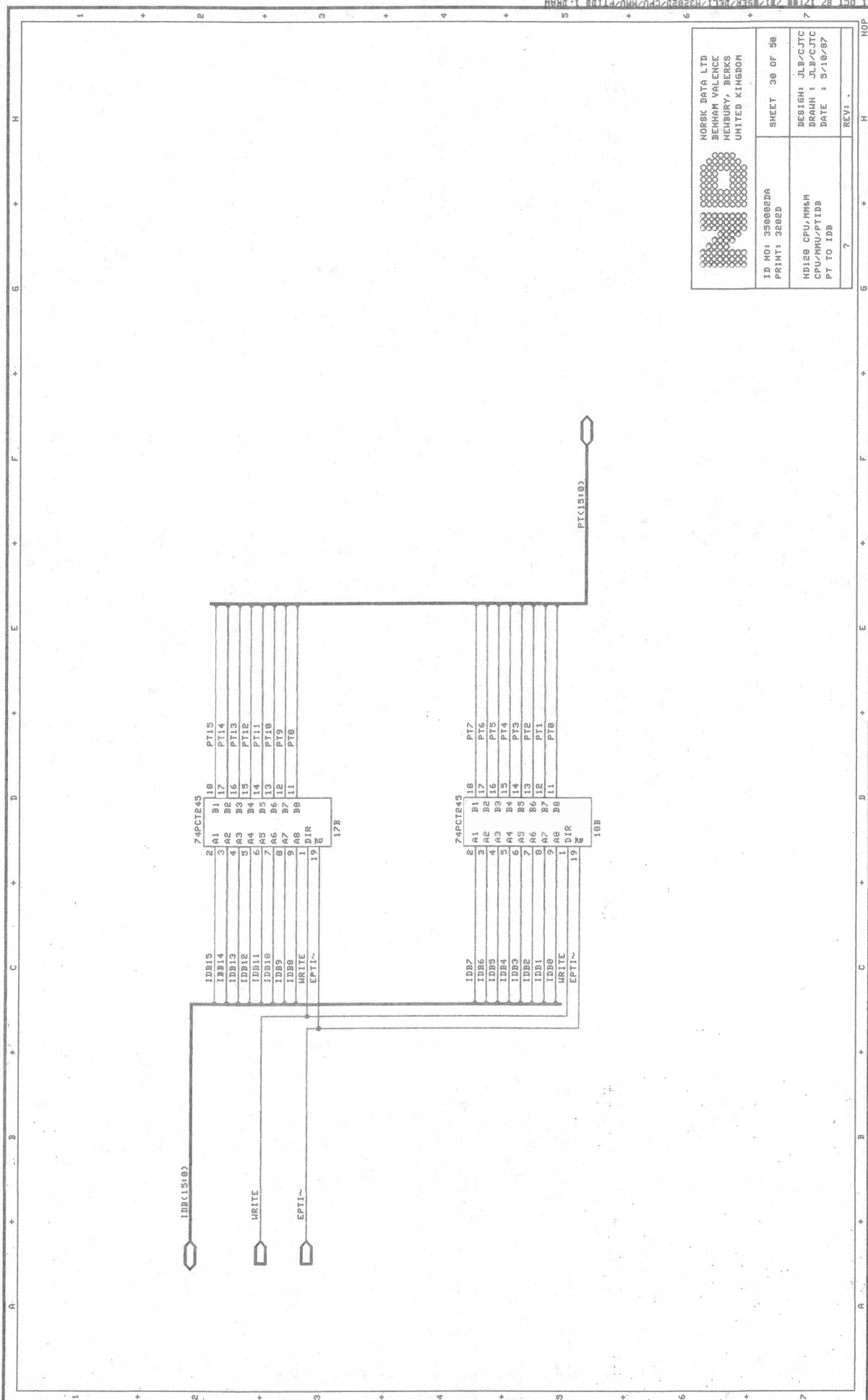
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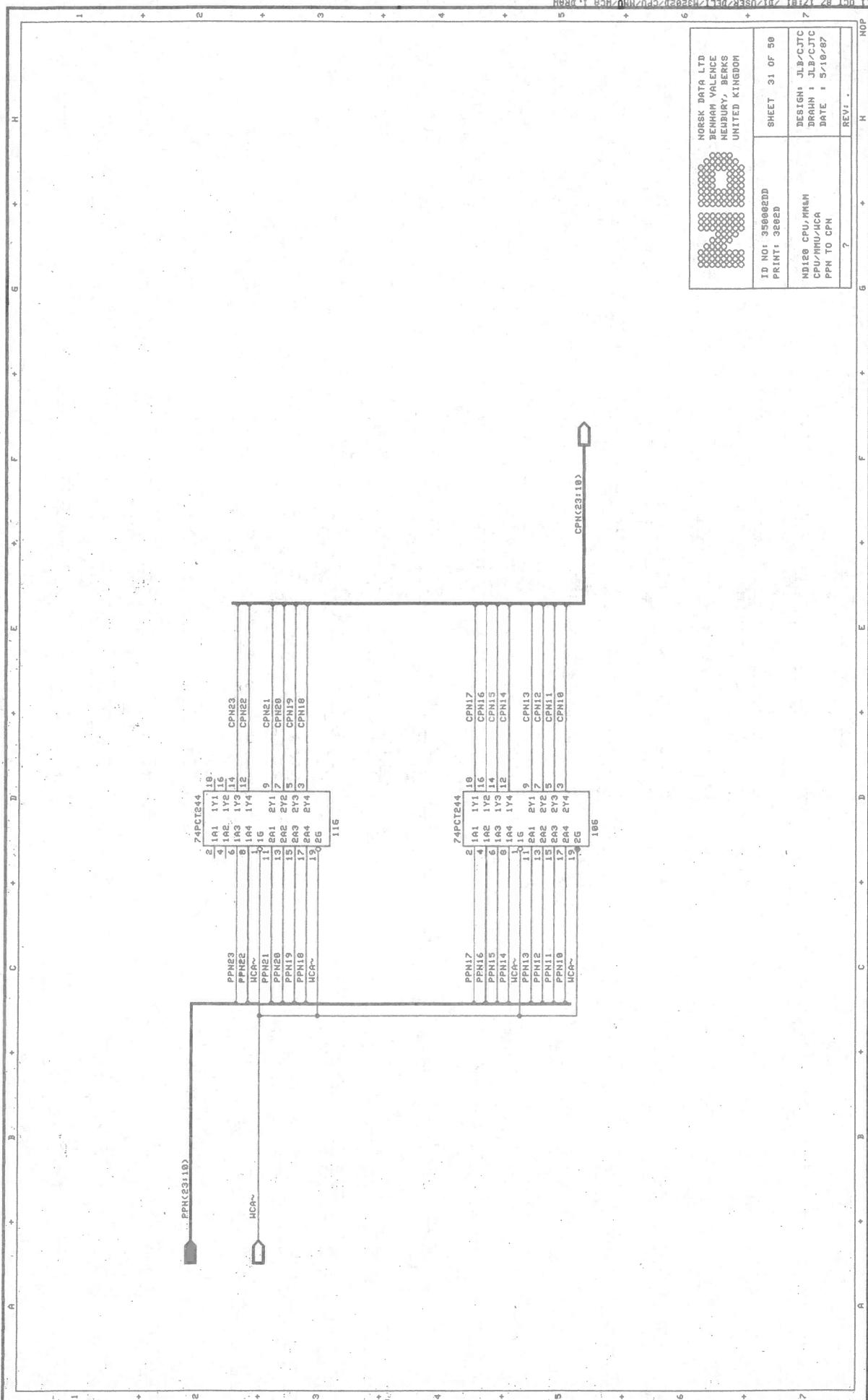
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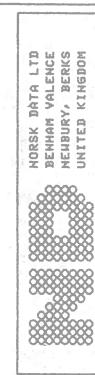
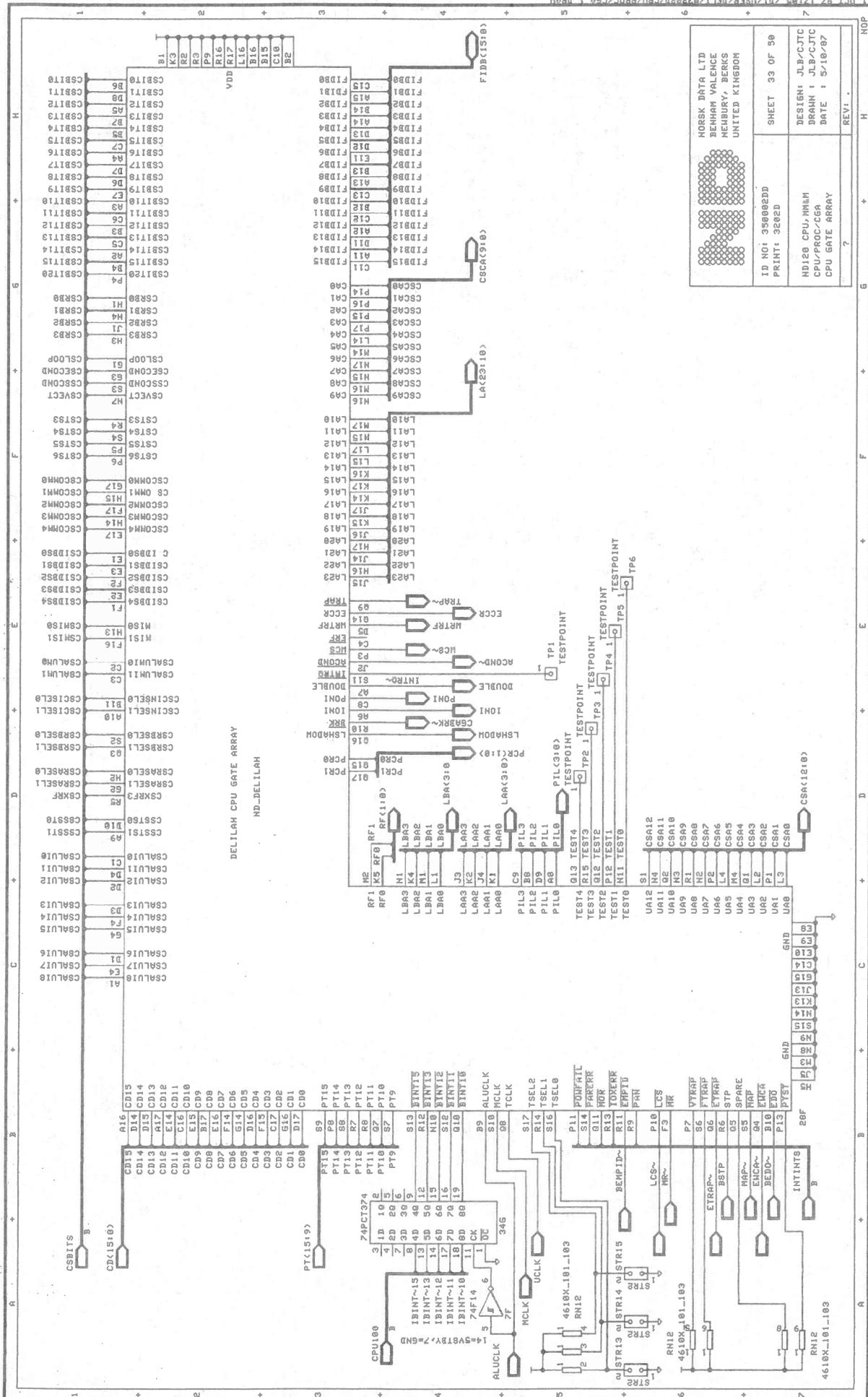
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ID NO: 39882DD	PRINT: 3220
DESIGN: JLB-CJTIC	DRAIN: JLB-CJTIC
CPU-GATE ARRAY	DATE: 5/10/87

SHEET 33 OF 39

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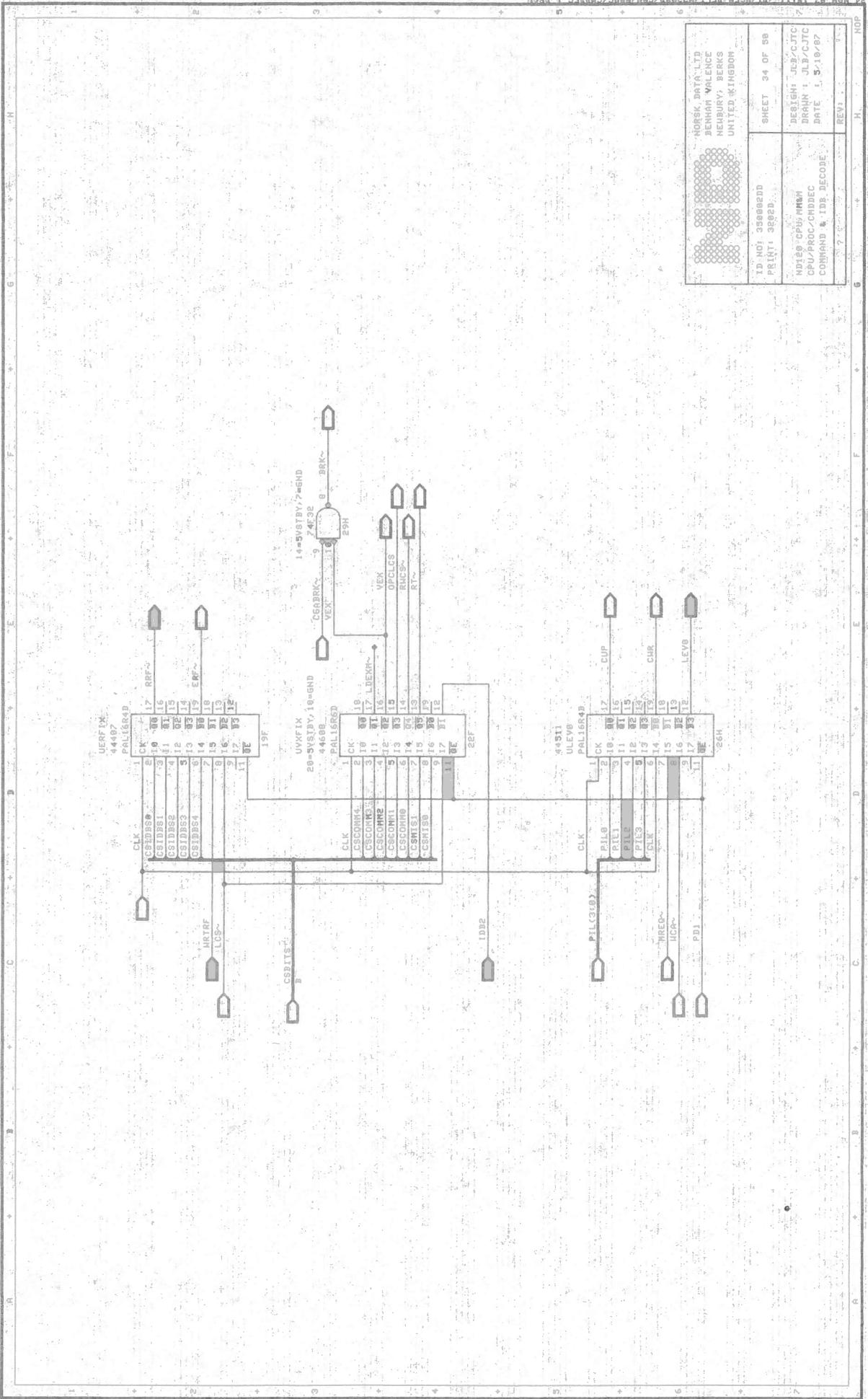
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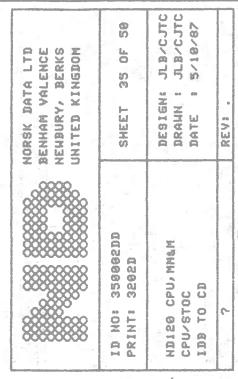
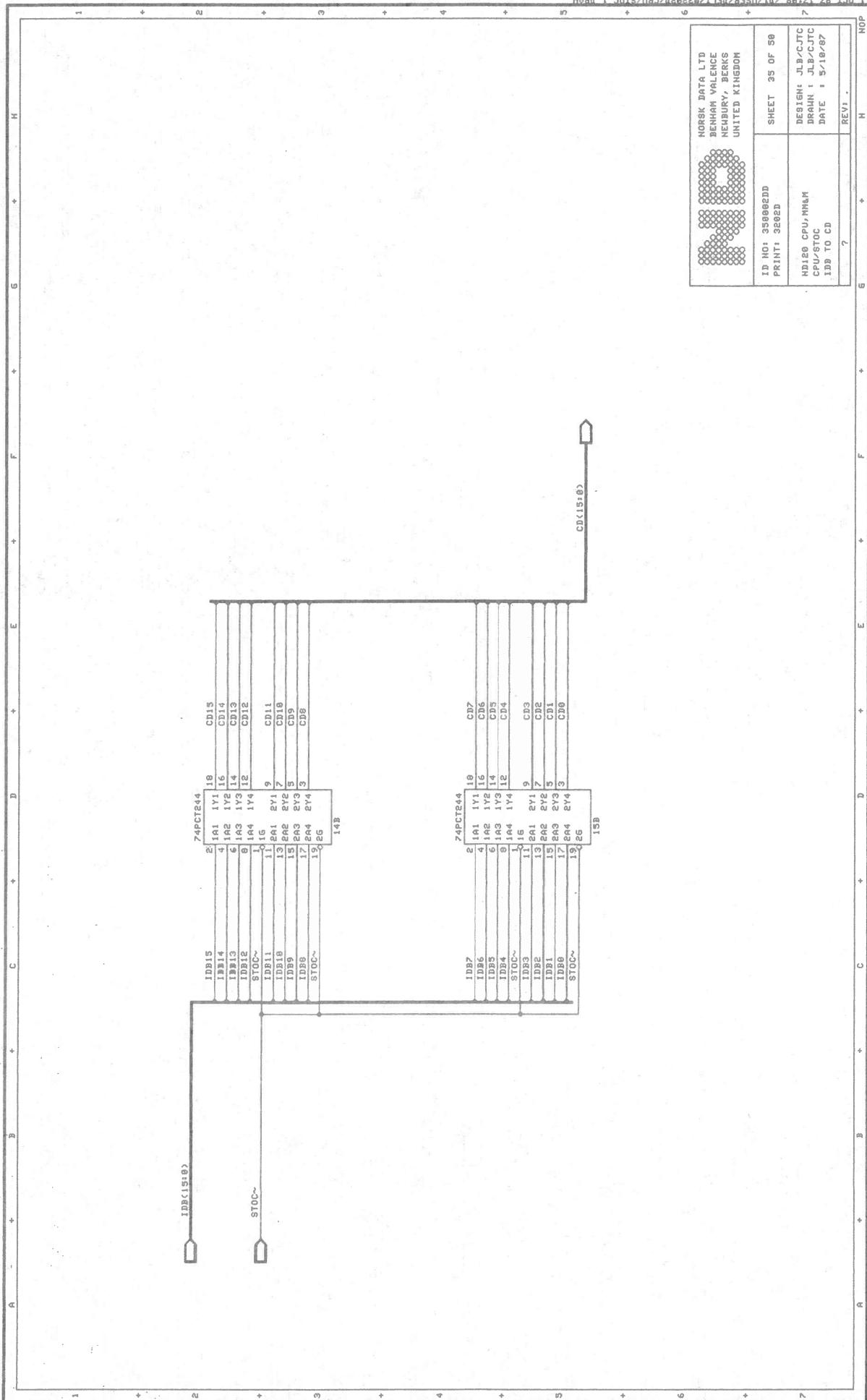
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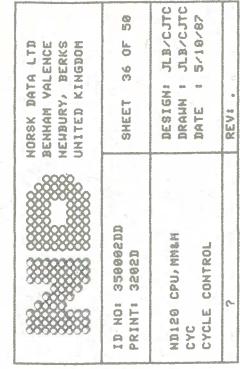
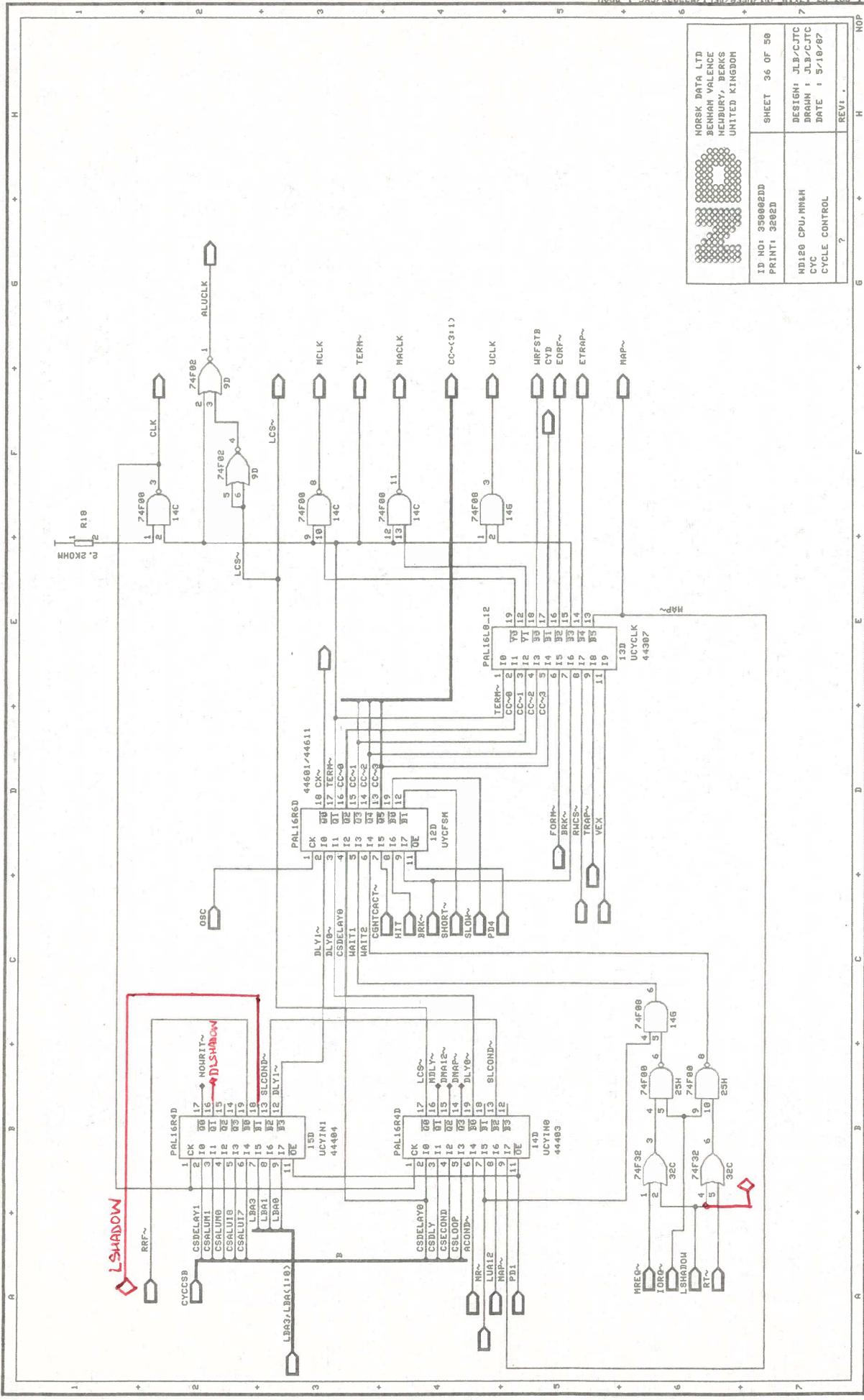
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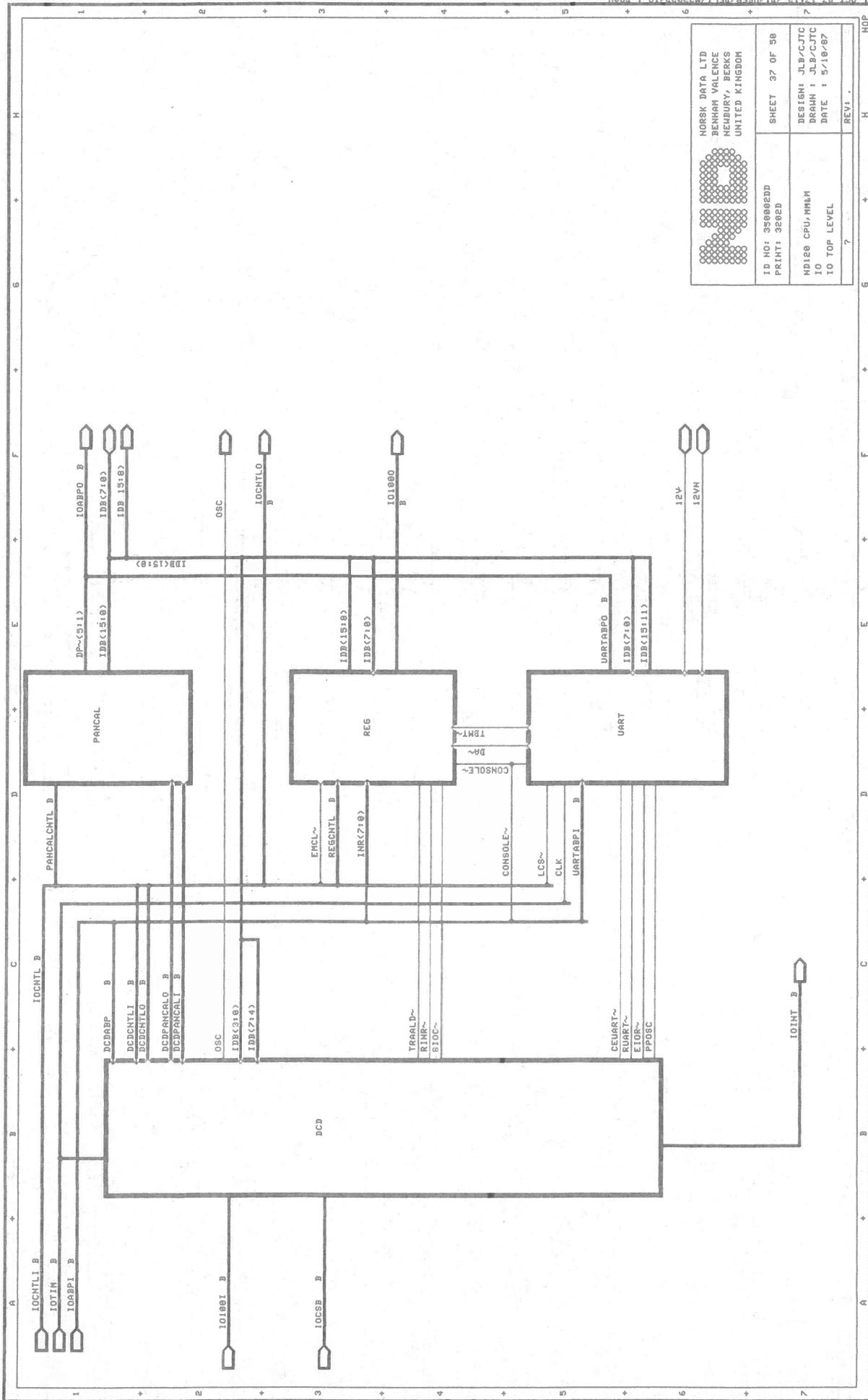


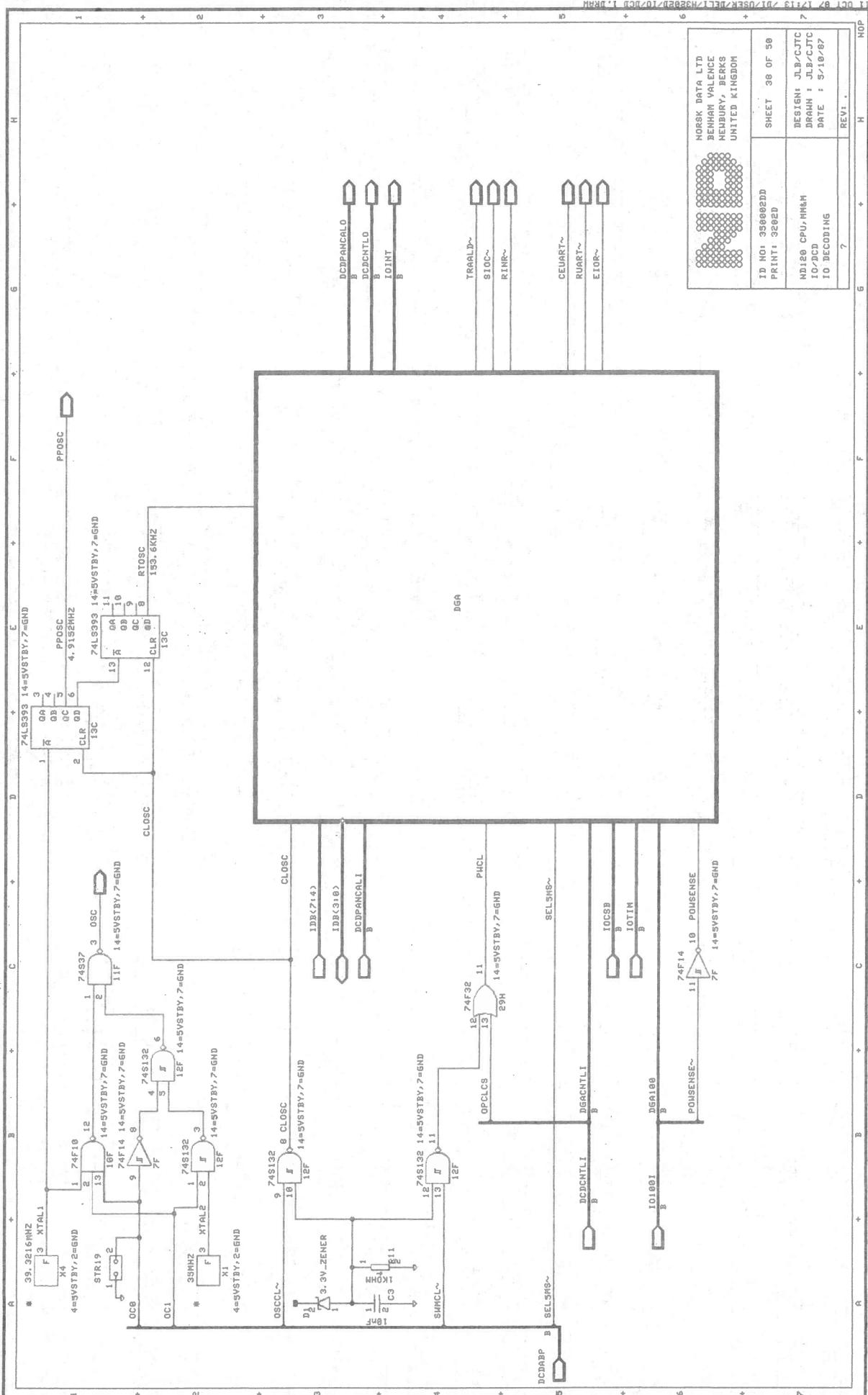
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PRINT: 3282D	
HD1120 CPU/MR&H CYC CYCLE CONTROL	

REV: ?

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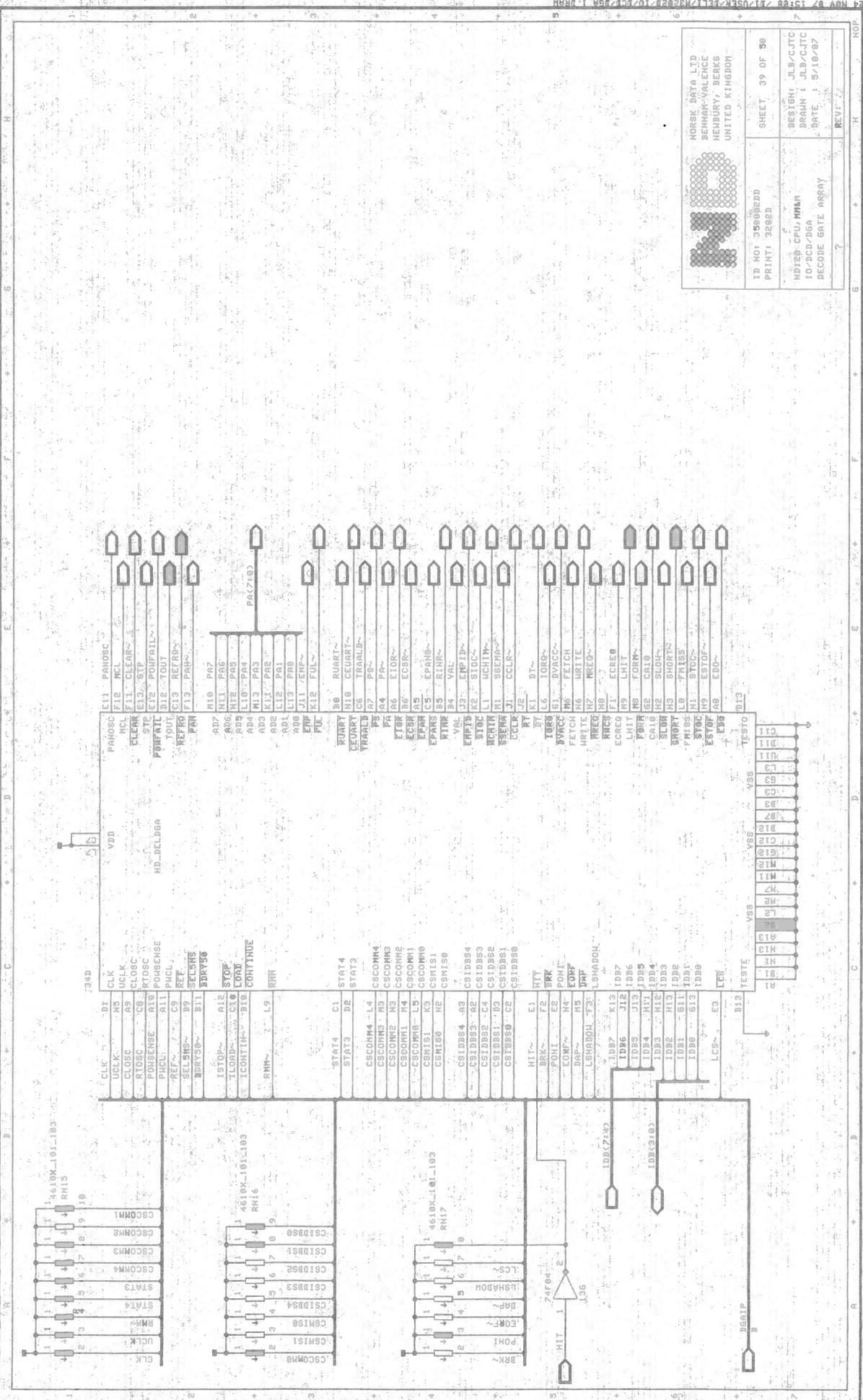
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NORISK DATA LTD
BENHAM VALENCE
HENRURY, BERKS
UNITED KINGDOM

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DRAWN: JLB-CJTIC	
DATE: 5-10-82	
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10 DECODING	
REV: ?	



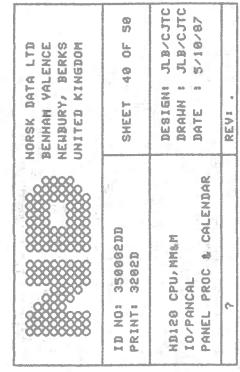
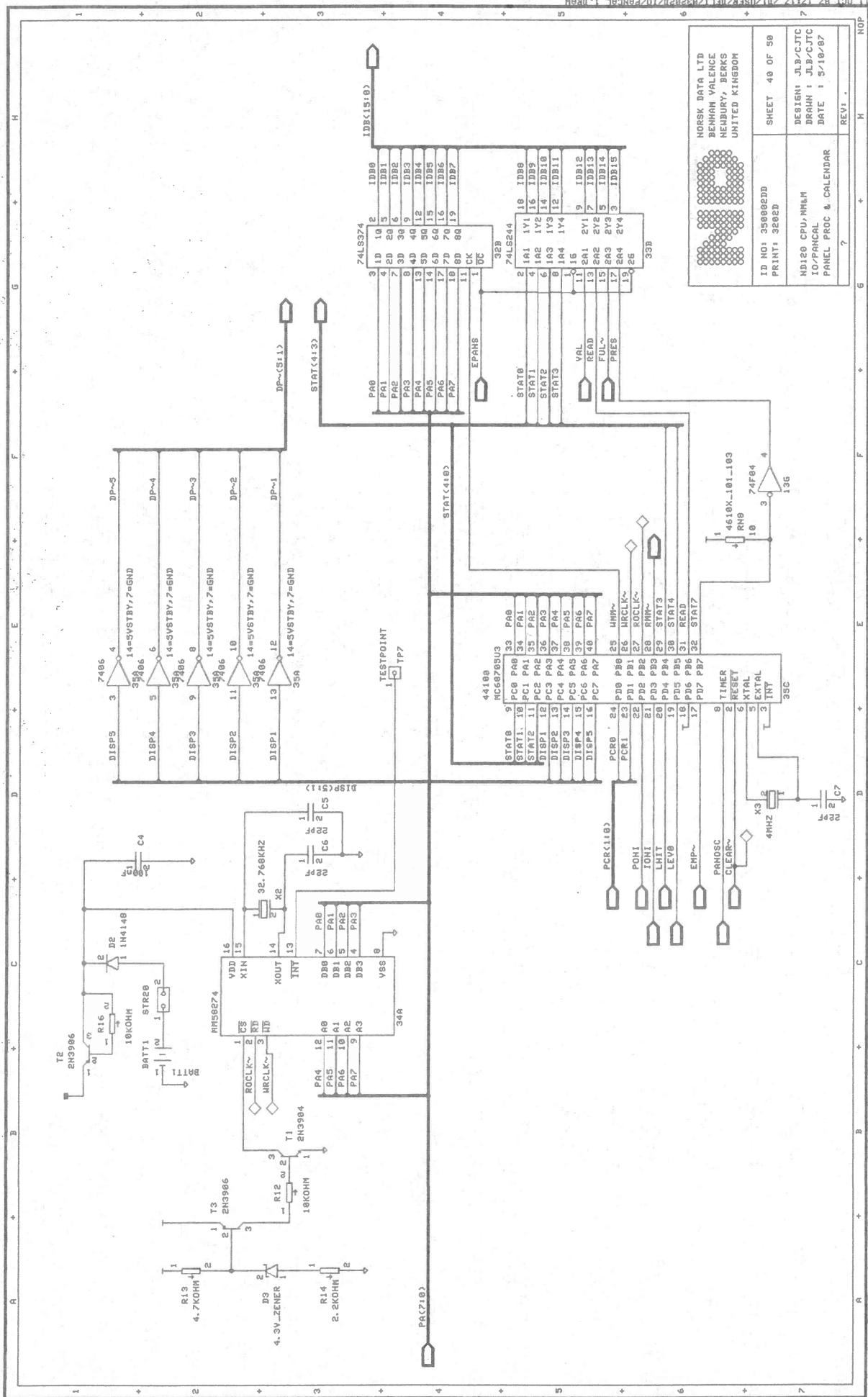
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ND NO: 3508202 PRINT: 32020

SHEET 39 OF 50

DESIGN: JLB-CITIC DRAWN: U.B.CITIC DATE: 5/16/87

ND128 CPU: MM4 DECODE GATE ARRAY



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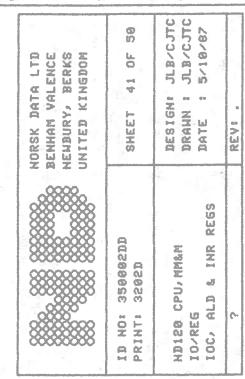
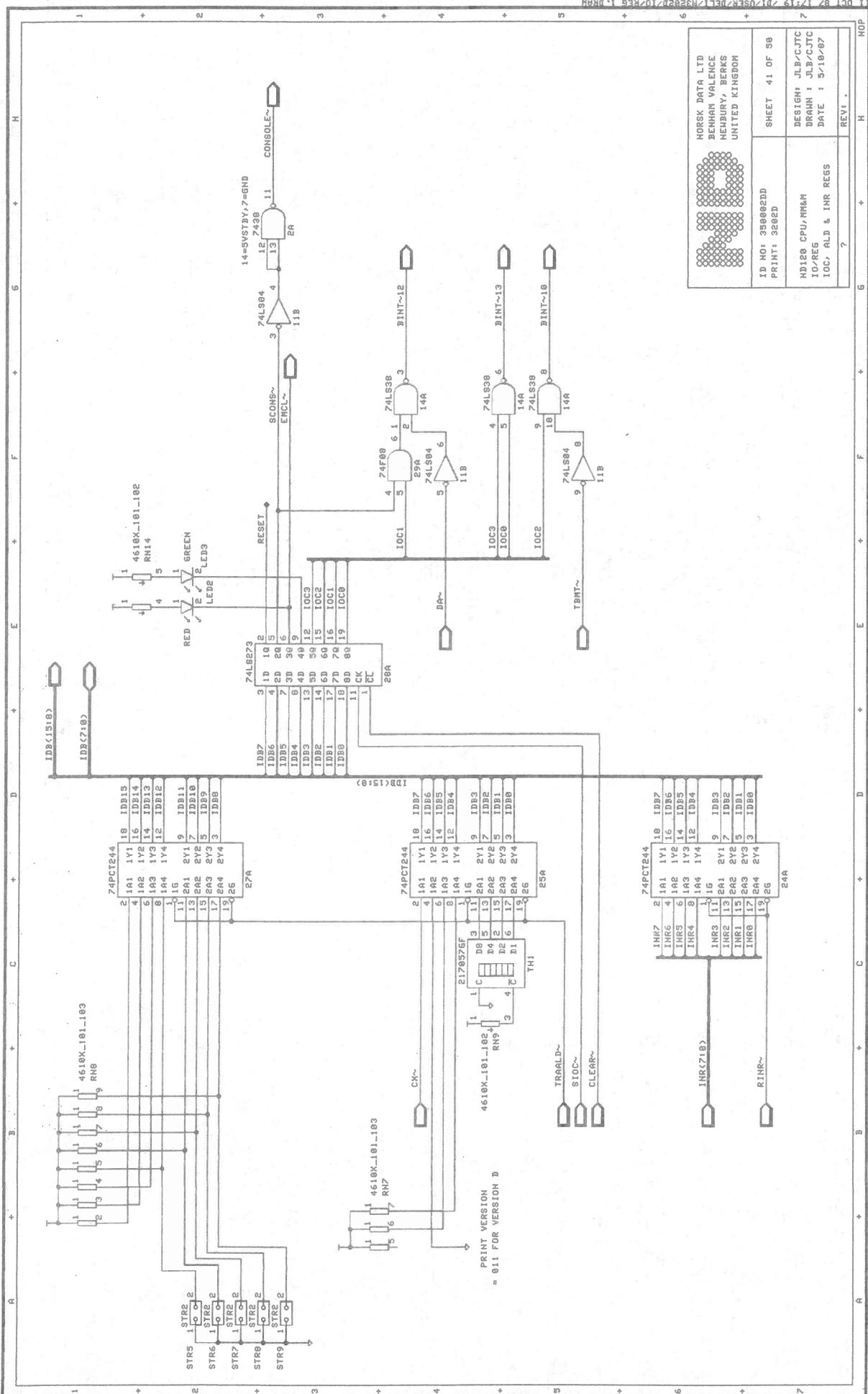
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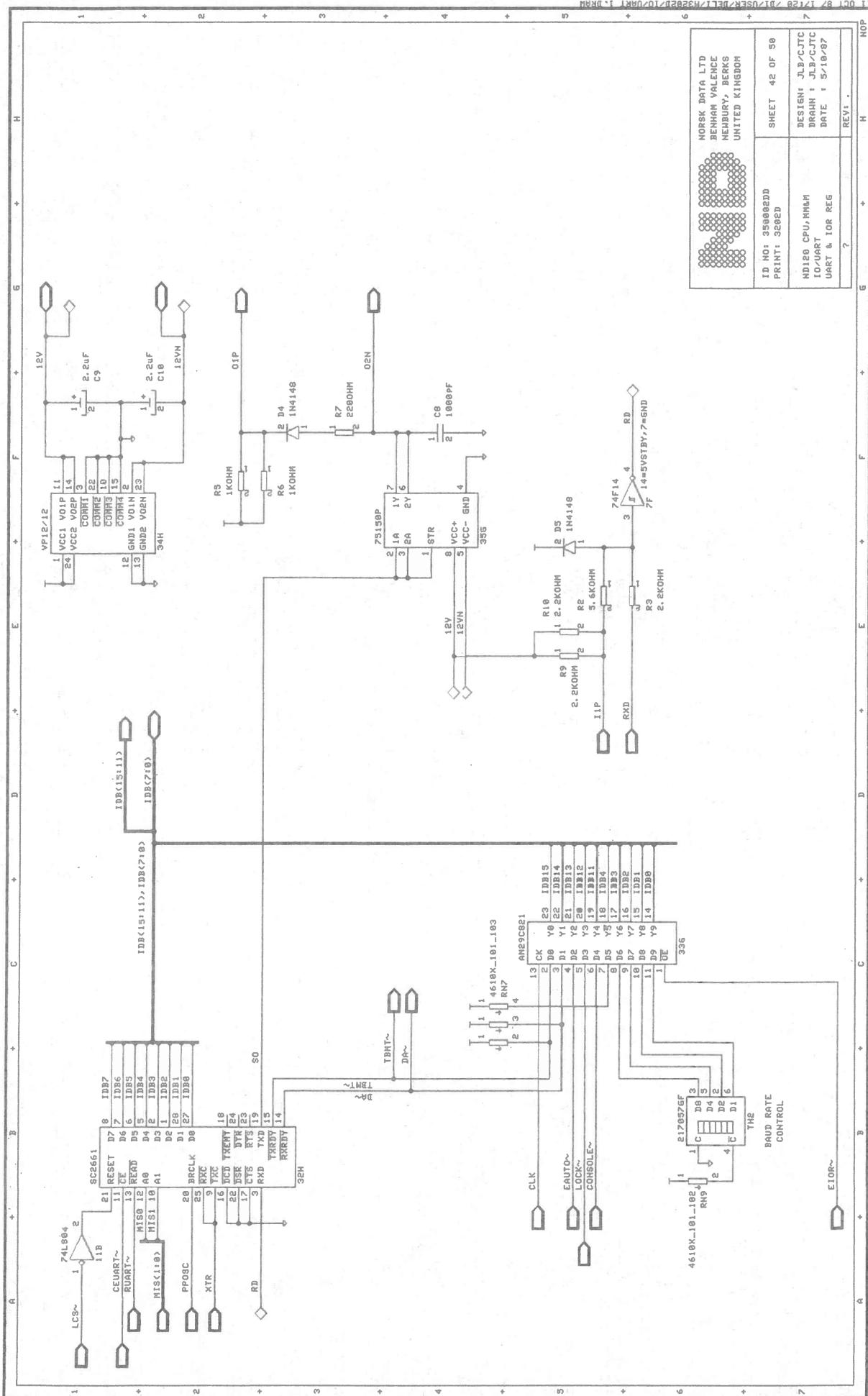
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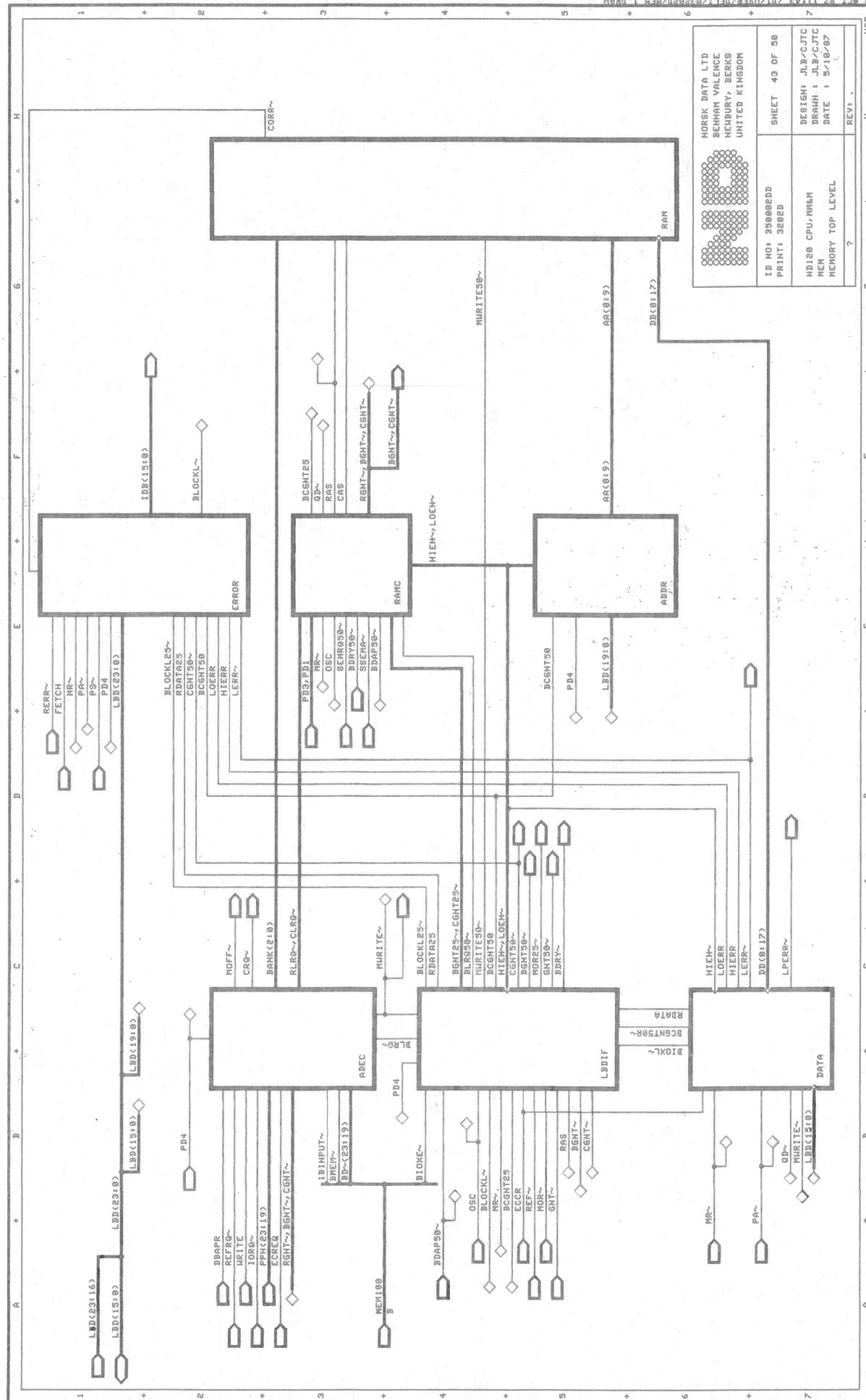
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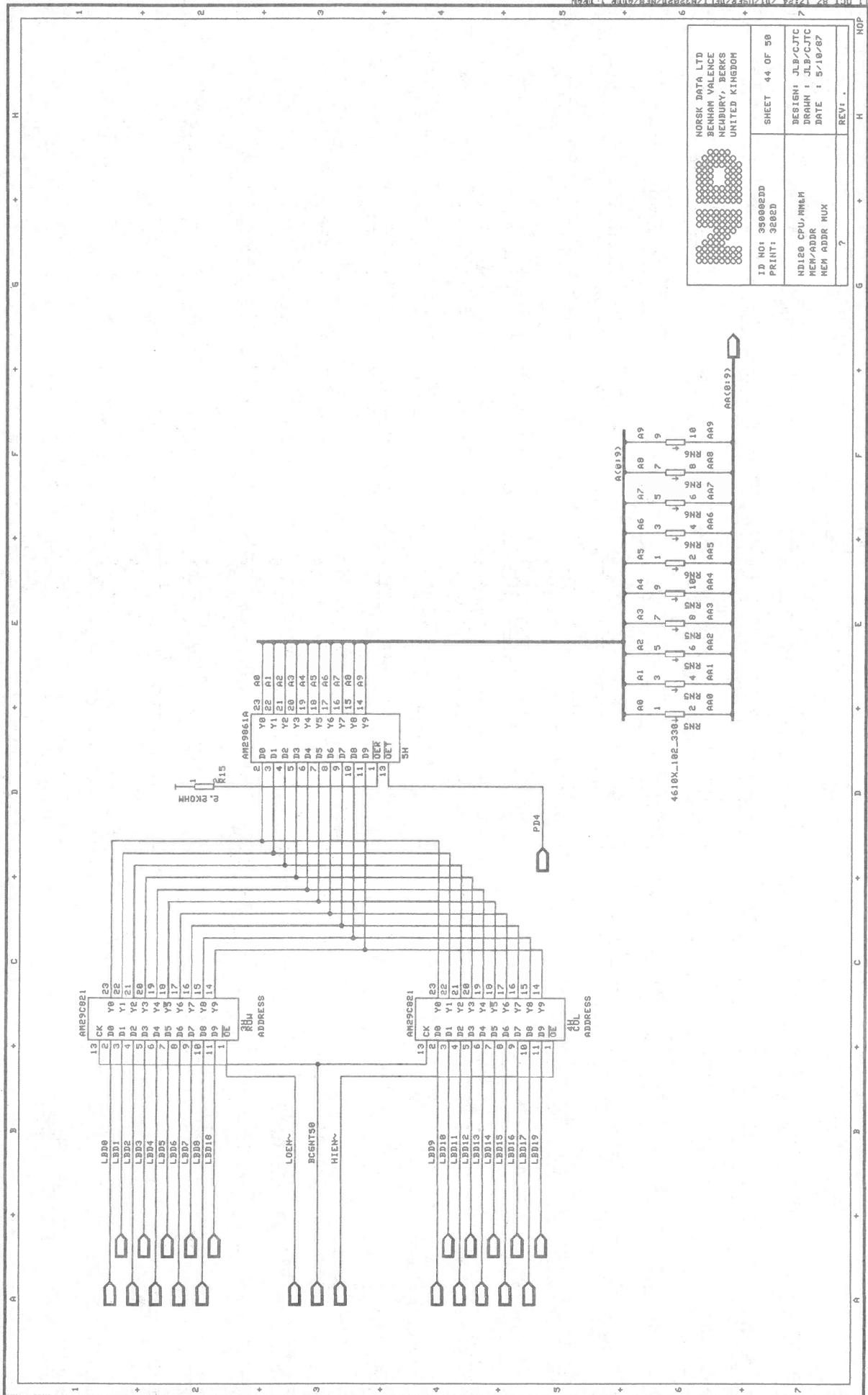
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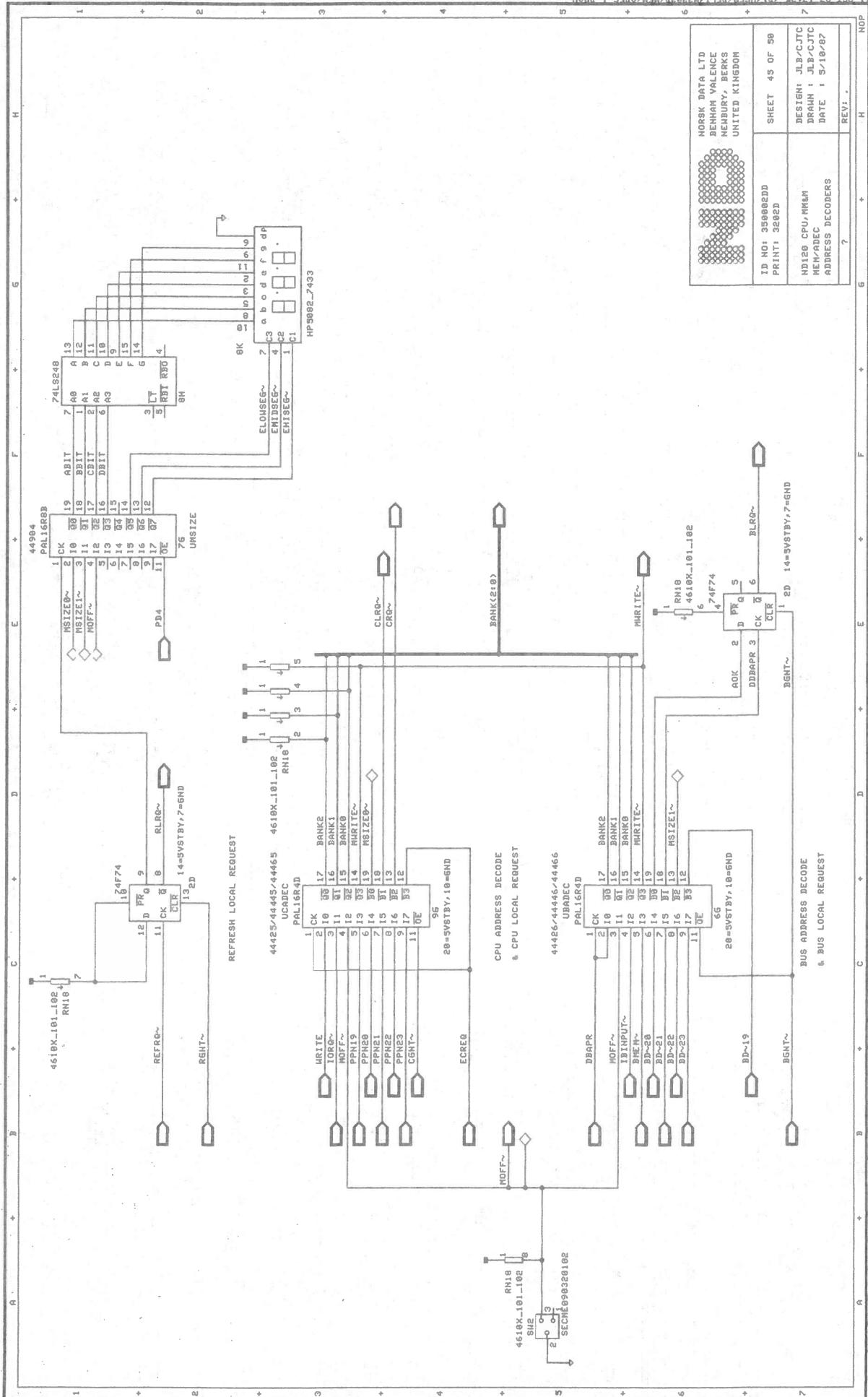
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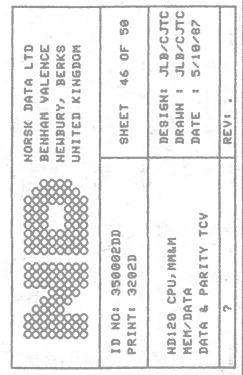
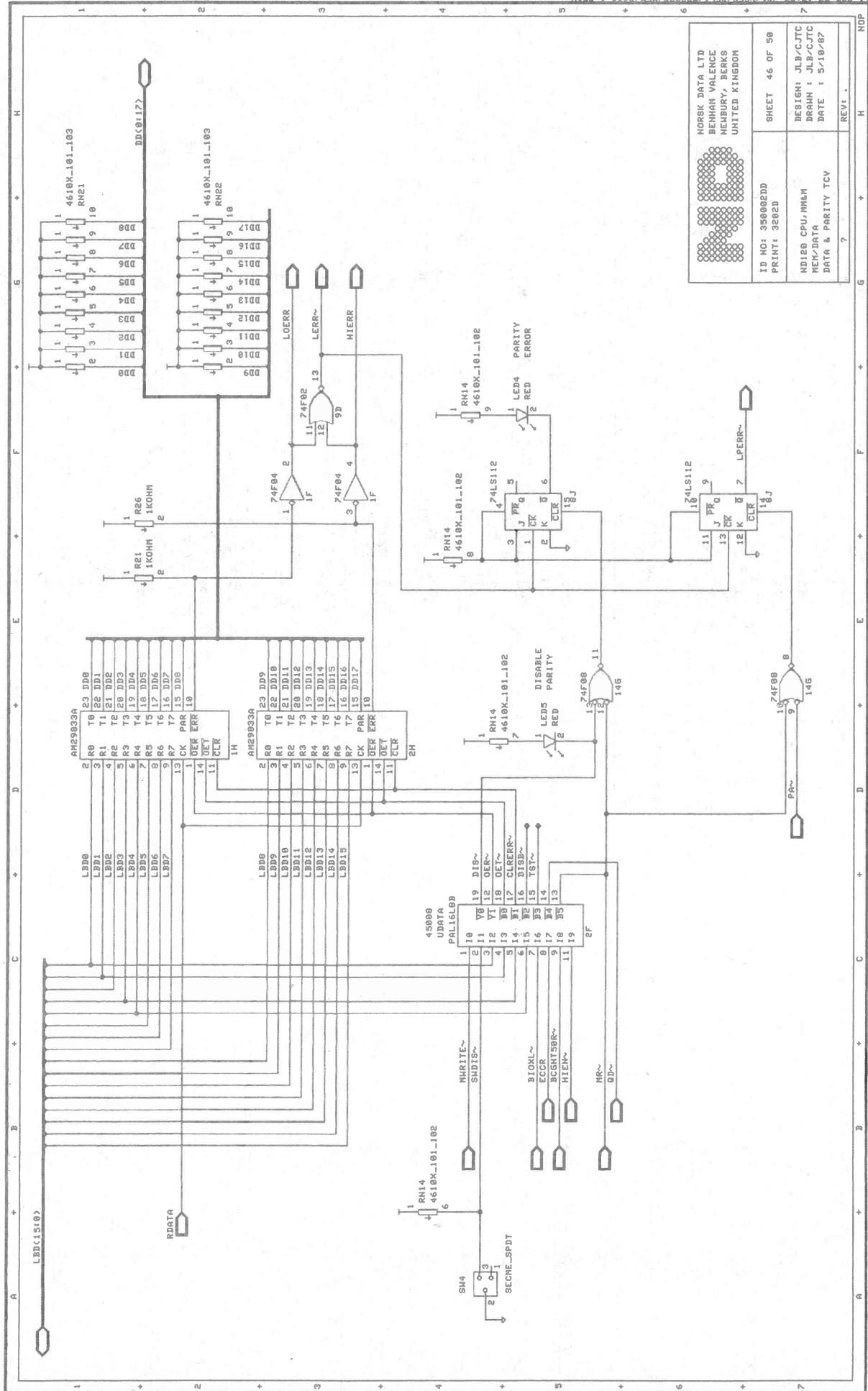


NOKIA DATA LTD BENNAH VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 350000000 PRINT: 30020	
ID NO: 350000000 PRINT: 30020	SHEET 42 OF 50 REV: ?









ID NO: 350002DD	PRINT: 3202D	SHEET 46 OF 50
ND128 CPU,MM&M MEM/DATA DATA & PARITY TCV	DESIGN: JLB/CITC DRAWN: JLB/CITC DATE: 5/19/87	REV: 1
7	?	HOP

