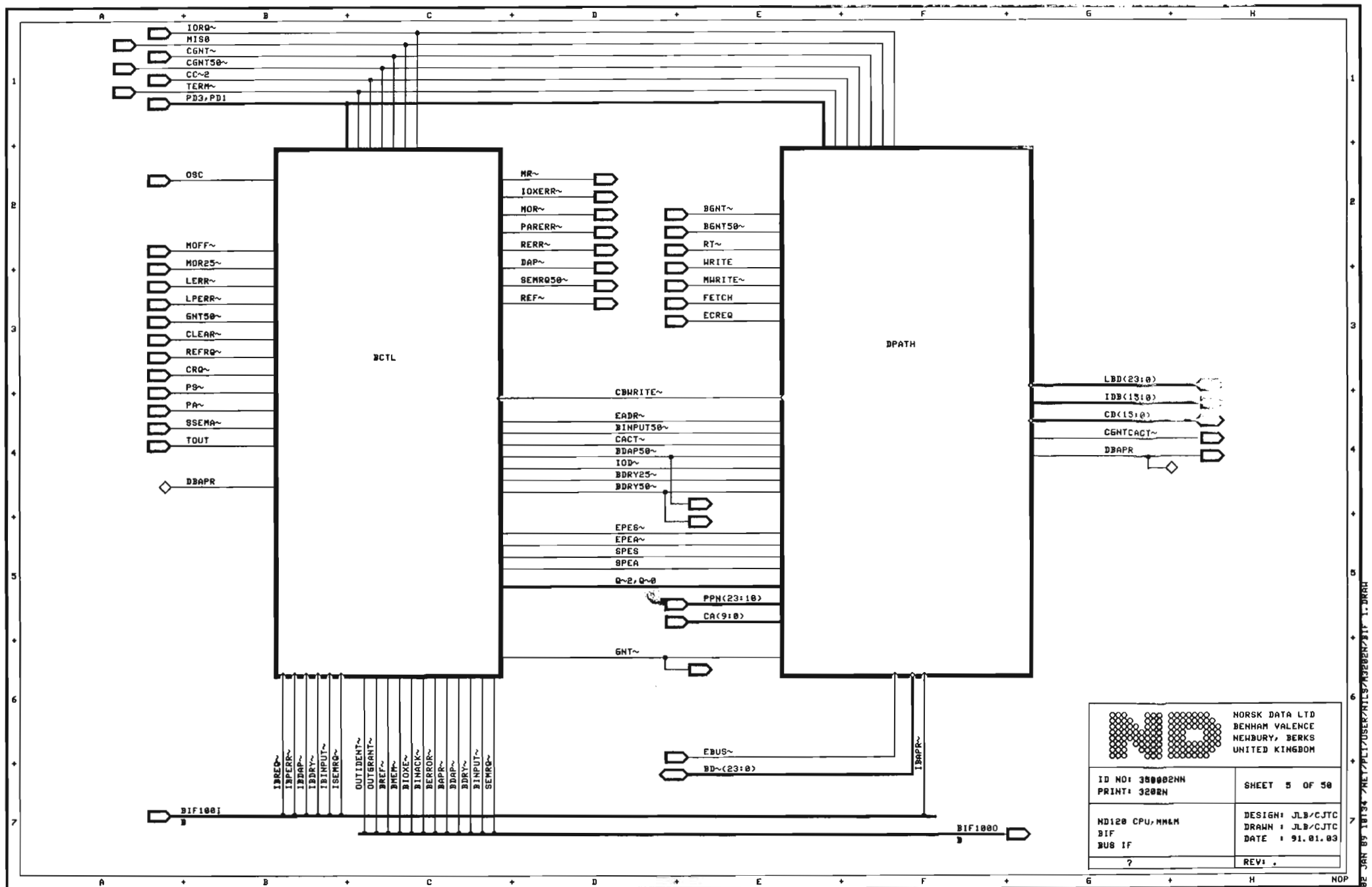
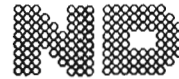
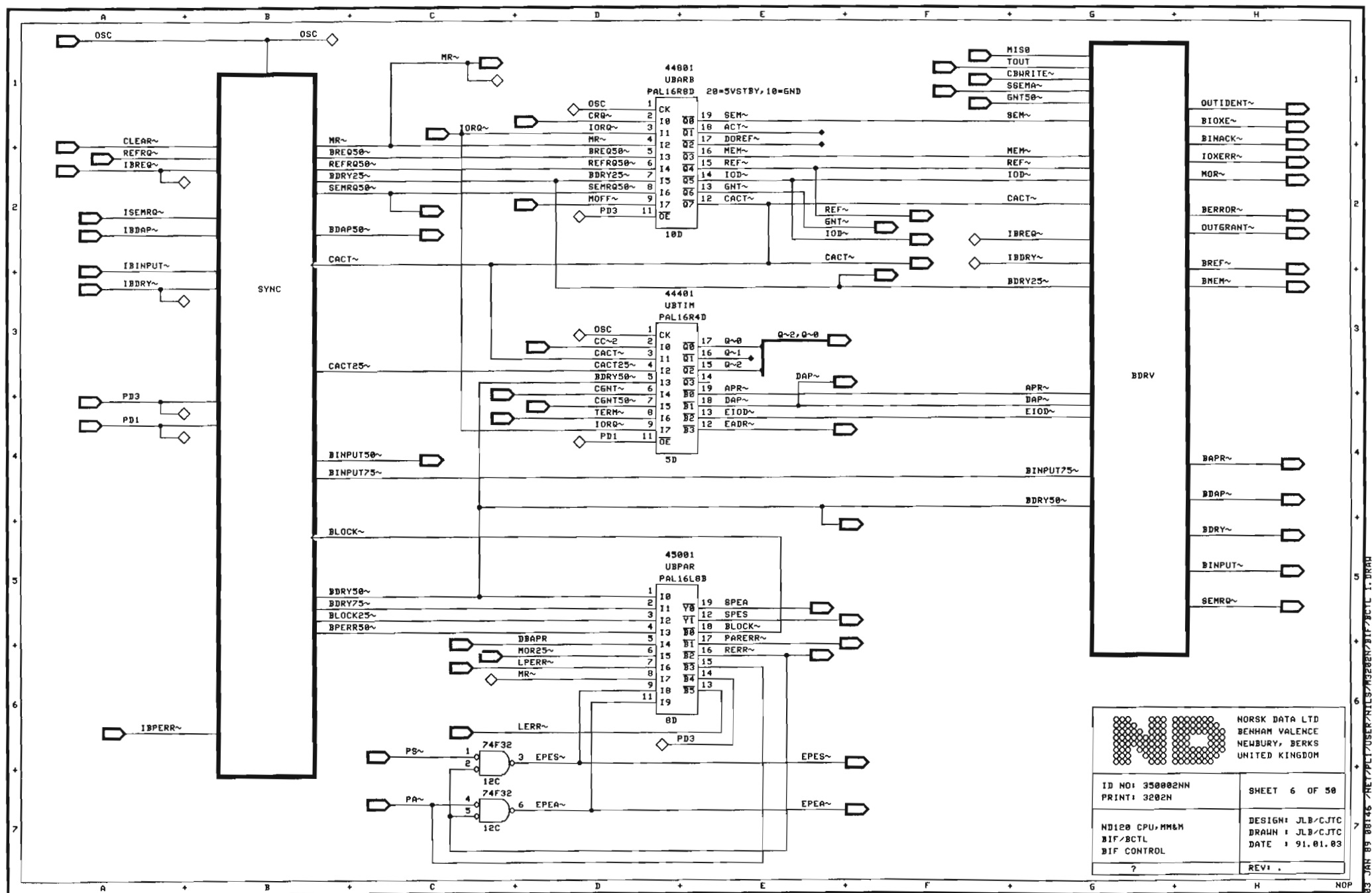
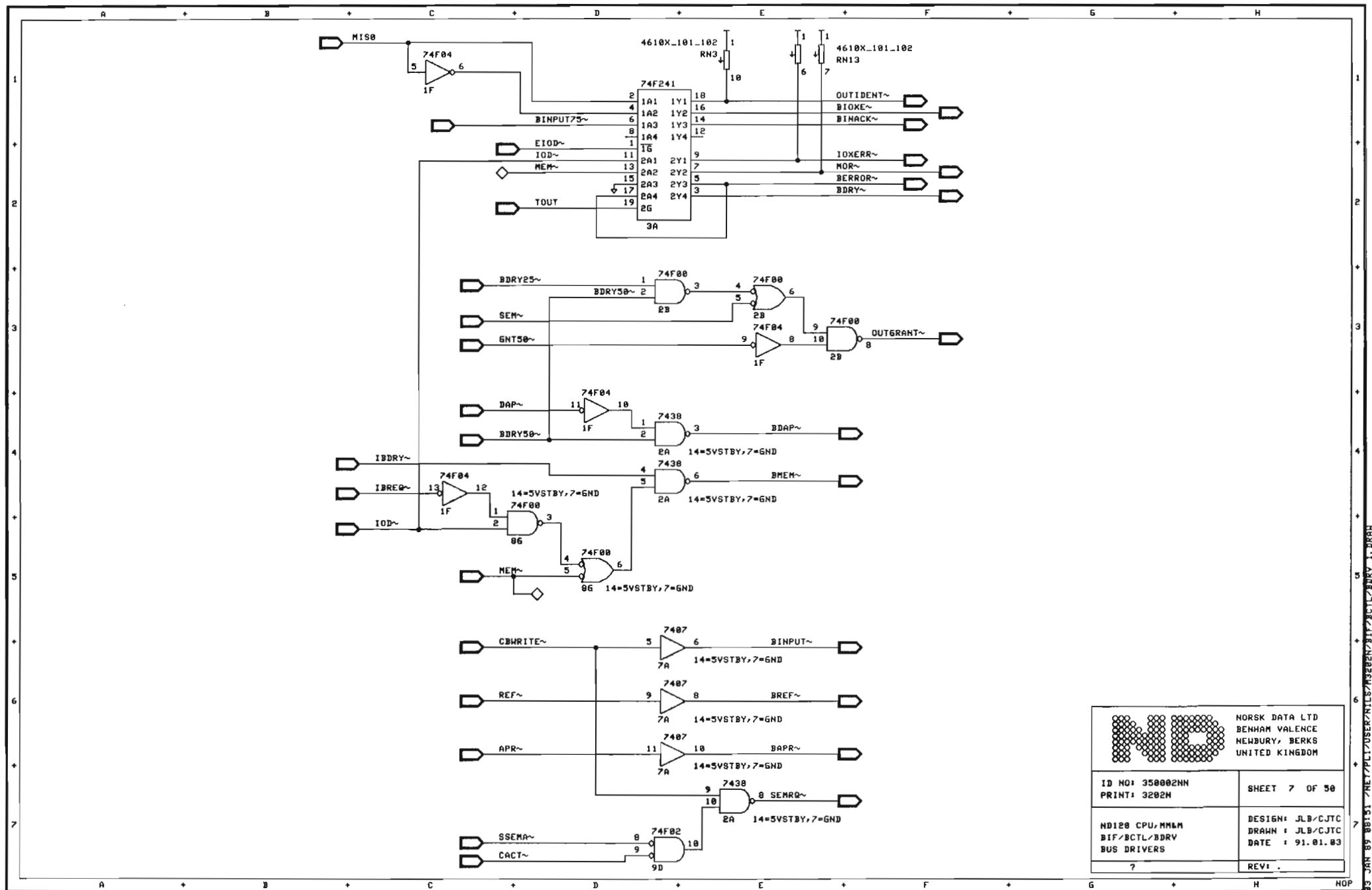
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ID NO: 35002NH PRINT: 3202N	SHEET 4 OF 50
ND120 CPU, MM&M DELILAH TOP LEVEL C PLUG	DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03
7	REV: .

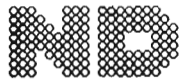
02 JAN 89 09:06 -NET/PLT/USER/NTLS/MSBEN 4, UKAN

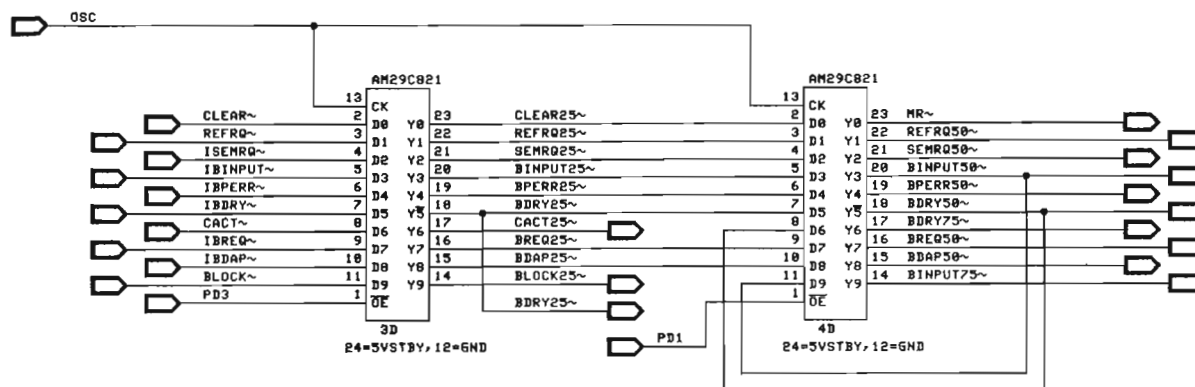



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ID NO: 350002NN PRINT: 3202N		SHEET 5 OF 50	
ND120 CPU, MM&M BIF BUS IF		DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03	
?		REV:	





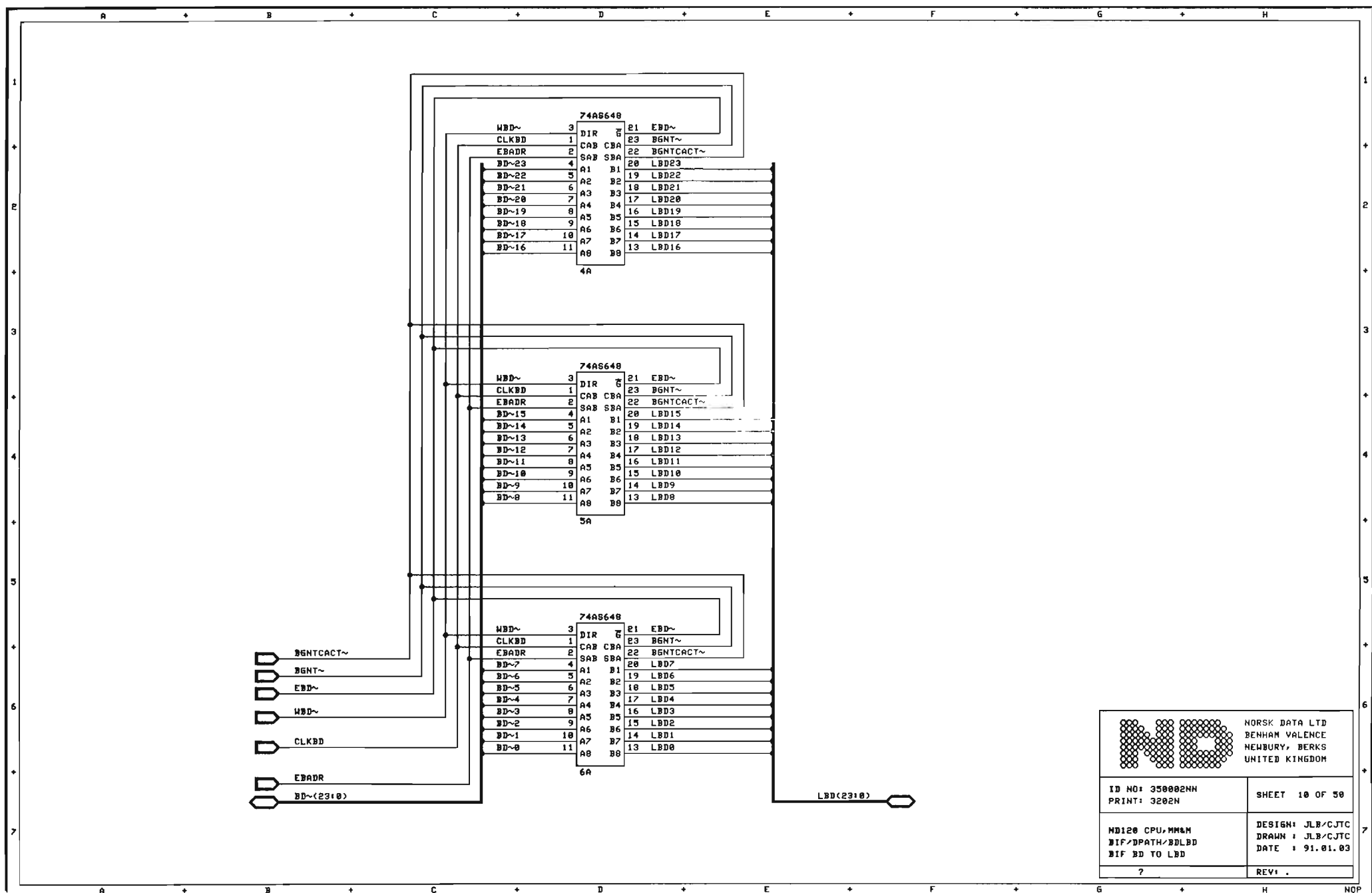
		NORSK DATA LTD BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 350002NN PRINT: 3202N		SHEET 7 OF 50	
HD120 CPU,MM/M BIF/BCTL/BDV BUS DRIVERS		DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03	
?		REV: .	

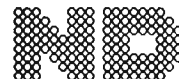


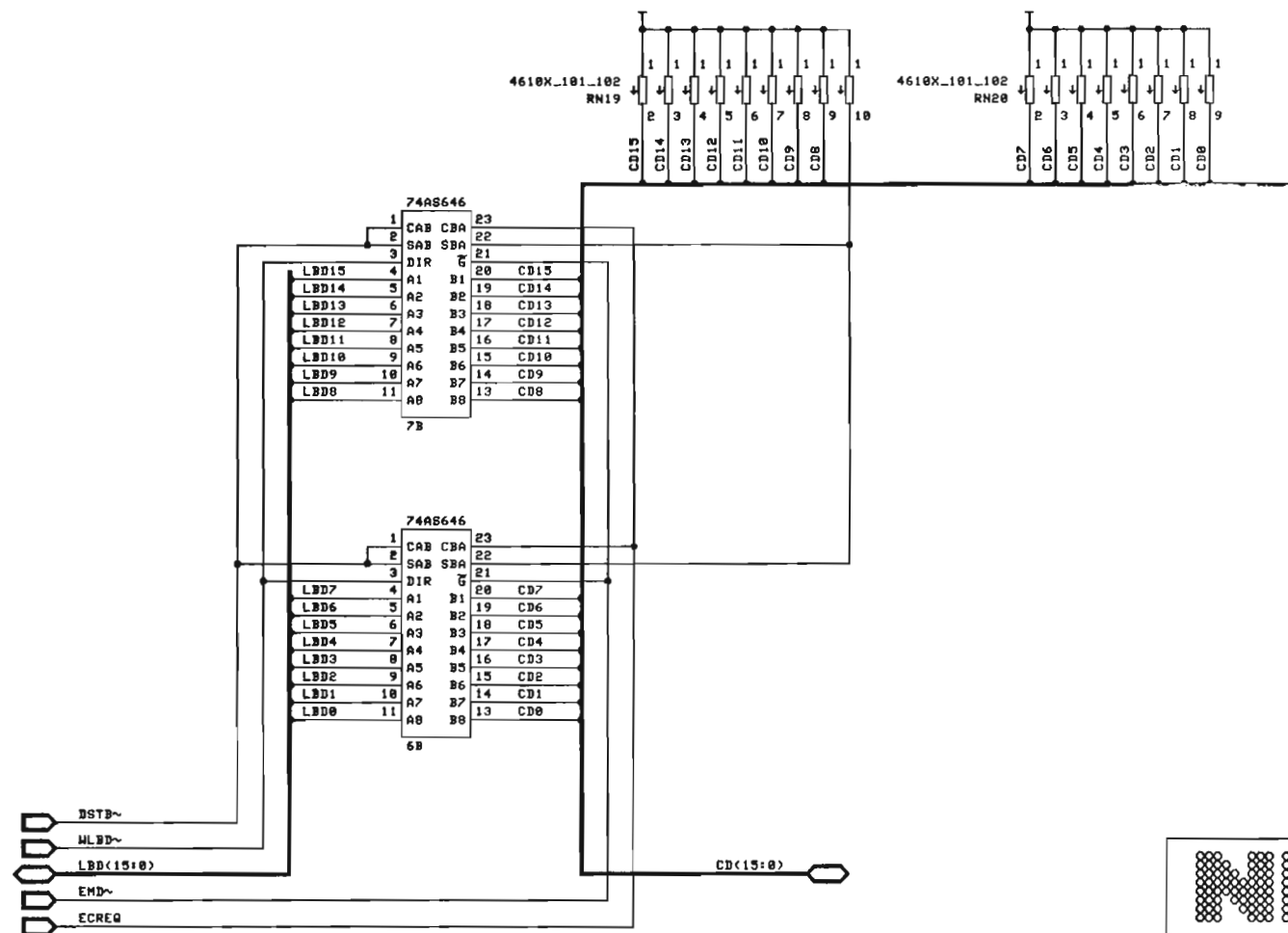
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ID NO: 35002NN PRINT: 3202N		SHEET 8 OF 50	
ND120 CPU/MM&M BIF/BCTL/SYNC BIF SYNC		DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03	
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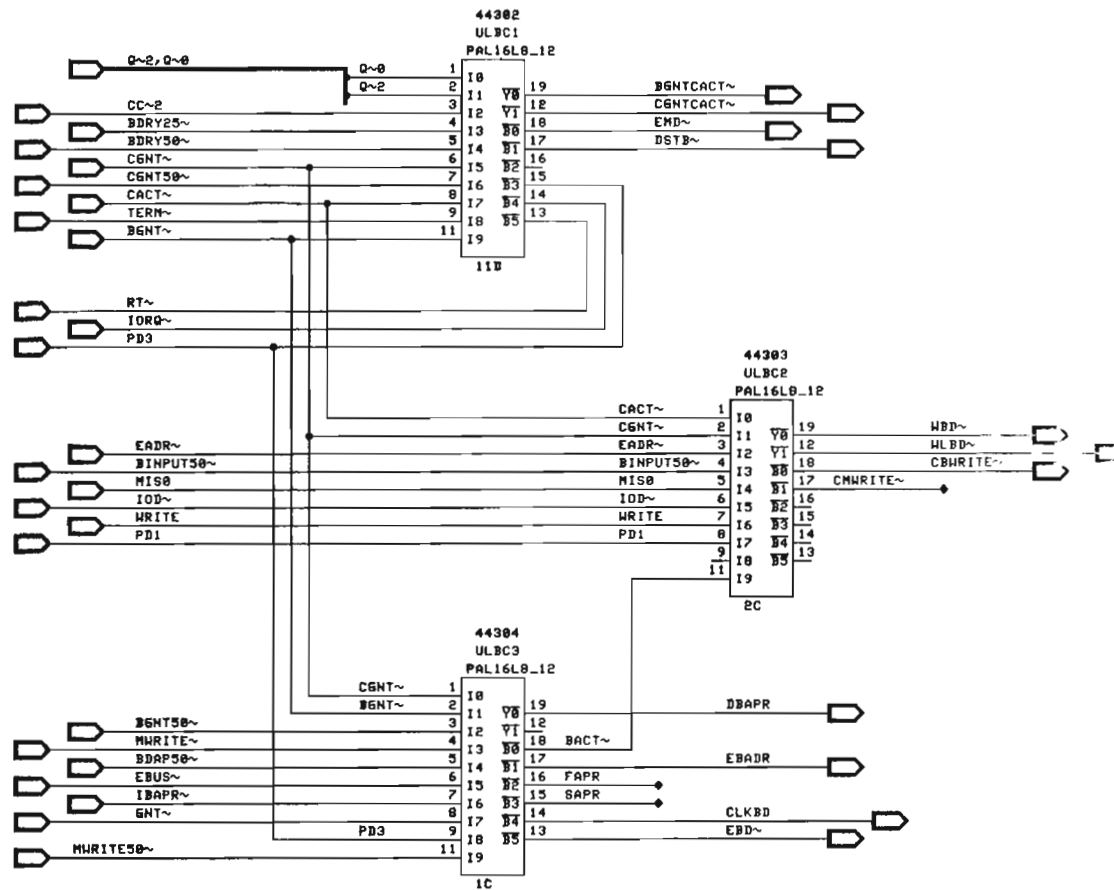





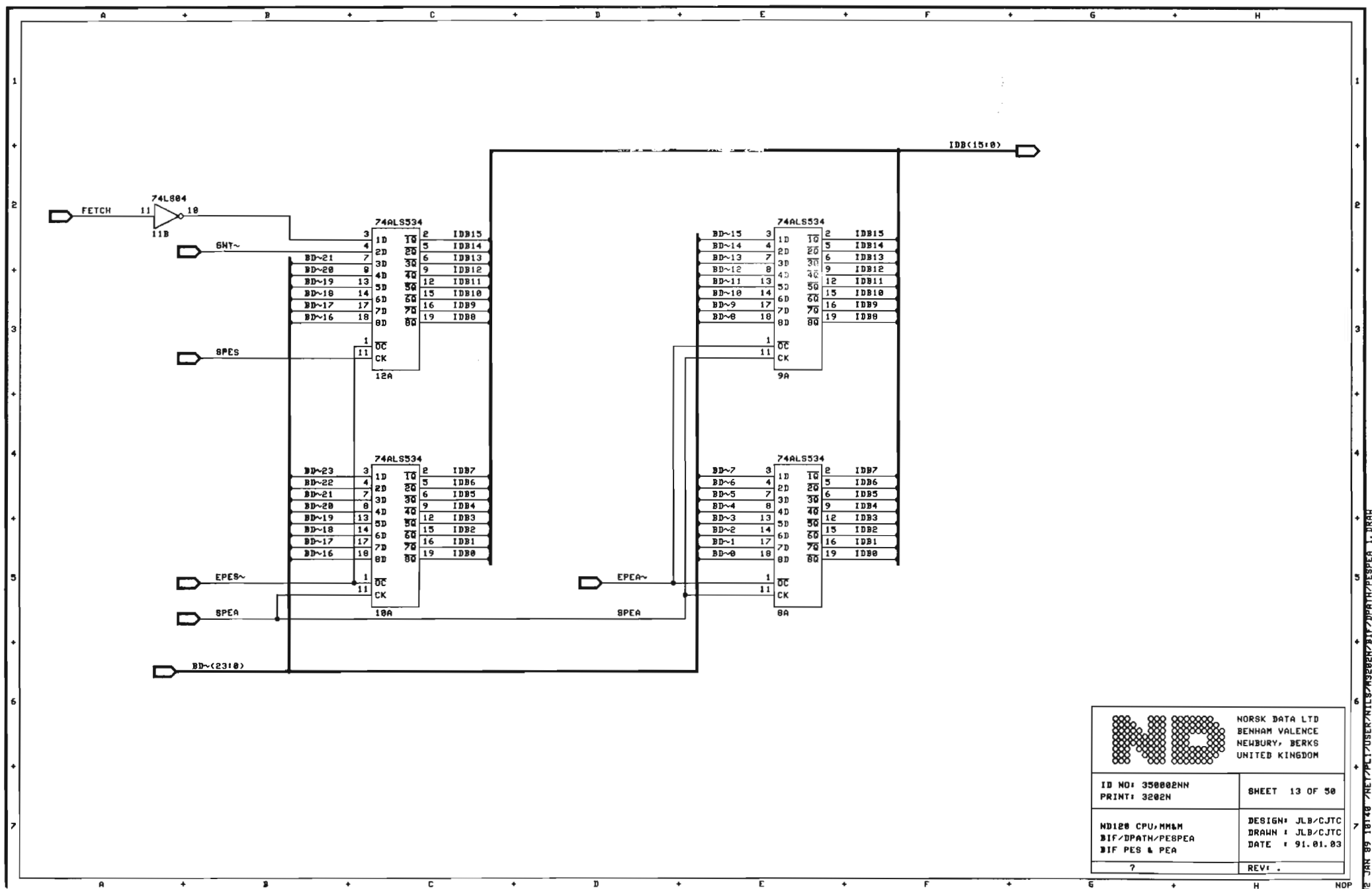
 <b>NORSK DATA LTD</b> BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 35002NN PRINT: 3202N	SHEET 10 OF 50
MD120 CPU, MM&M BIF/DPATH/BDLBD BIF BD TO LBD	DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03
?	REV: .




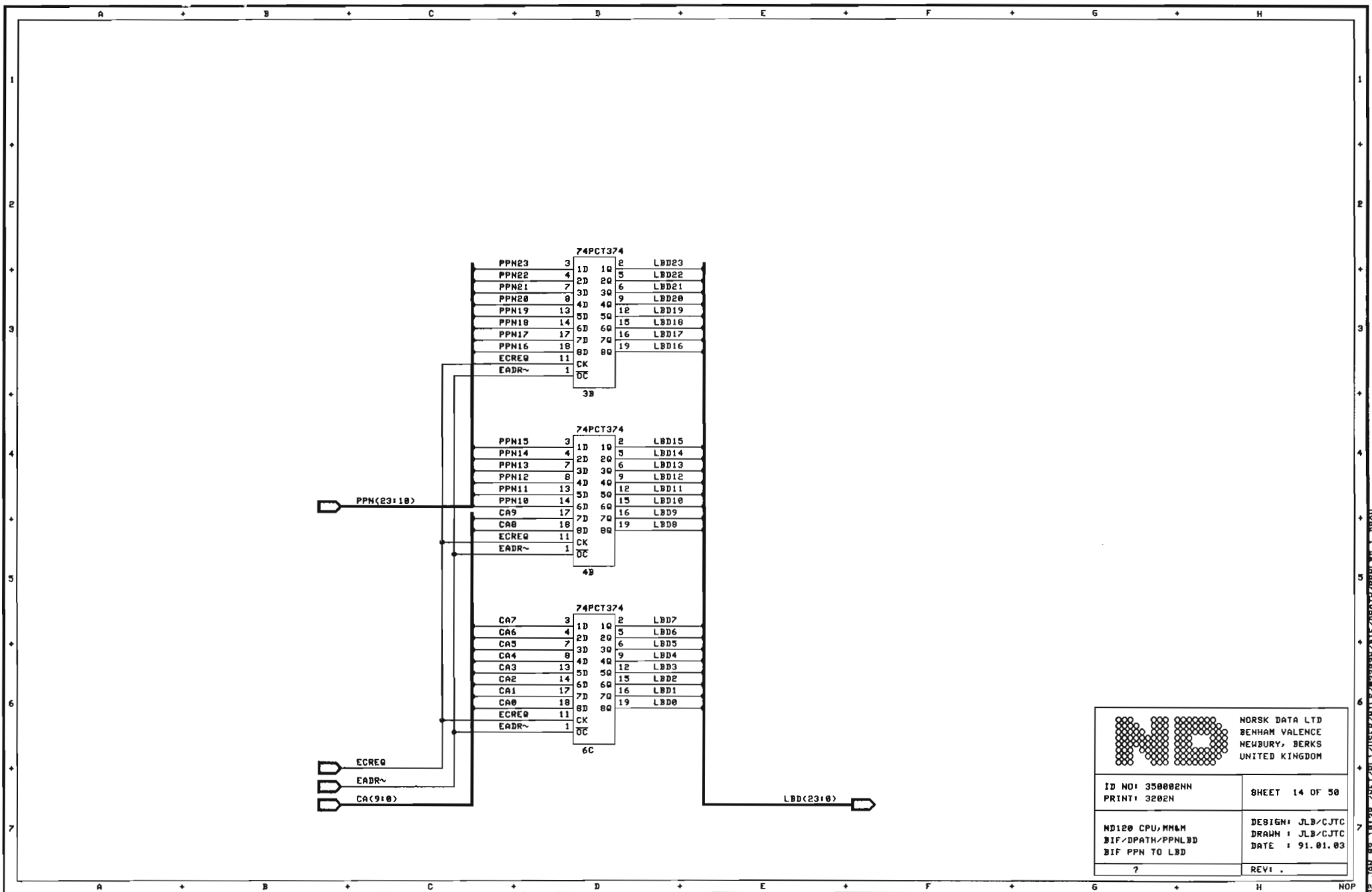
 HORSK DATA LTD BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 350002NH PRINT: 3202N	SHEET 11 OF 50
MD120 CPU-MM&M BIF/DPATH/CDLBD BIF CD TO LBD	DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03
?	REV: .



		NORSK DATA LTD BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 35002NN PRINT: 3202N		SHEET 12 OF 50	
MD120 CPU/MMM BIF/DPATH/LBDCTL LBD CONTROL		DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03	
?		REV:	



		NORSK DATA LTD BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM
ID NO: 350002NN PRINT: 3202N		SHEET 13 OF 50
ND128 CPU/MM&M BIF/DPATH/PESPEA BIF PES & PEA		DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03
?		REV: .



NORSK DATA LTD  
BENHAM VALENCE  
NEWBURY, BERKS  
UNITED KINGDOM

ID NO: 350002NN  
PRINT: 3202N

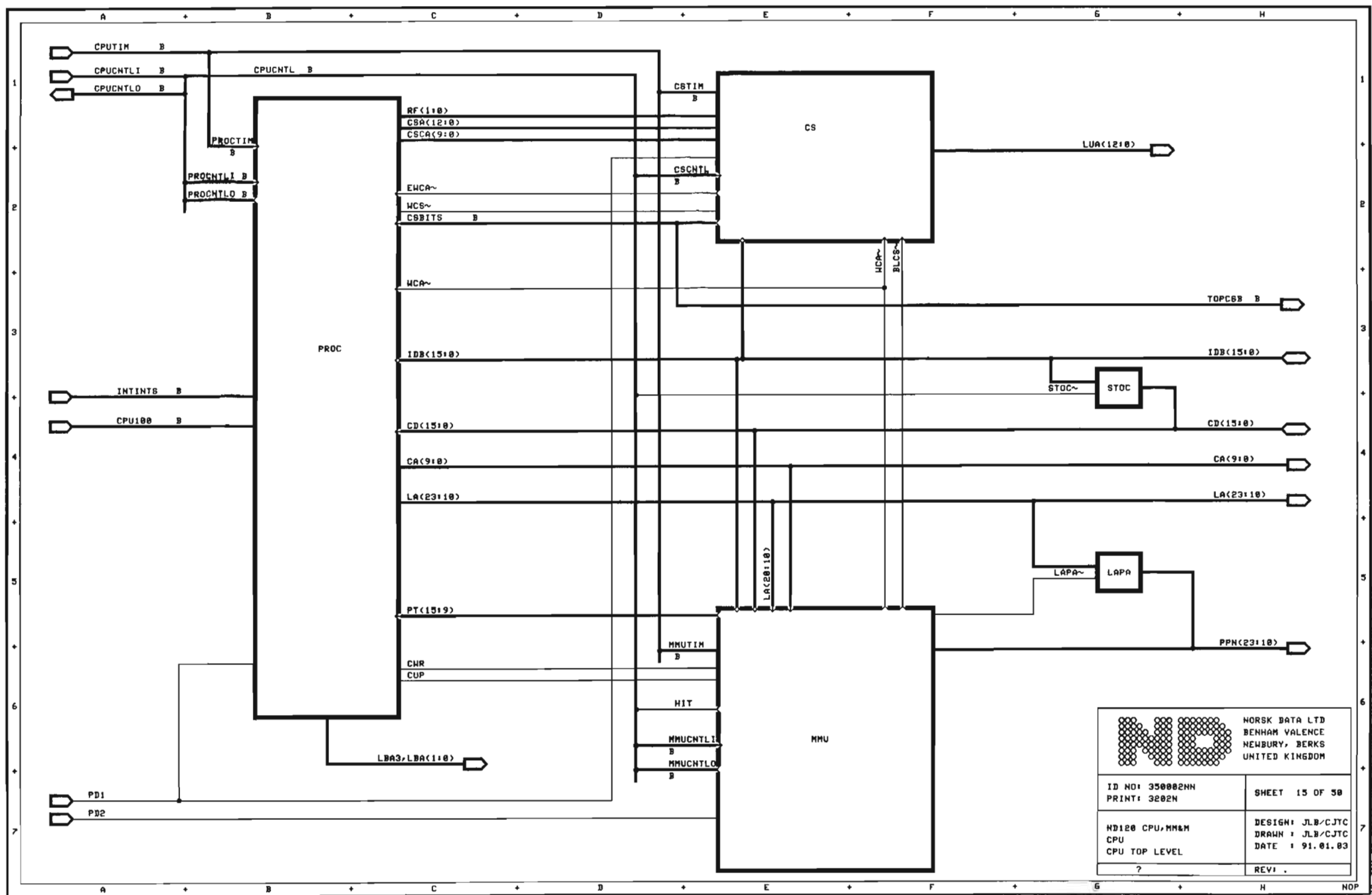
SHEET 14 OF 50

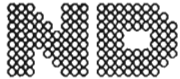
ND120 CPU, MM&M  
BIF/DPATH/PPNLBD  
BIF PPN TO LBD

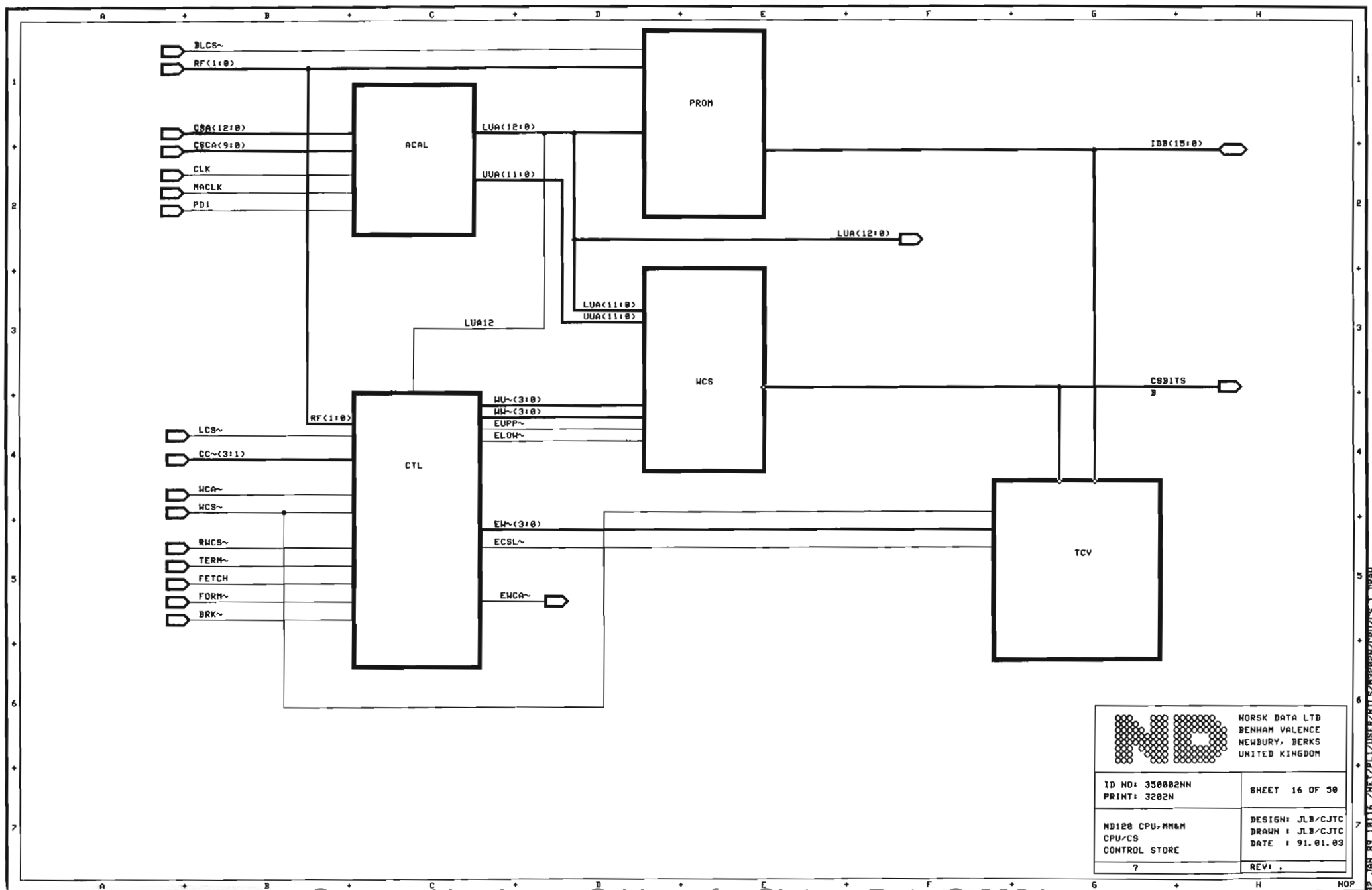
DESIGN: JLB/CJTC  
DRAWN: JLB/CJTC  
DATE: 91.01.03

?

REV: .



		NORSK DATA LTD BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 350002NN PRINT: 3202N		SHEET 15 OF 50	
ND120 CPU, MM&M CPU CPU TOP LEVEL		DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03	
?		REV: .	



HORSK DATA LTD  
BENHAM VALENCE  
NEWBURY, BERKS  
UNITED KINGDOM

ID NO: 350002NN  
PRINT: 3202N

SHEET 16 OF 30

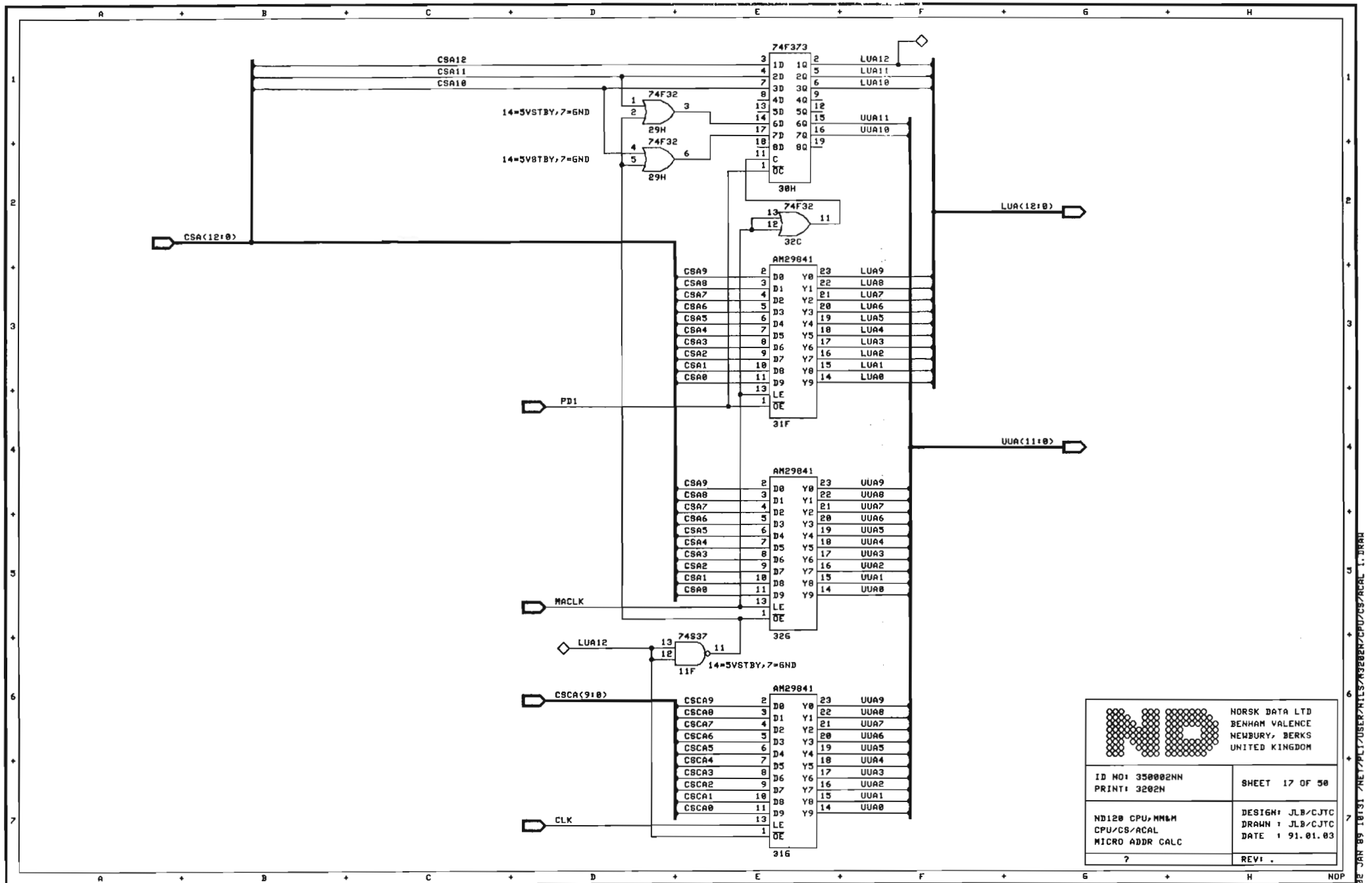
ND128 CPU/MM&M  
CPU/CS  
CONTROL STORE


DESIGN: JLB/CJTC  
DRAWN: JLB/CJTC  
DATE: 91.01.03

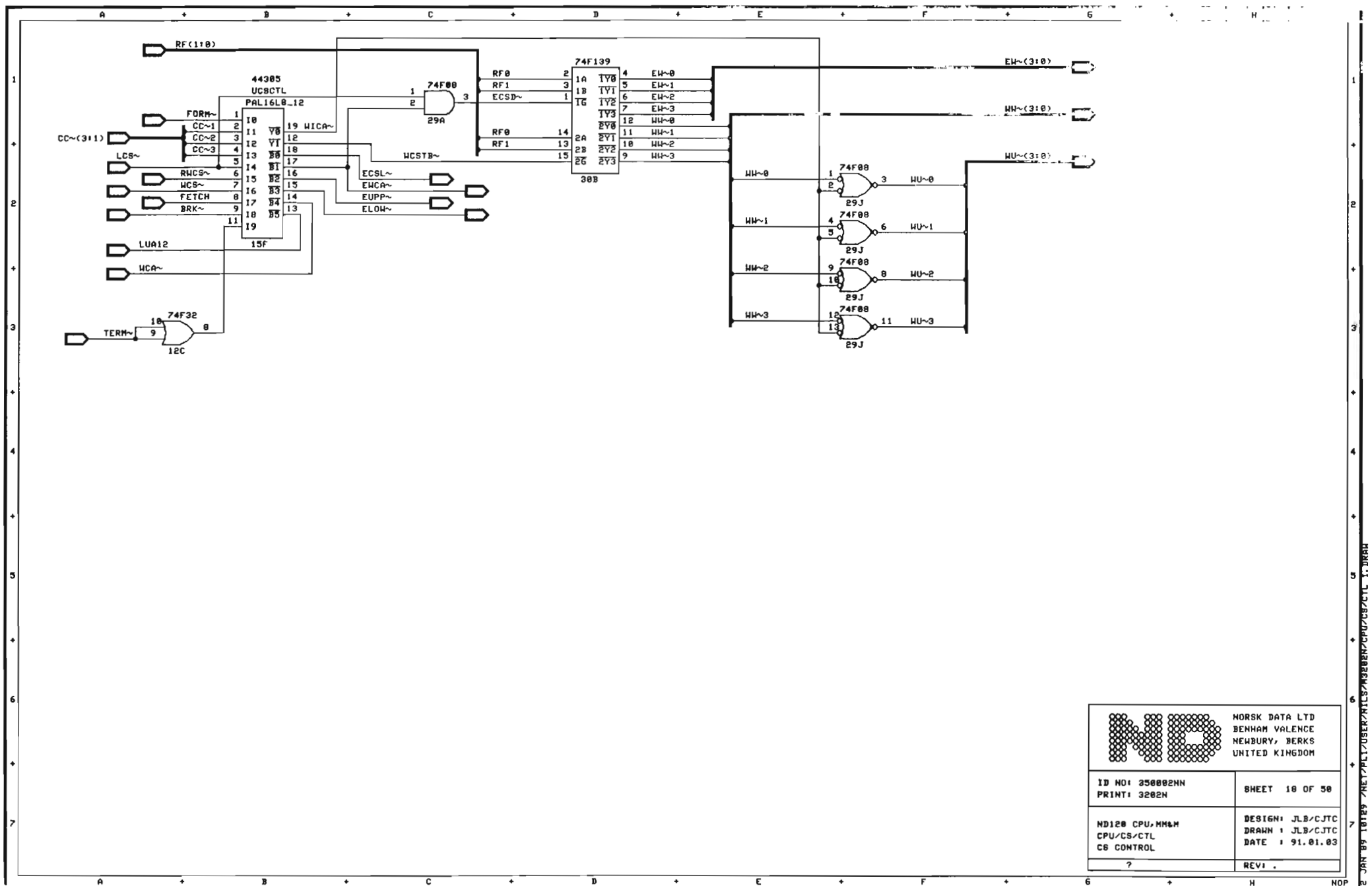
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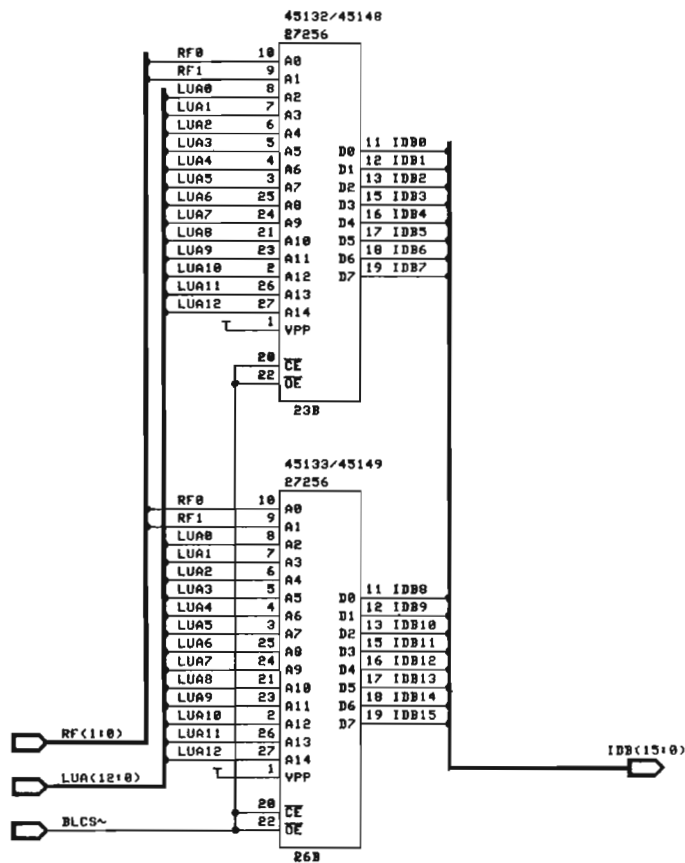
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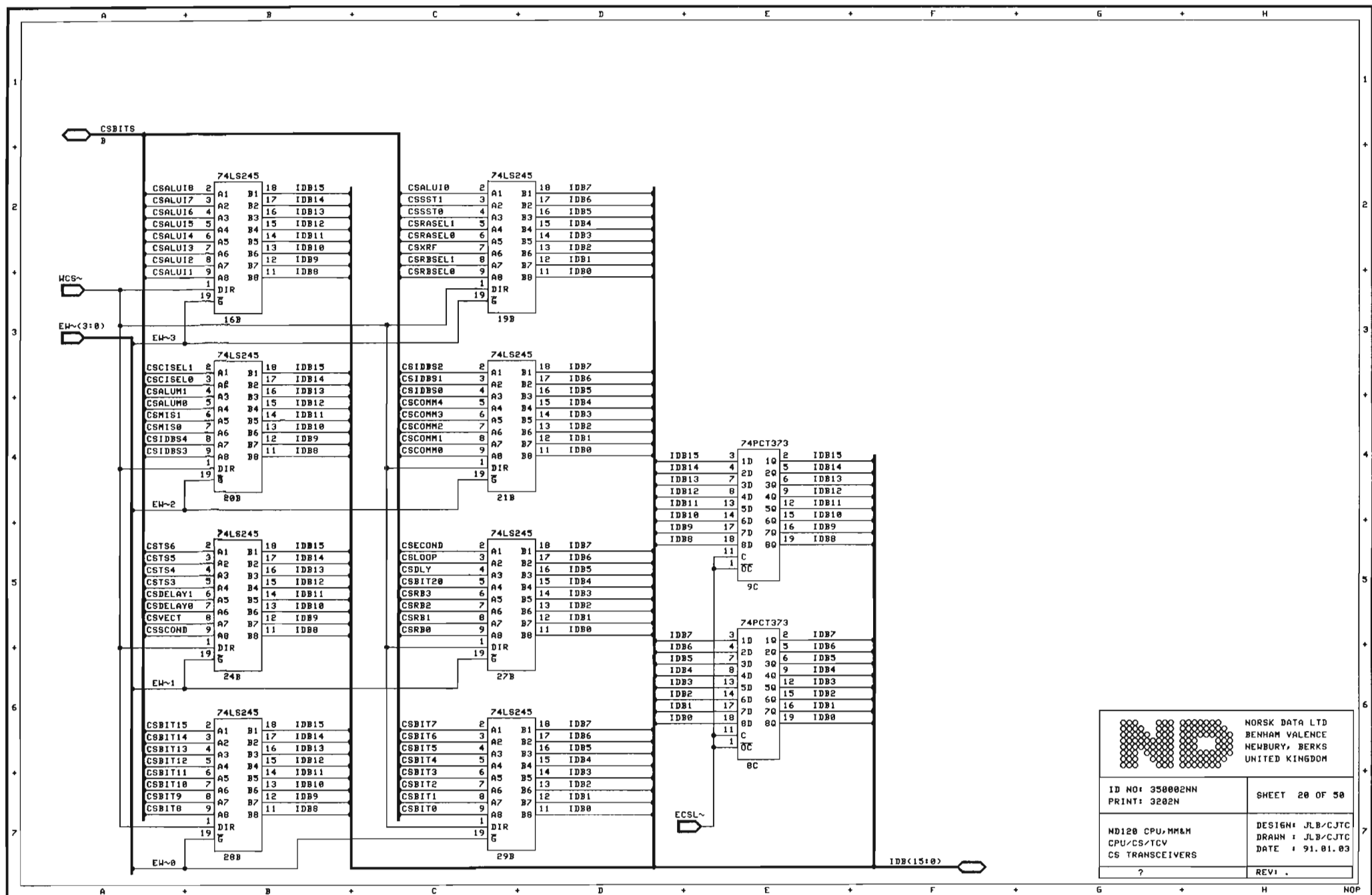



		NORSK DATA LTD BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 350002HN PRINT: 3202N		SHEET 17 OF 50	
ND120 CPU, MM&M CPU/CS/ACAL MICRO ADDR CALC		DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03	
?		REV: .	

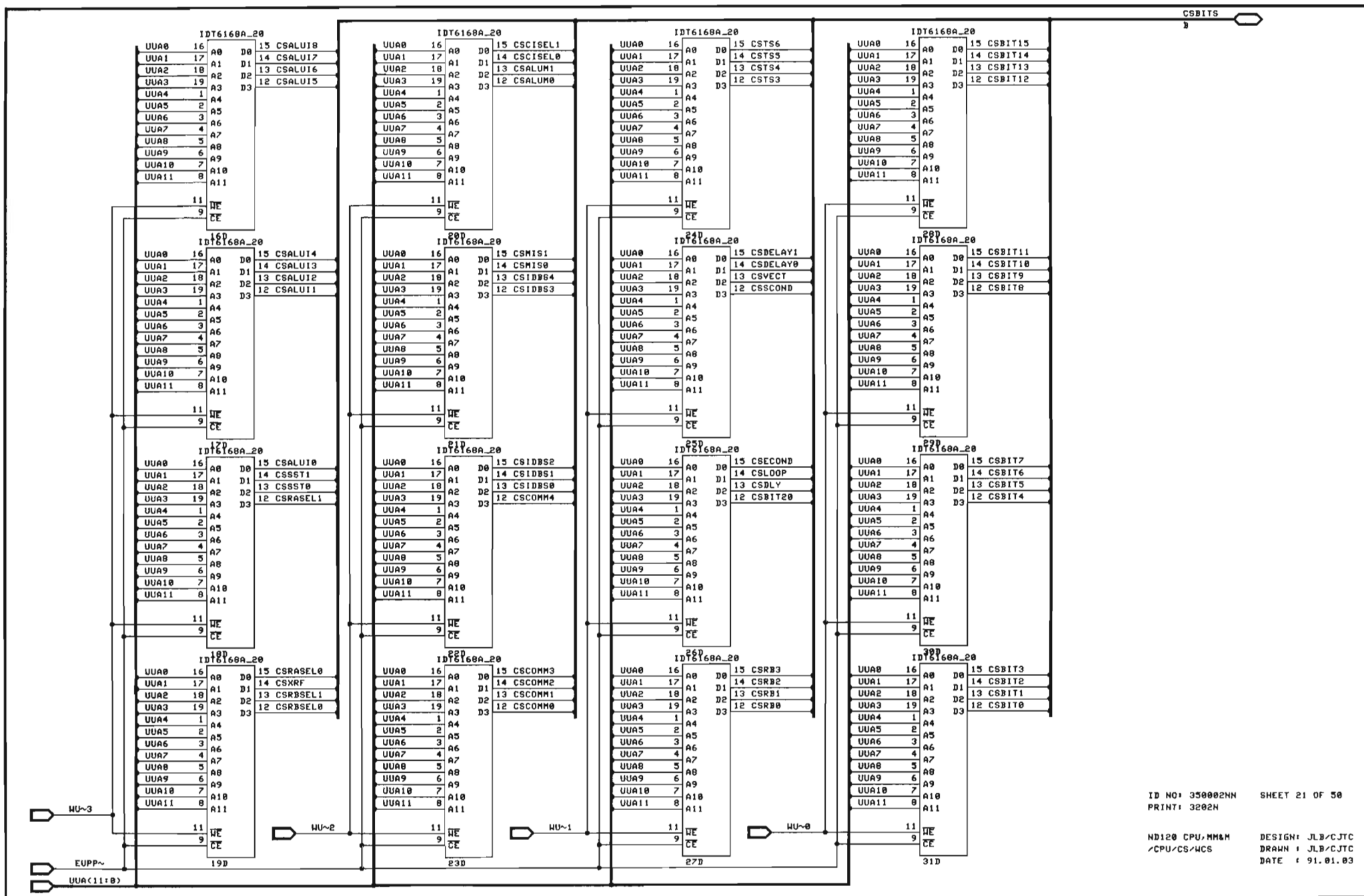




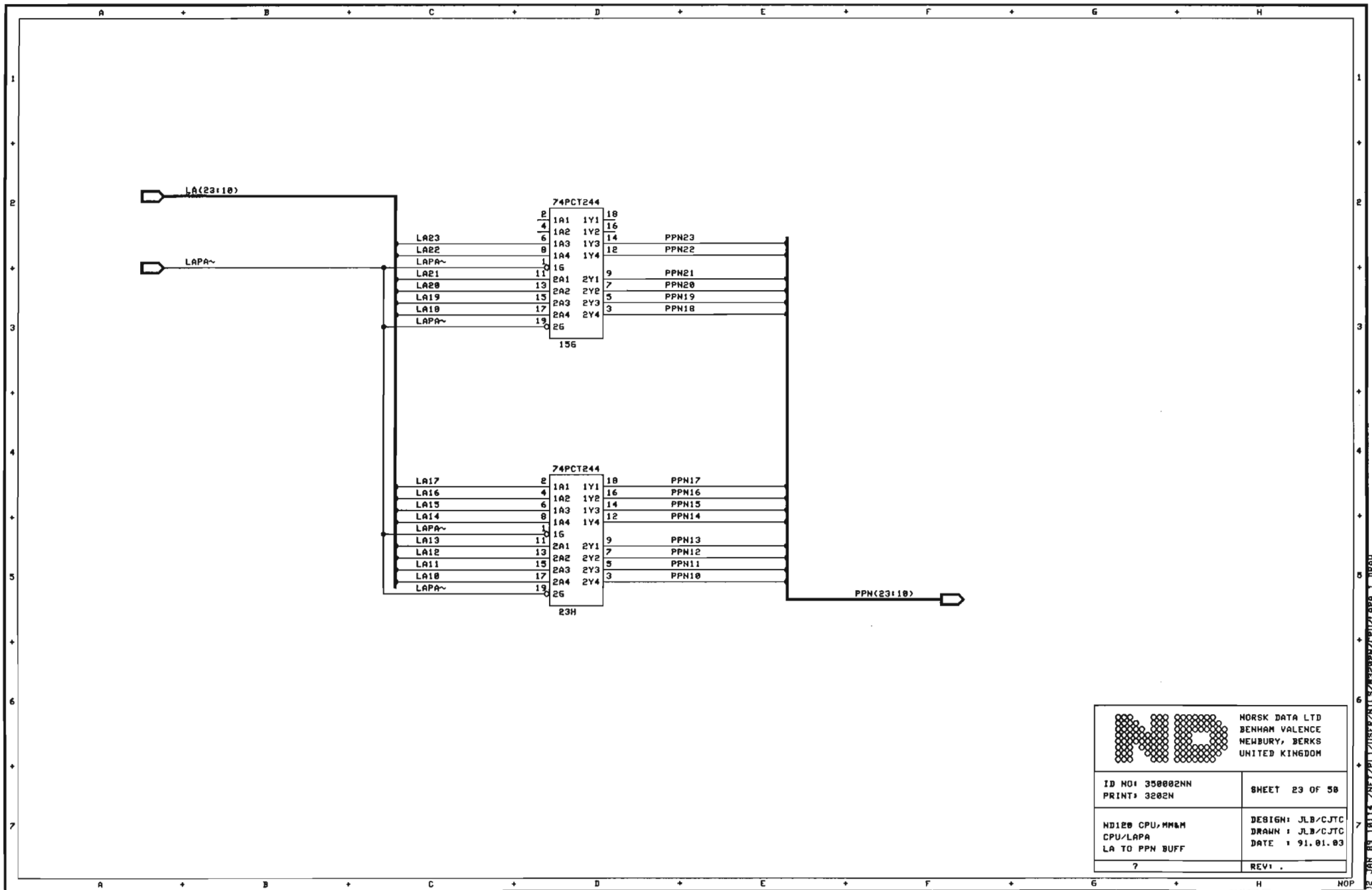
	
NORSK DATA LTD BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 350002NH PRINT: 3202H	SHEET 19 OF 50
HD128 CPU/MM&M CPU/CS/PROM CS PROMS	DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03
?	REV: .



		NORSK DATA LTD BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 35002NN PRINT: 3202N		SHEET 28 OF 50	
MD120 CPU/MM&M CPU/CS/TCV CS TRANSCEIVERS		DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03	
?		REV: .	







NORSK DATA LTD  
BENHAM VALENCE  
NEWBURY, BERKS  
UNITED KINGDOM

ID NO: 35002NN  
PRINT: 3202N

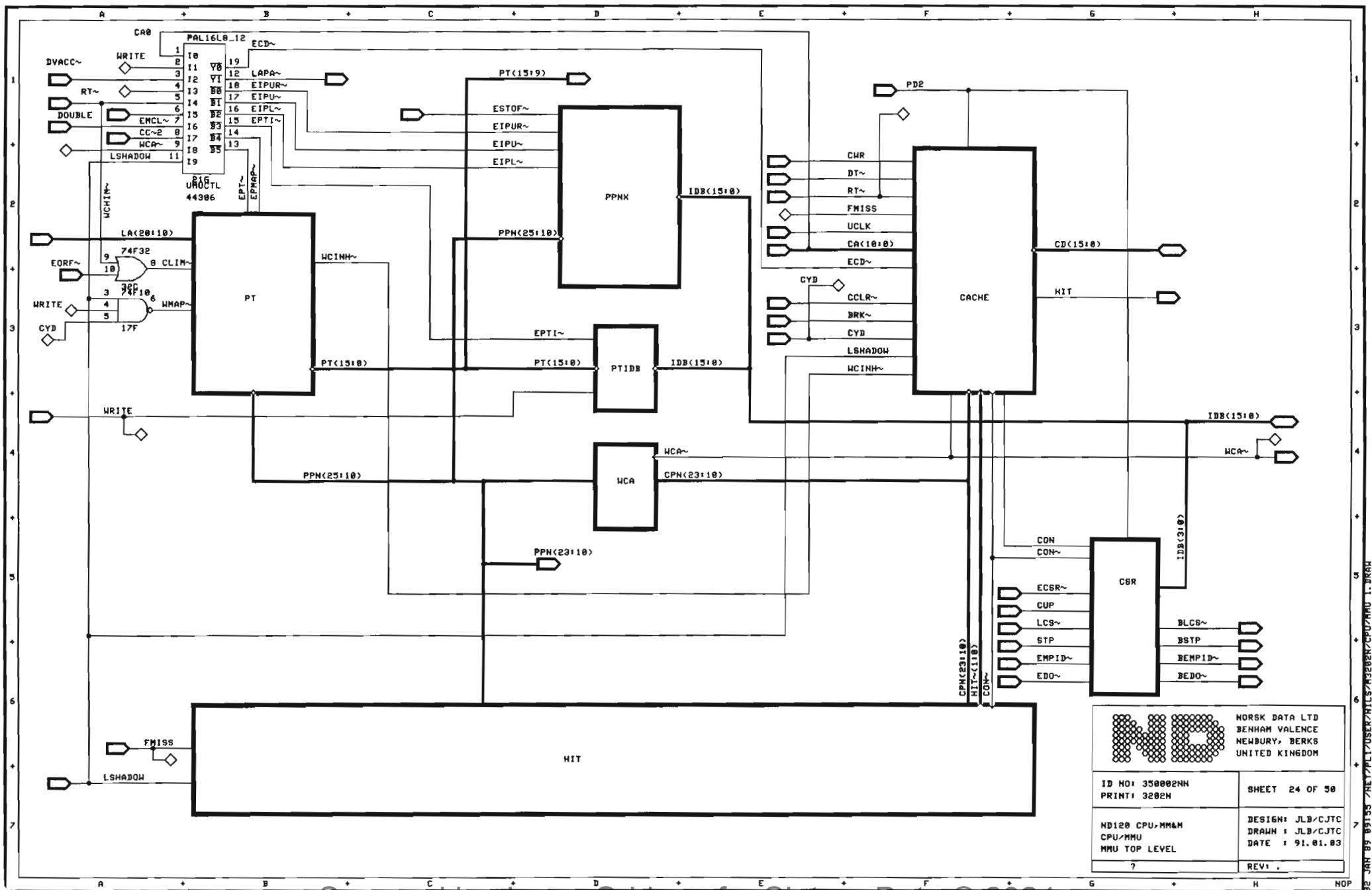
SHEET 23 OF 50

ND120 CPU, MM&M  
CPU/LAPA  
LA TO PPN BUFF

DESIGN: JLB/CJTC  
DRAWN: JLB/CJTC  
DATE: 91.01.03

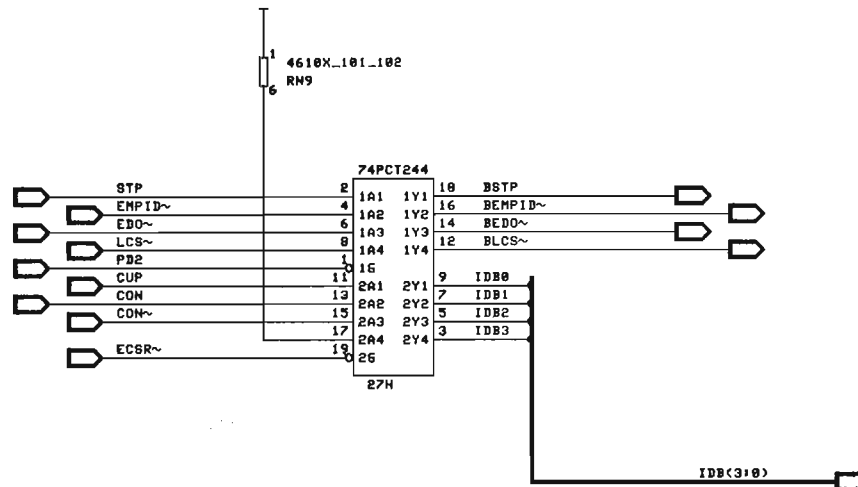
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REV: .









NORSK DATA LTD  
BENHAM VALENCE  
NEWBURY, BERKS  
UNITED KINGDOM

ID NO: 350002NN  
PRINT: 3202N

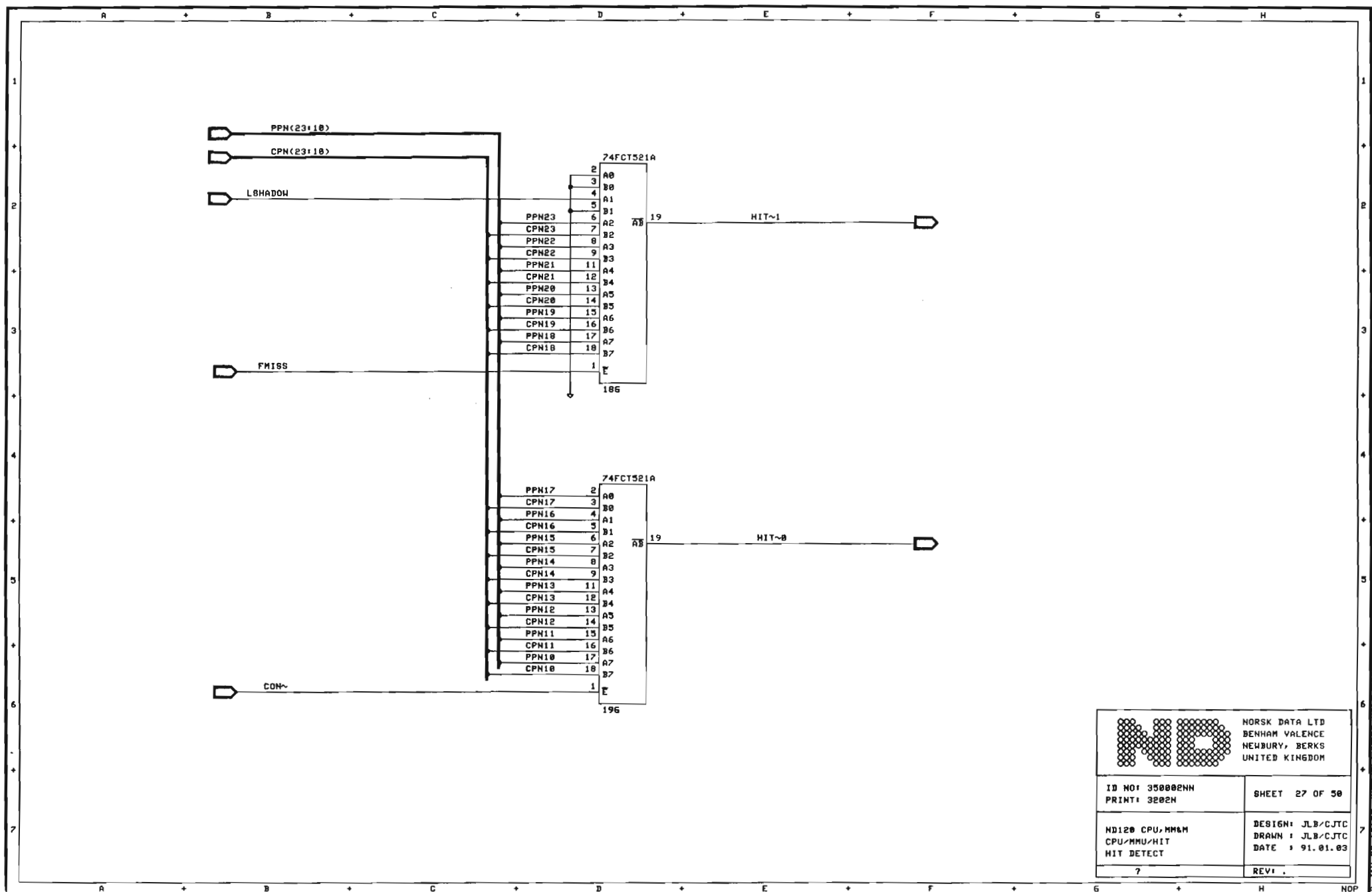
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ND120 CPU, MM&M  
CPU/MMU/CSR  
CACHE STATUS REG

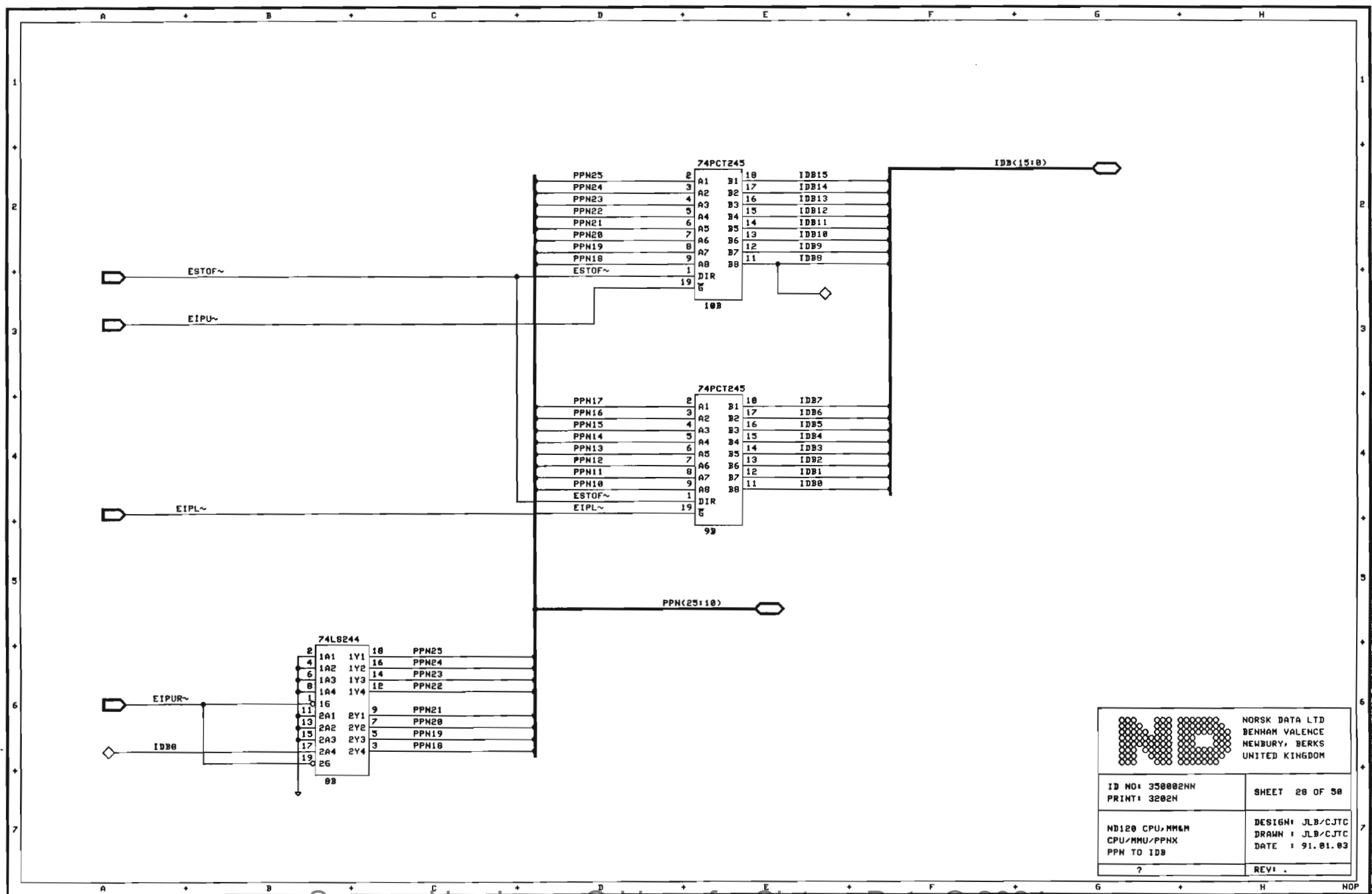
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DATE: 91.01.03


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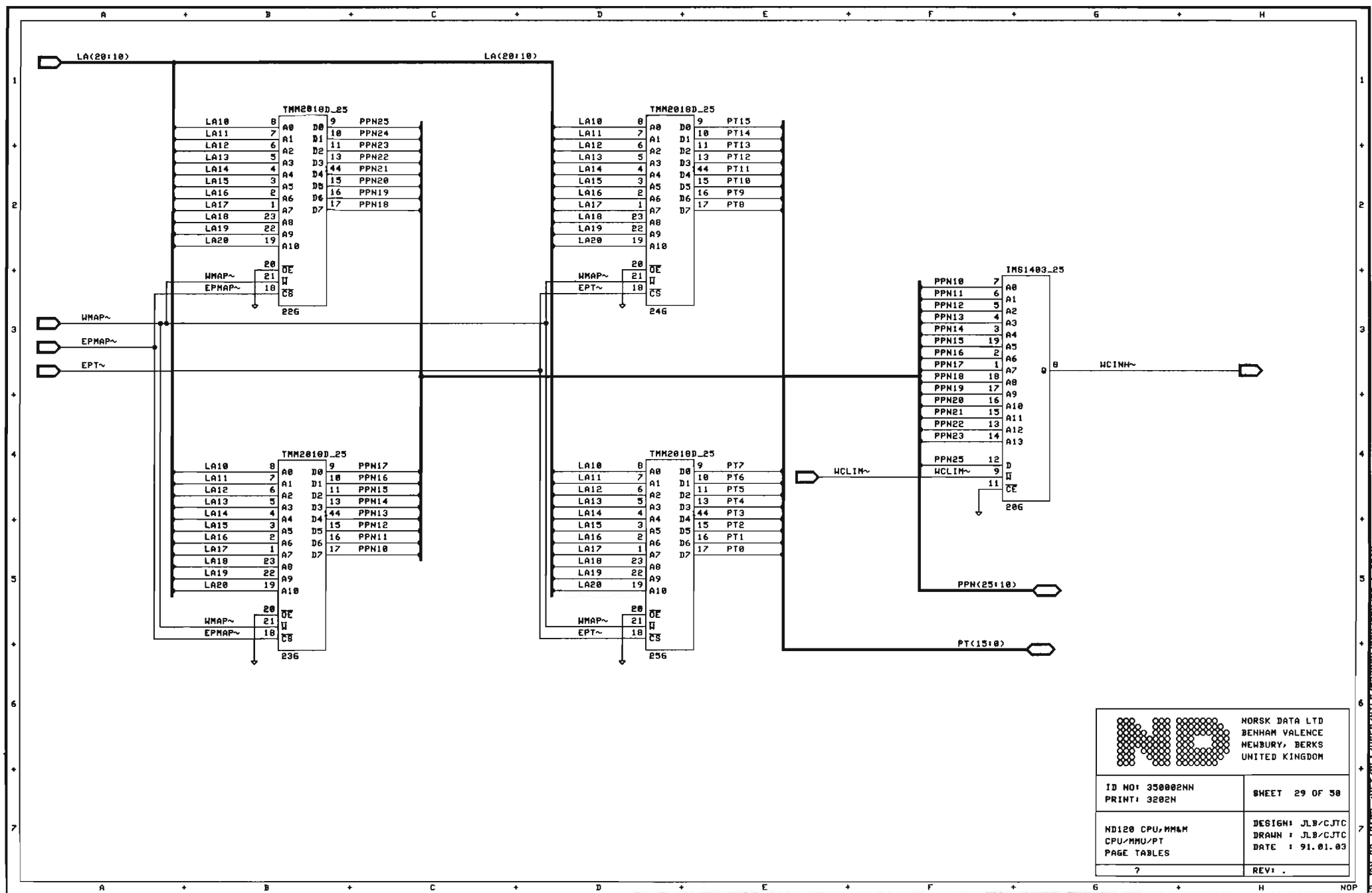
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 HORSK DATA LTD BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 350002NN PRINT: 3202N	SHEET 27 OF 50
ND120 CPU, MM&M CPU/MMU/HIT HIT DETECT	DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03
7	REV: .



 <b>NORSK DATA LTD</b> BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 35002NN PRINT: 3202H	SHEET 28 OF 38
ND120 CPU/MM&M CPU/MMU/PPHX PPN TO IDB	DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03
?	REV: .



NORSK DATA LTD  
BENHAM VALENCE  
NEWBURY, BERKS  
UNITED KINGDOM

ID NO: 35002NN  
PRINT: 3202N

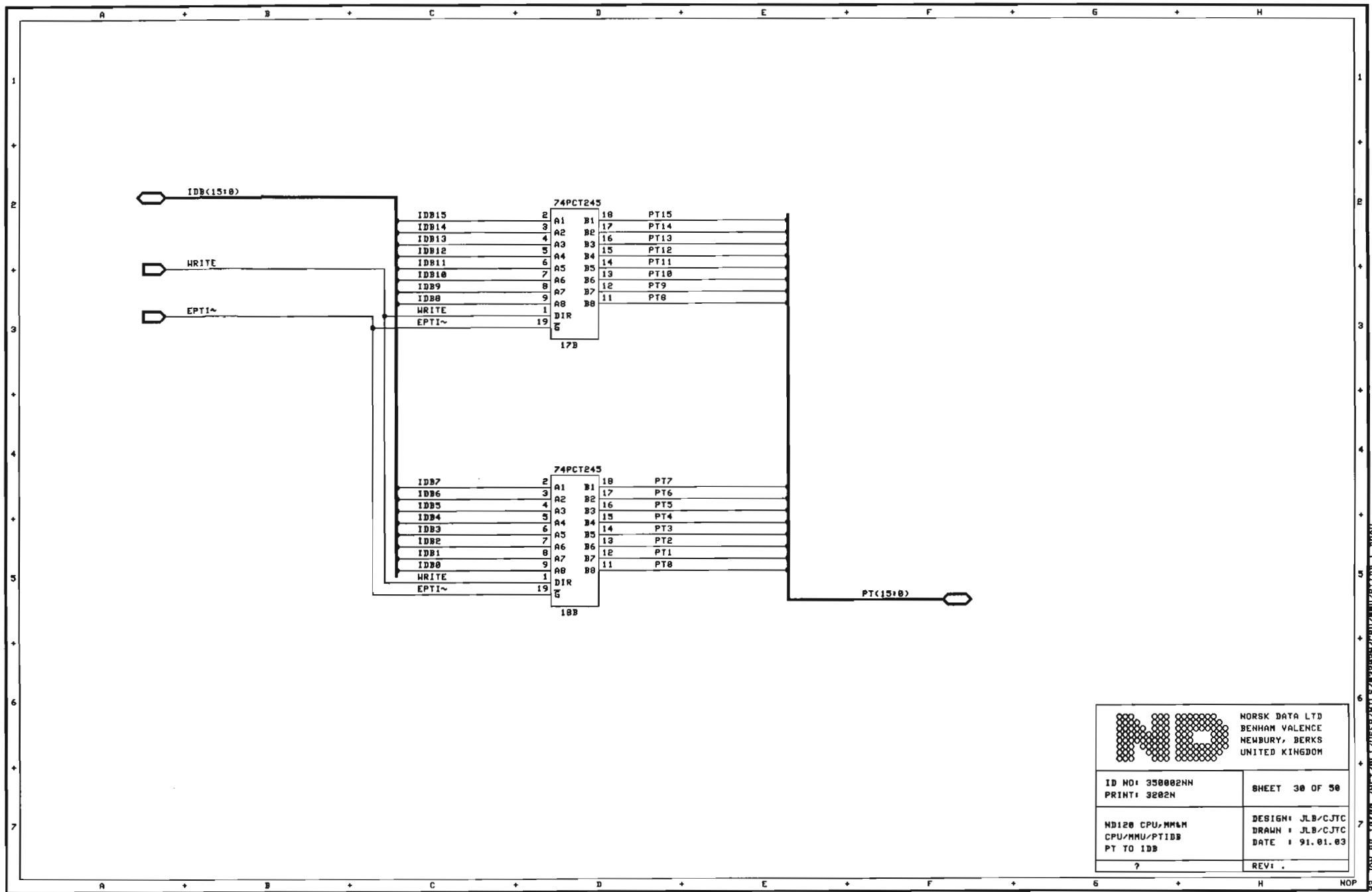
SHEET 29 OF 50

ND120 CPU/MM&M  
CPU/MMU/PT  
PAGE TABLES

DESIGN: JLB/CJTC  
DRAWN: JLB/CJTC  
DATE: 91.01.03

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REV: .



NORSK DATA LTD  
BENHAM VALENCE  
NEWBURY, BERKS  
UNITED KINGDOM

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PRINT: 3202N

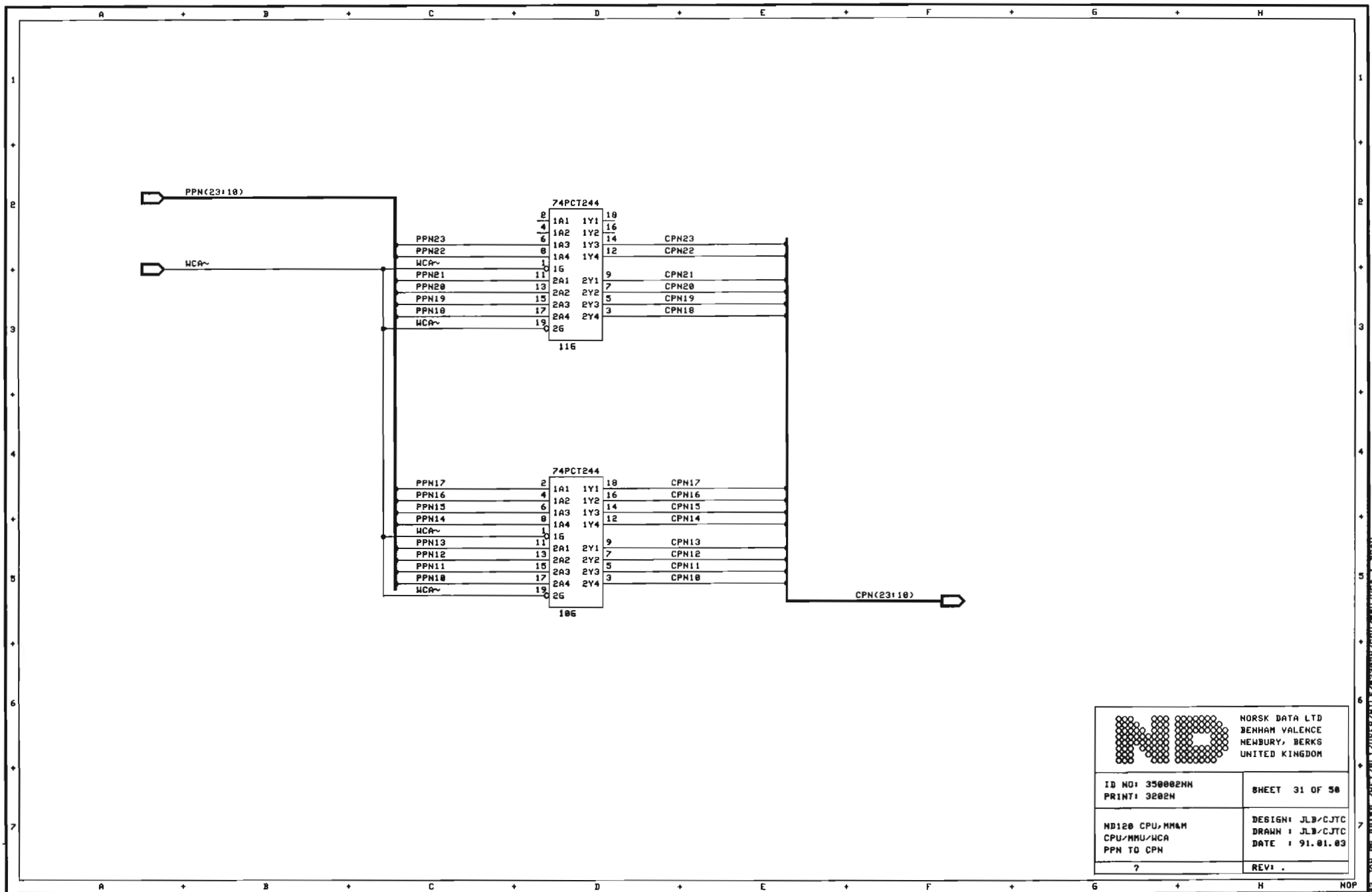
SHEET 30 OF 50


ND120 CPU,MM&M  
CPU/MMU/PTIDB  
PT TO IDB

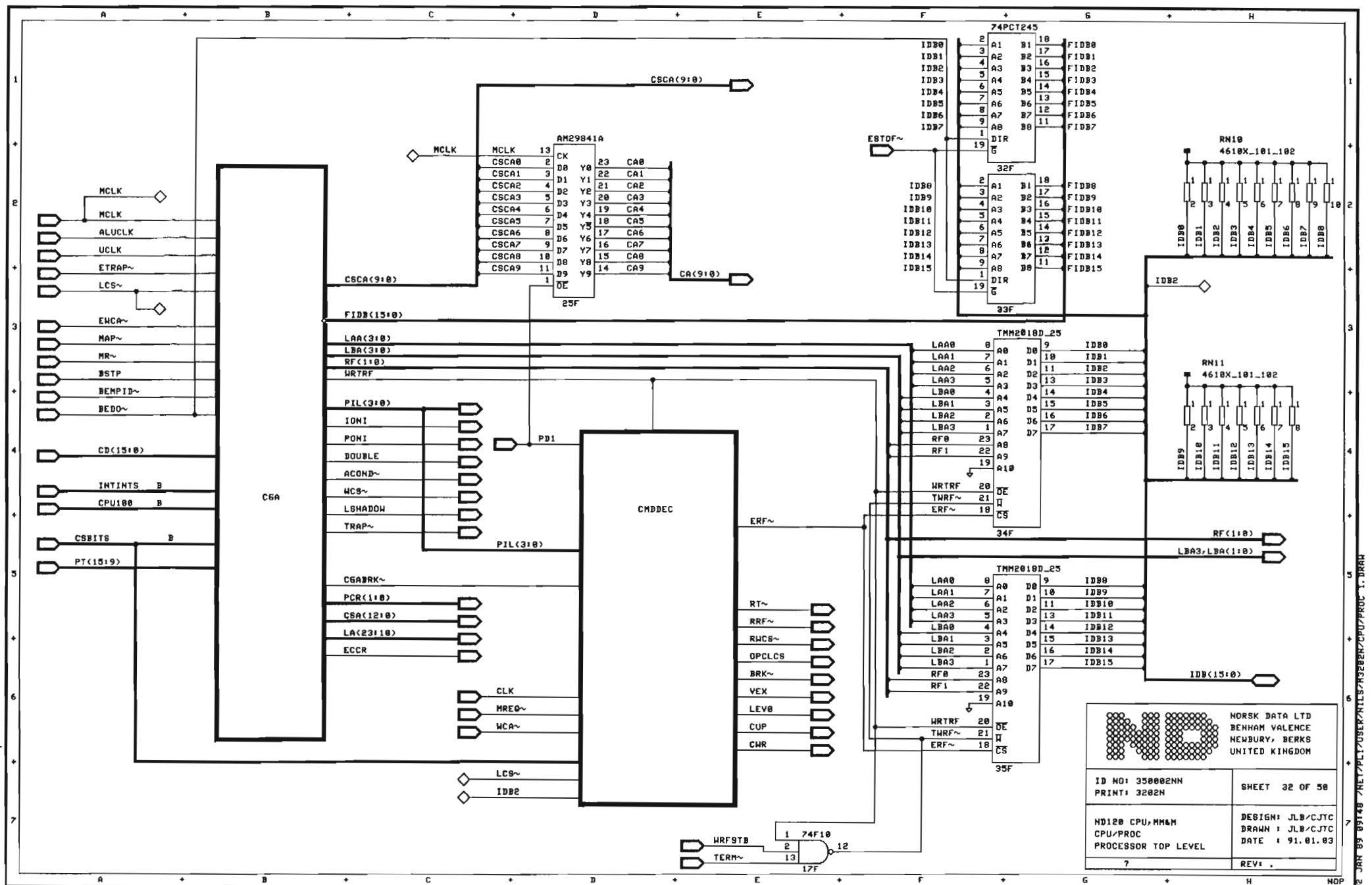
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DATE: 91.01.03

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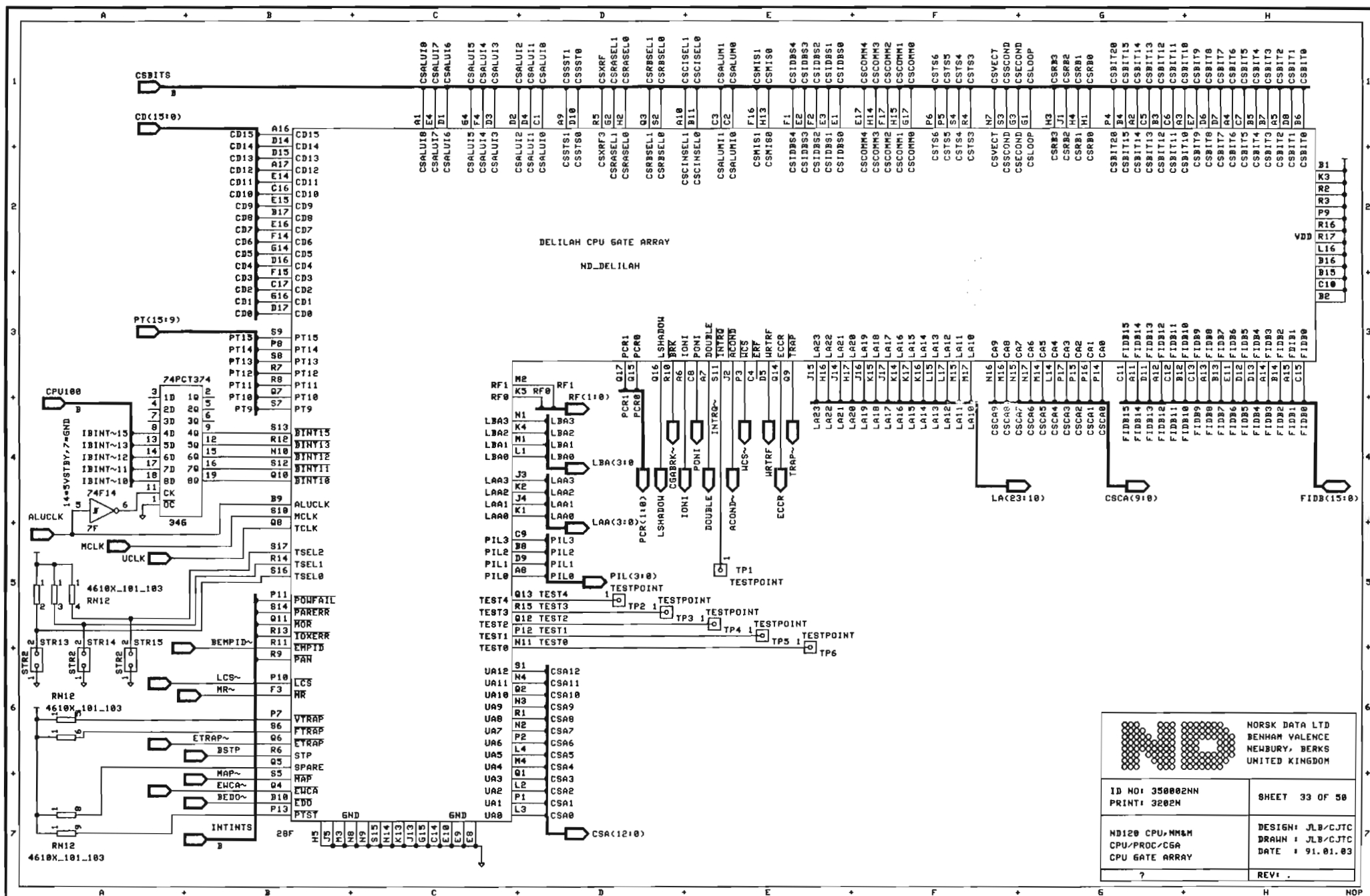
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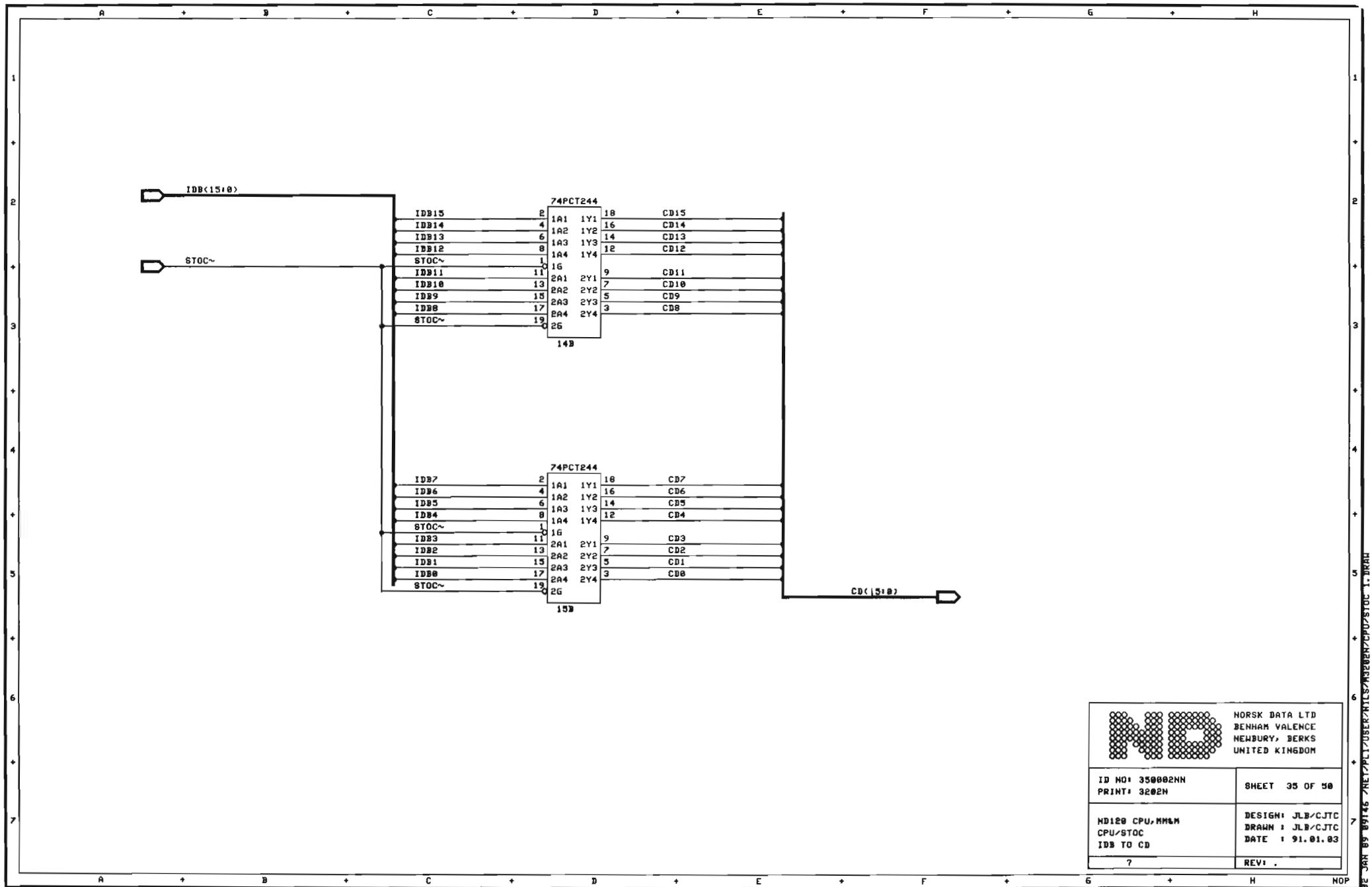
 NORSK DATA LTD BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 350002NN PRINT: 3202N	SHEET 31 OF 50
ND120 CPU/MM&H CPU/MMU/WCA PPN TO CPN	DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03
?	REV: .






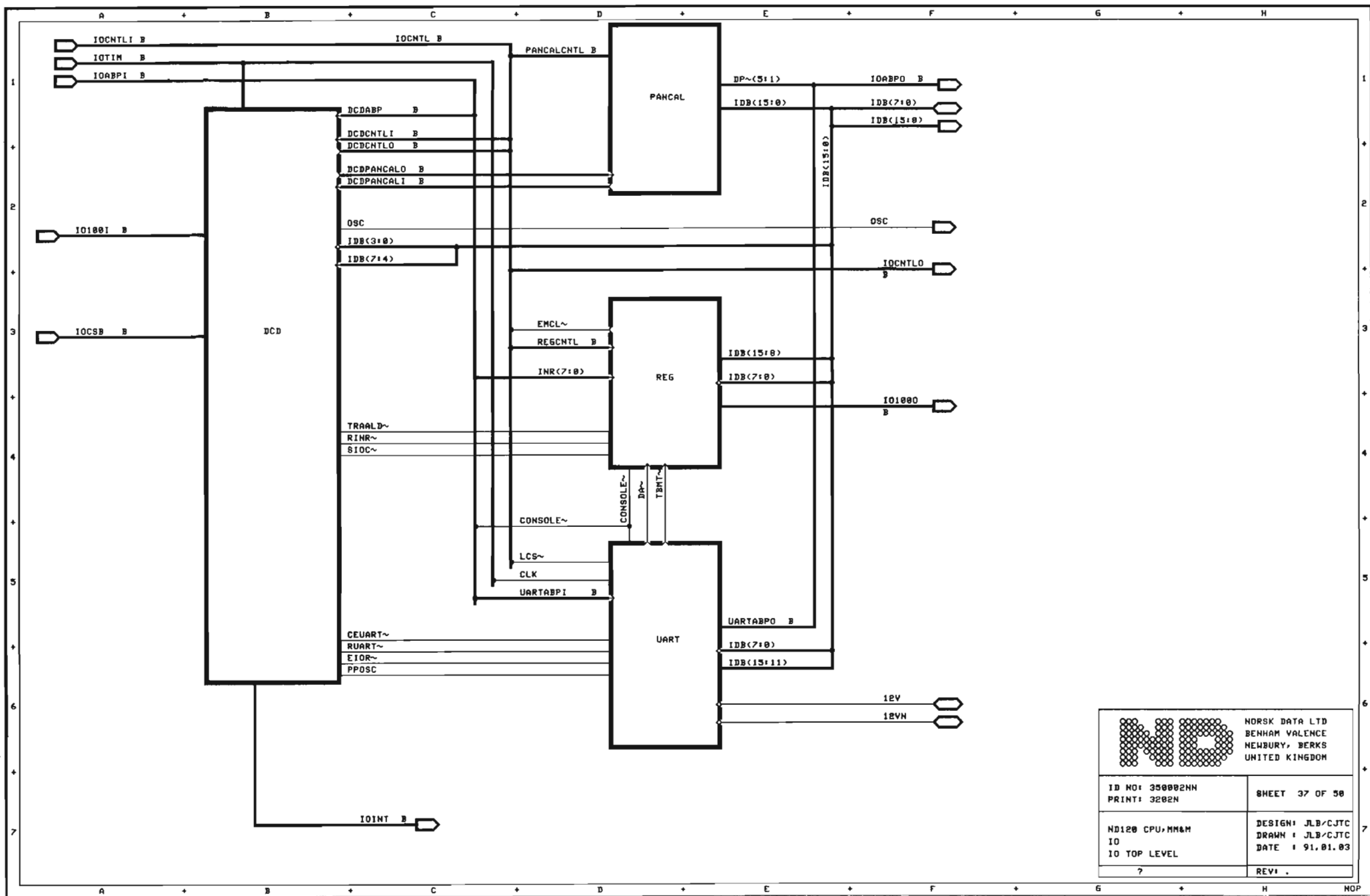


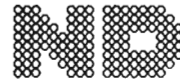


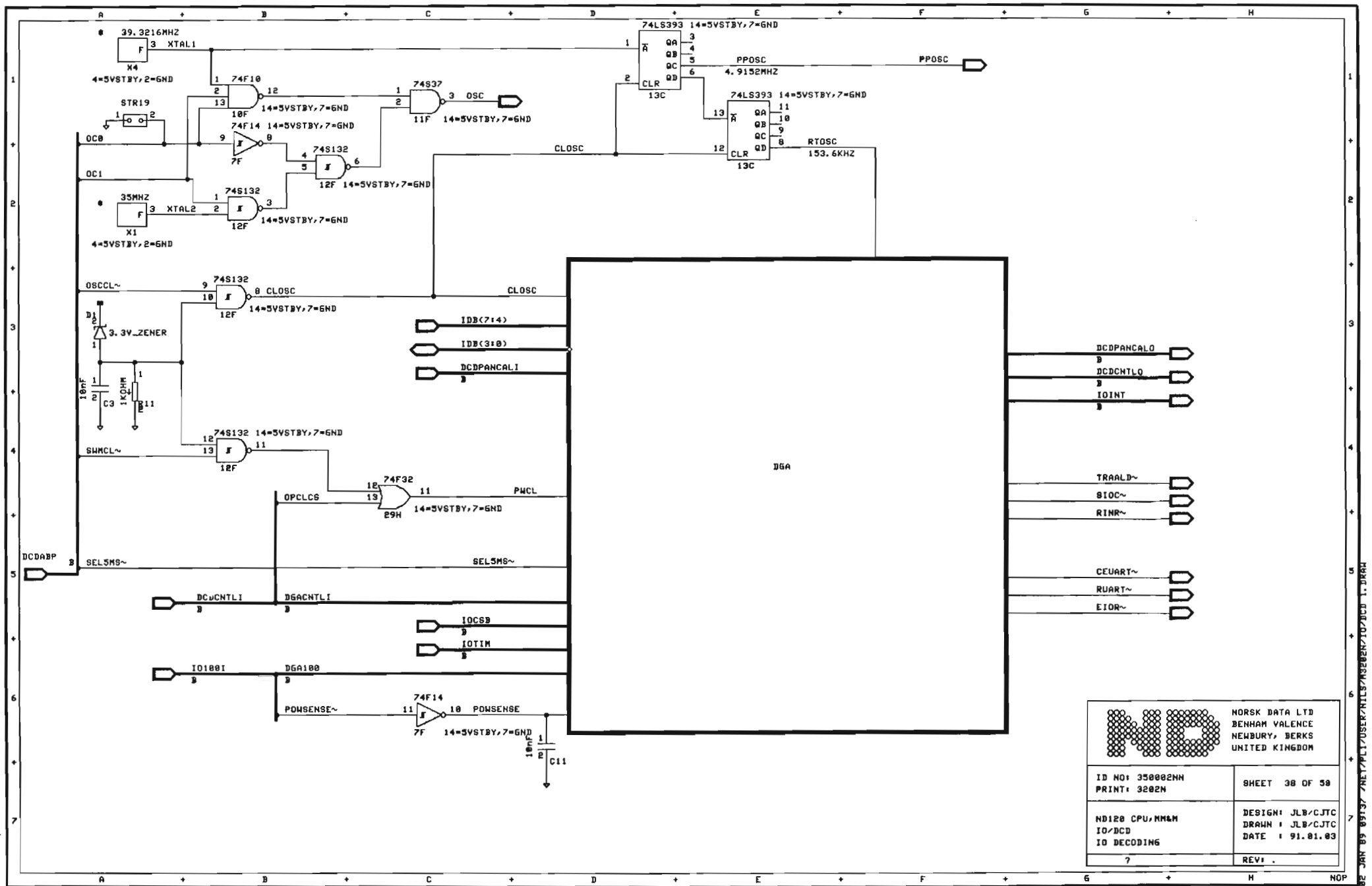



 <b>NORSK DATA LTD</b> BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 350002NN PRINT: 3202N	SHEET 35 OF 50
ND120 CPU, MM&M CPU/STOC IDB TO CD	DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03
?	REV: .

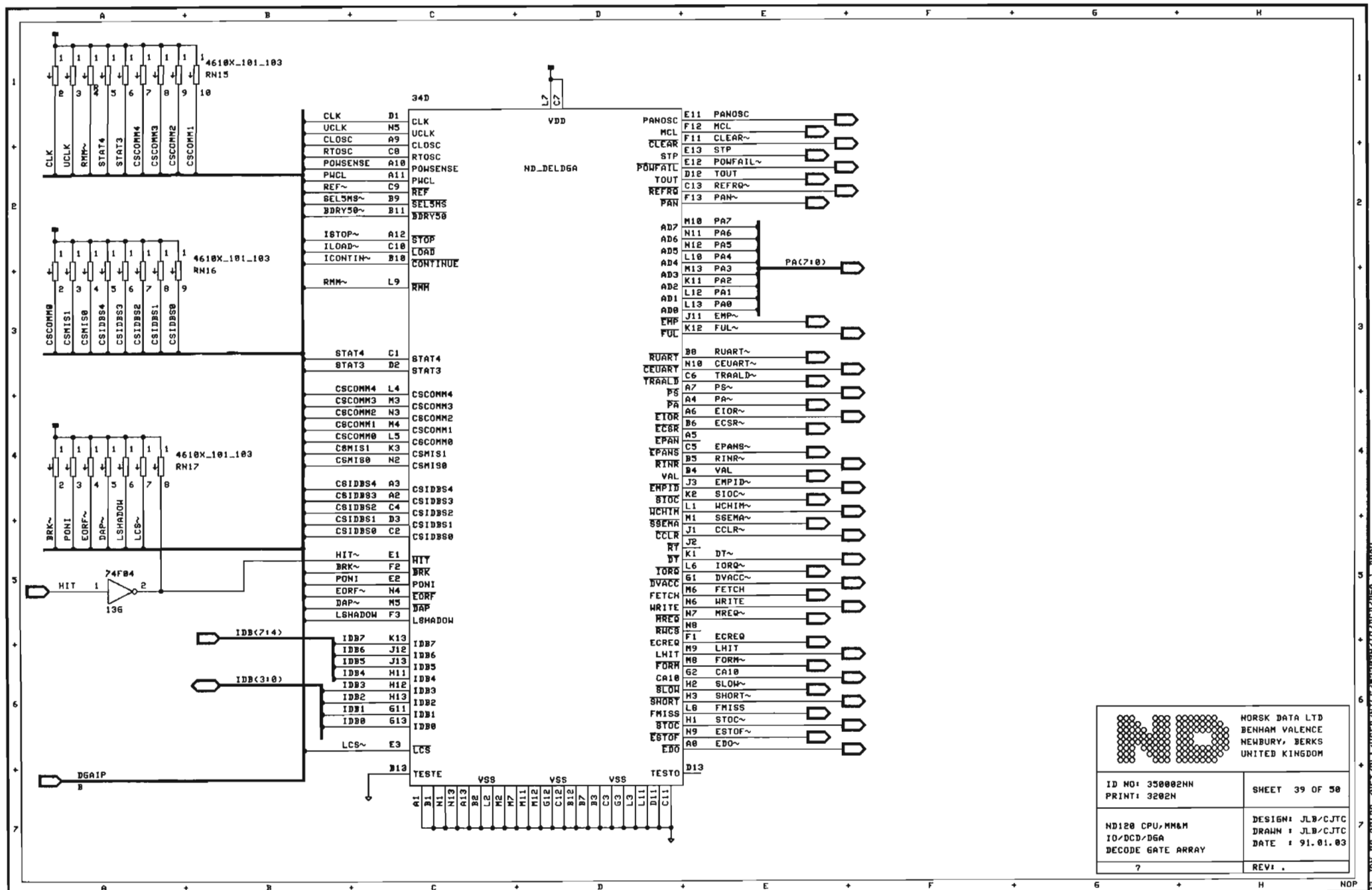


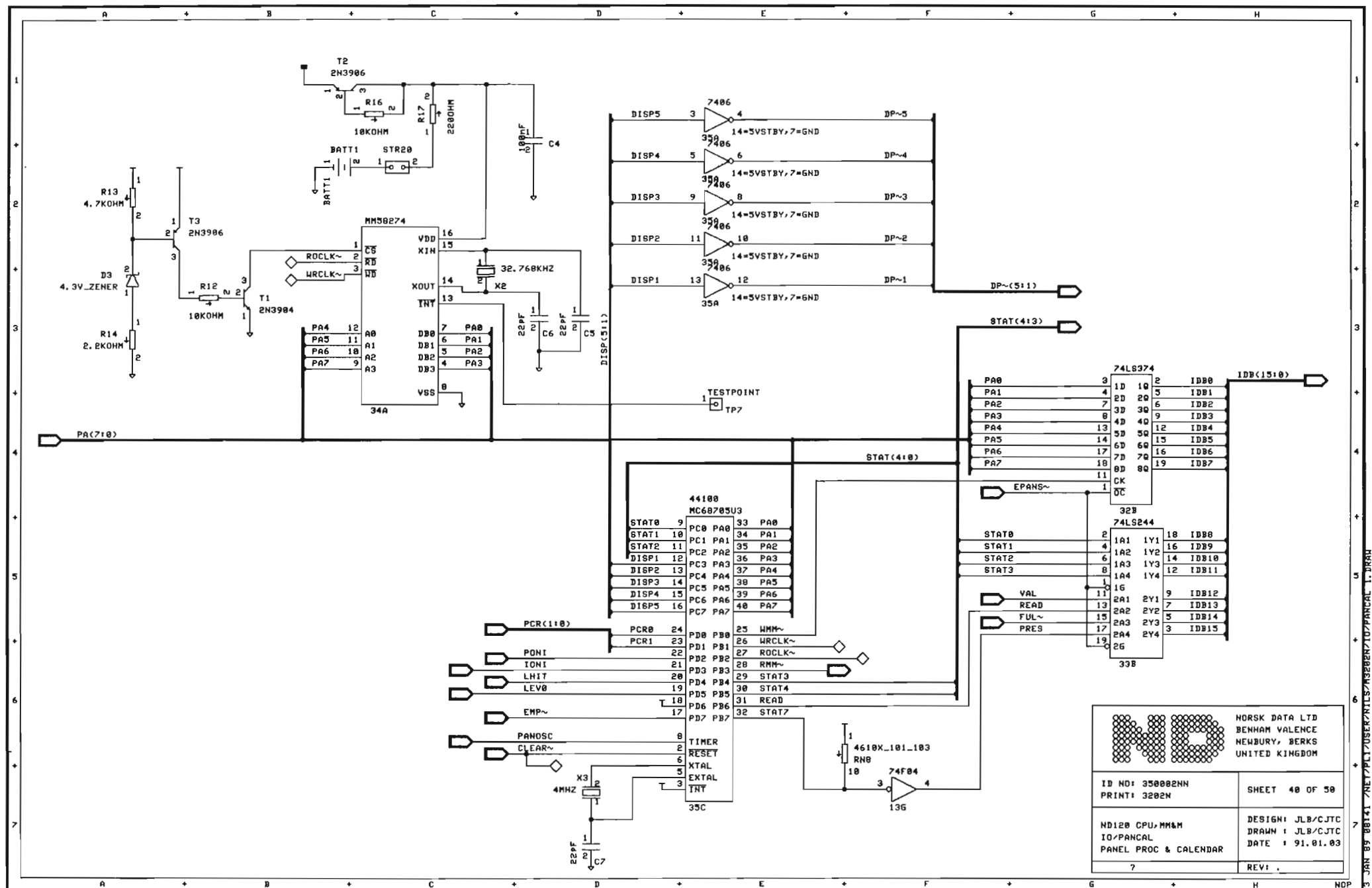


 NORSK DATA LTD BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 35002NH PRINT: 3202N	SHEET 37 OF 50
ND120 CPU, MM&M IO IO TOP LEVEL	DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 19.01.83
?	REV: .



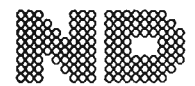
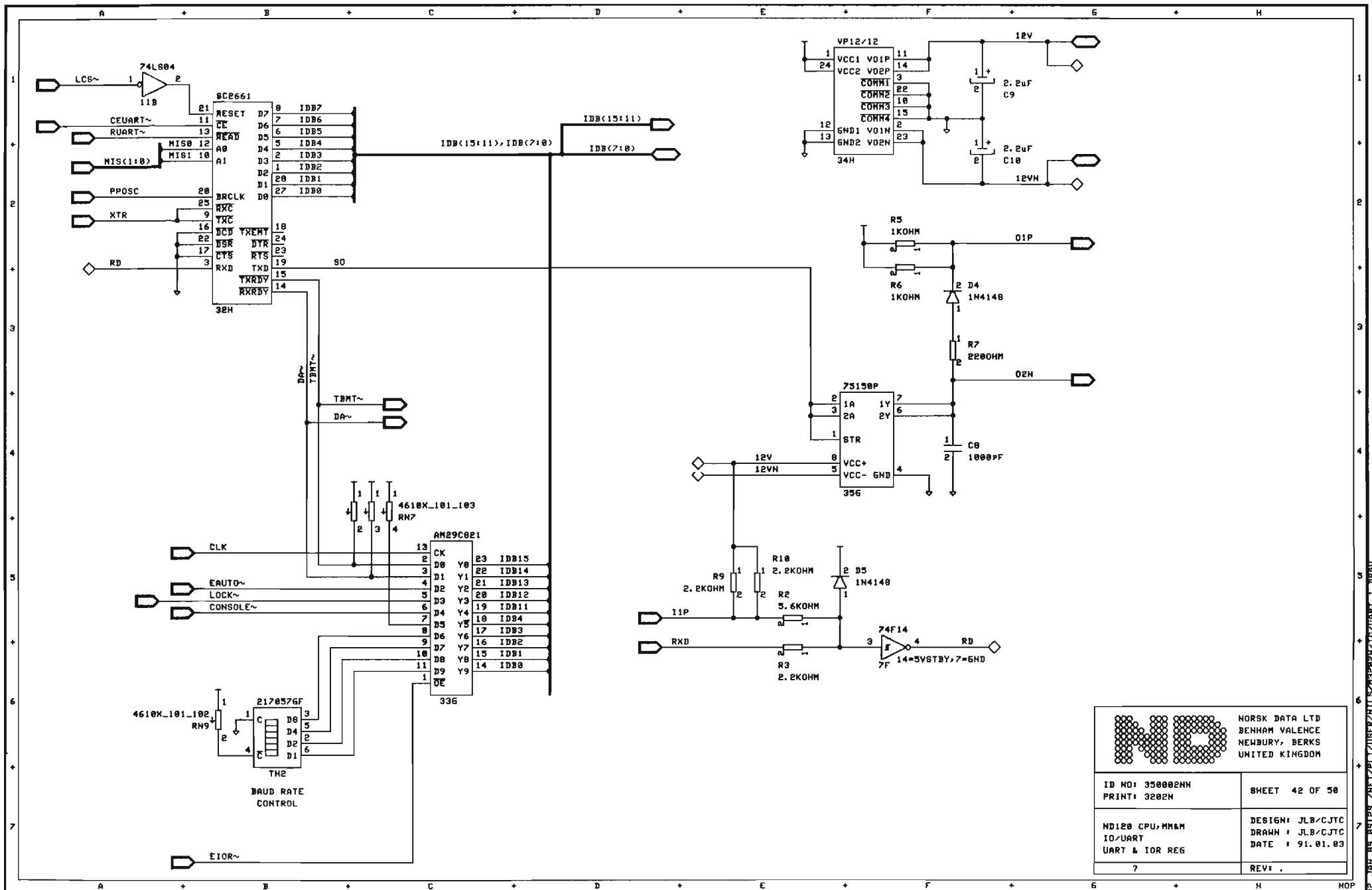
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ID NO: 35002NN PRINT: 3202H		SHEET 38 OF 58	
ND120 CPU/MM&M IO/DCD IO DECODING		DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03	
?		REV: .	





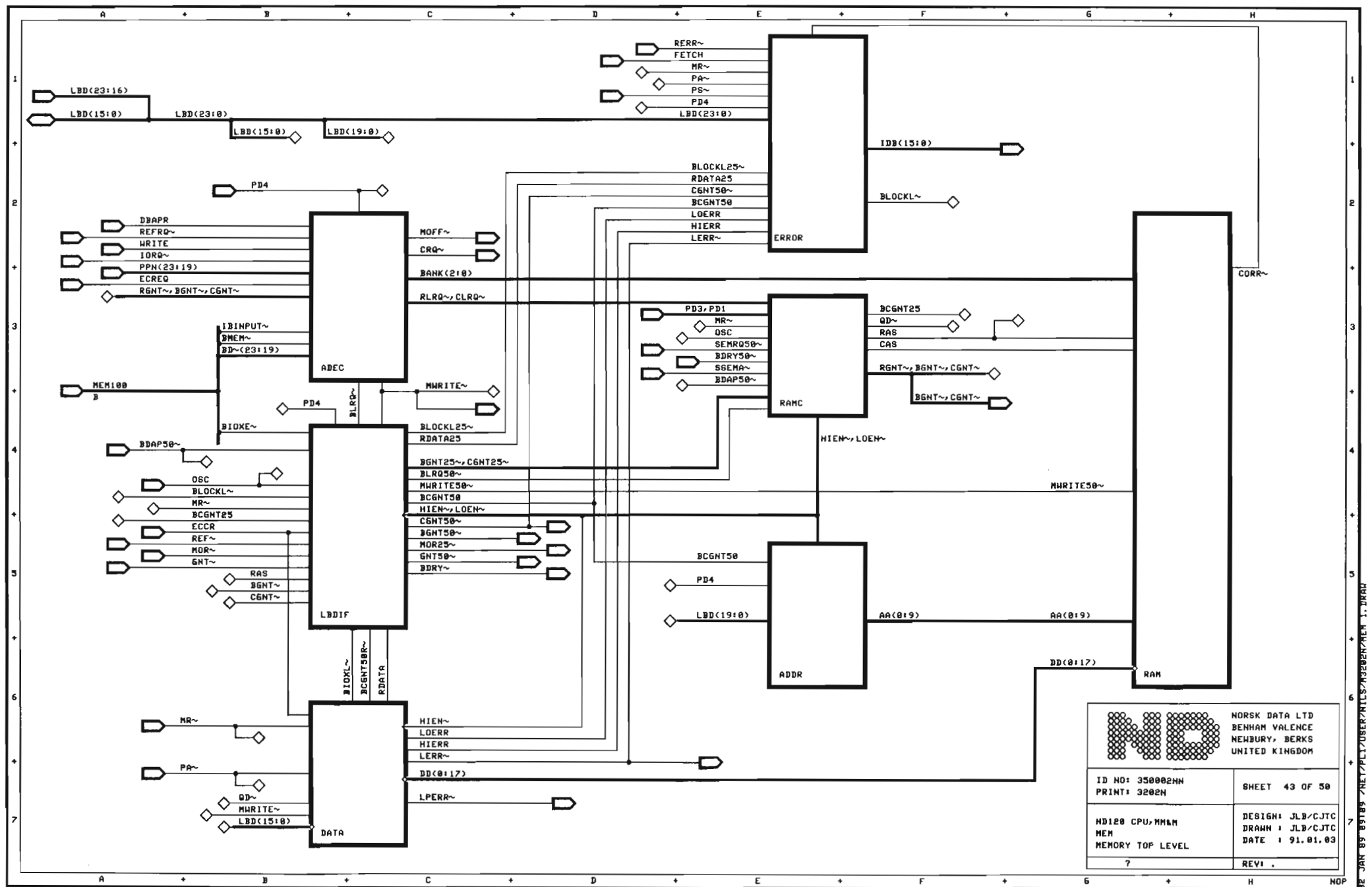


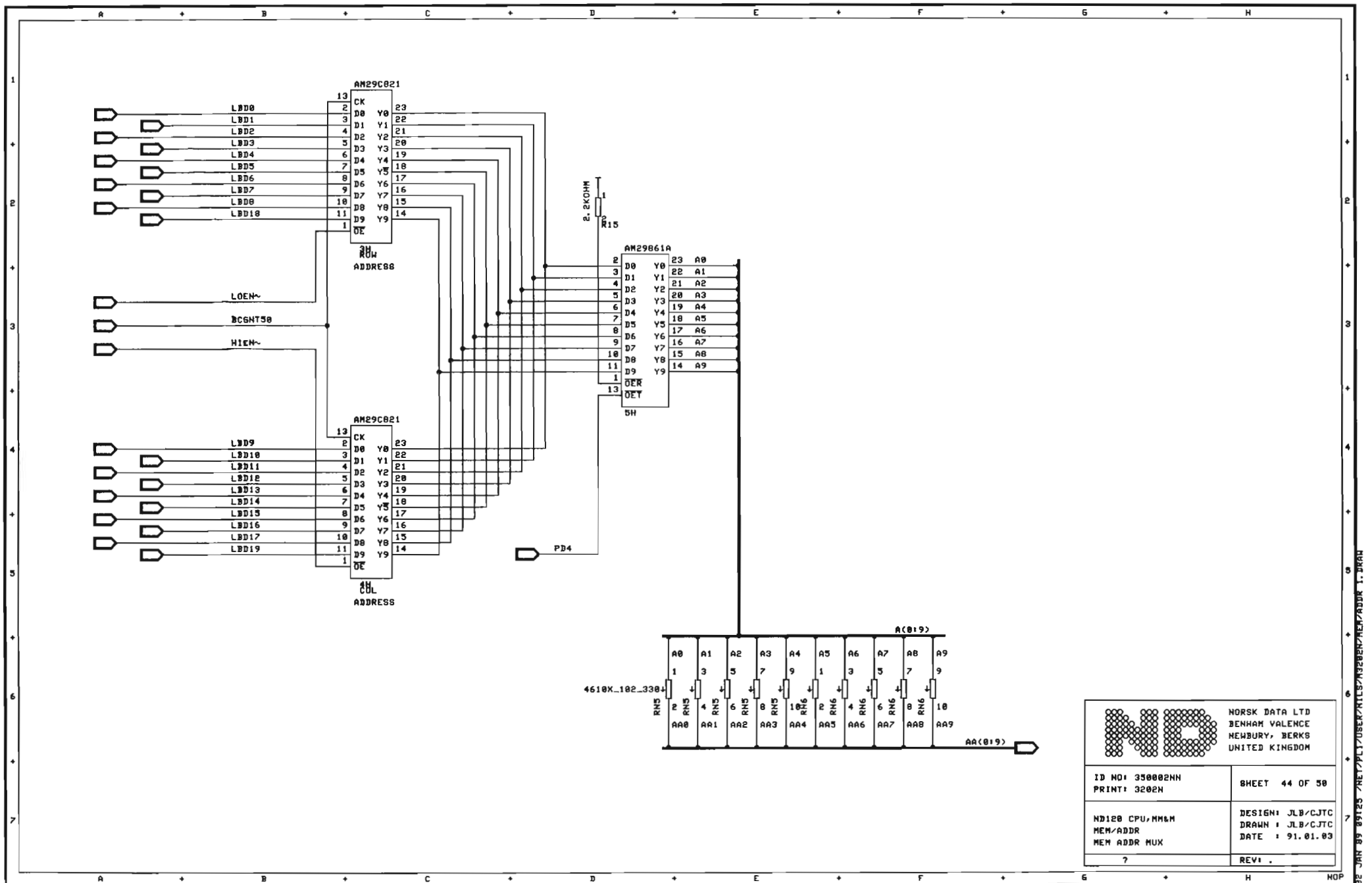




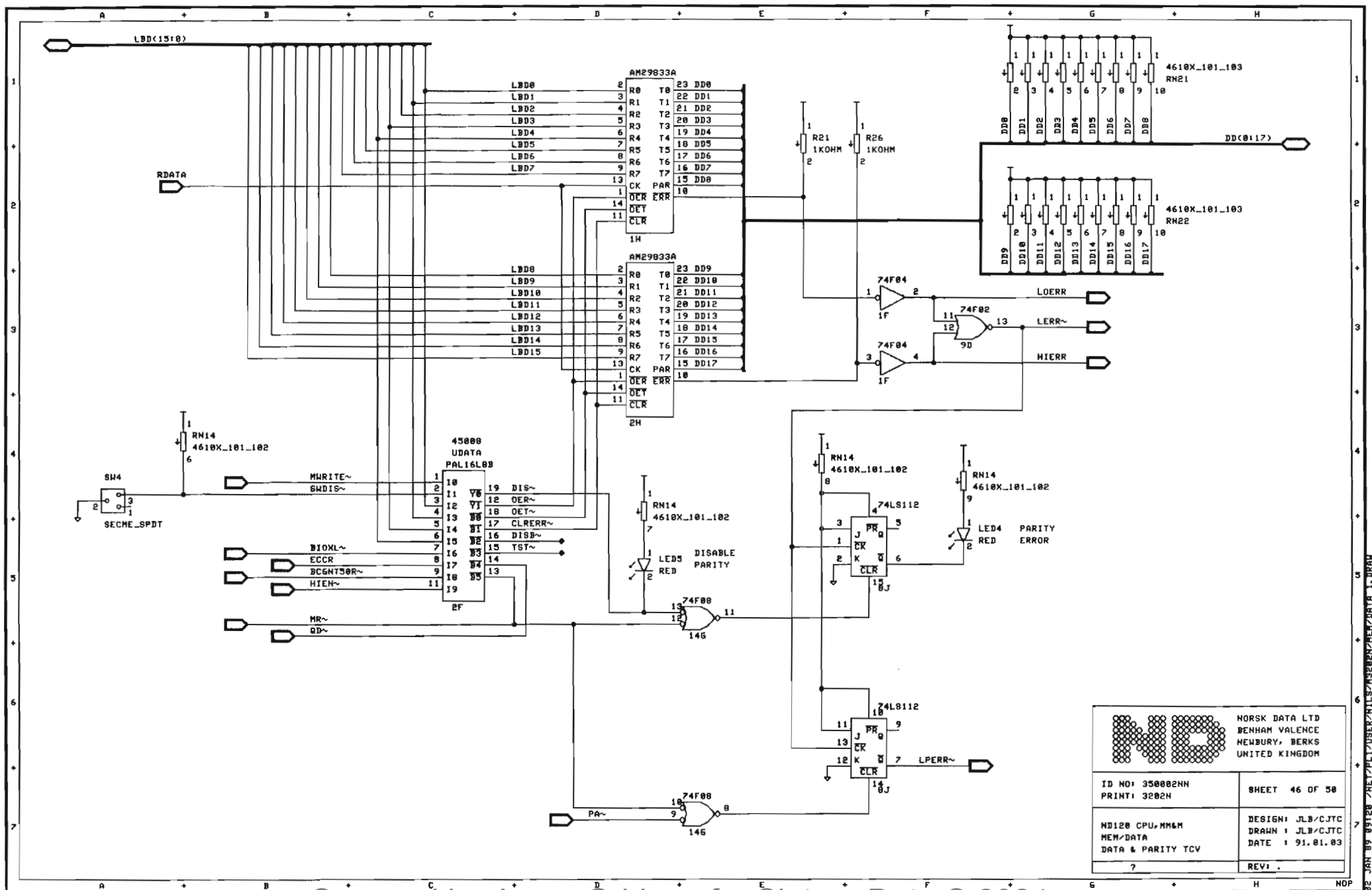
NORSK DATA LTD  
BENHAM VALENCE  
NEWBURY, BERKS  
UNITED KINGDOM

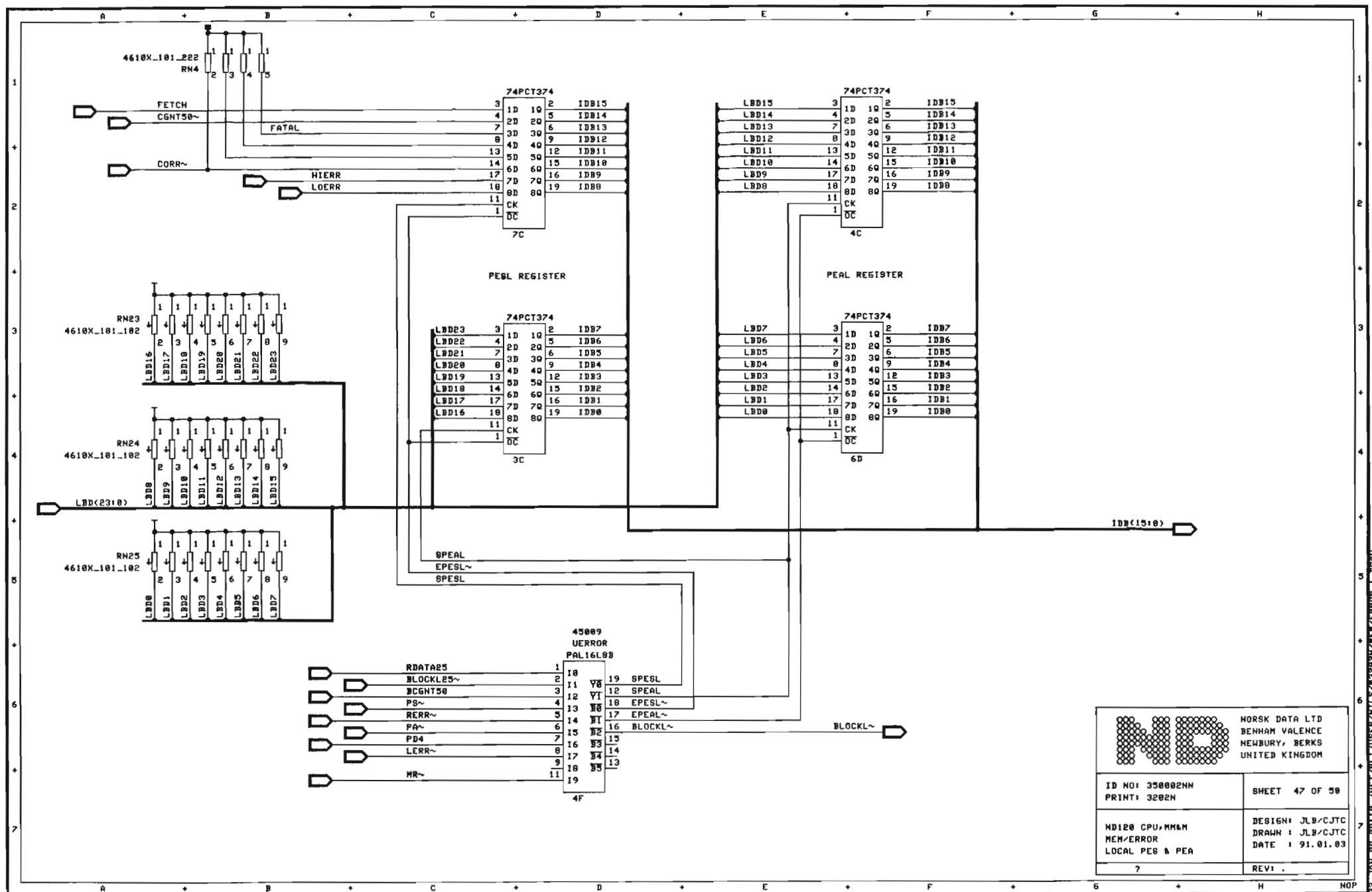
ID NO: 350002NH PRINT: 3202M	SHEET 42 OF 50
MD120 CPU, MM&M IO/UART UART & IOR REG	DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03
?	REV: .

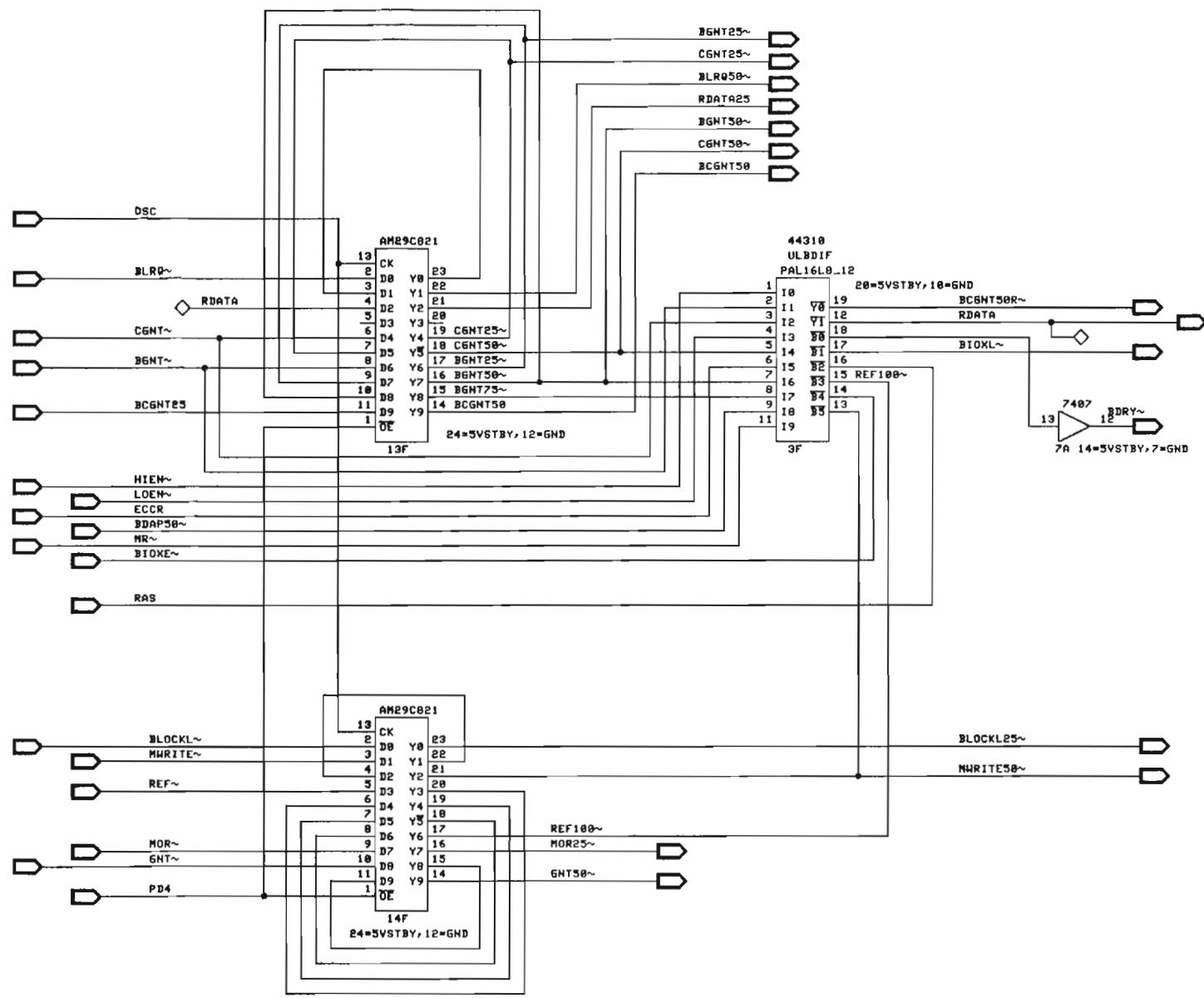


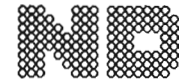






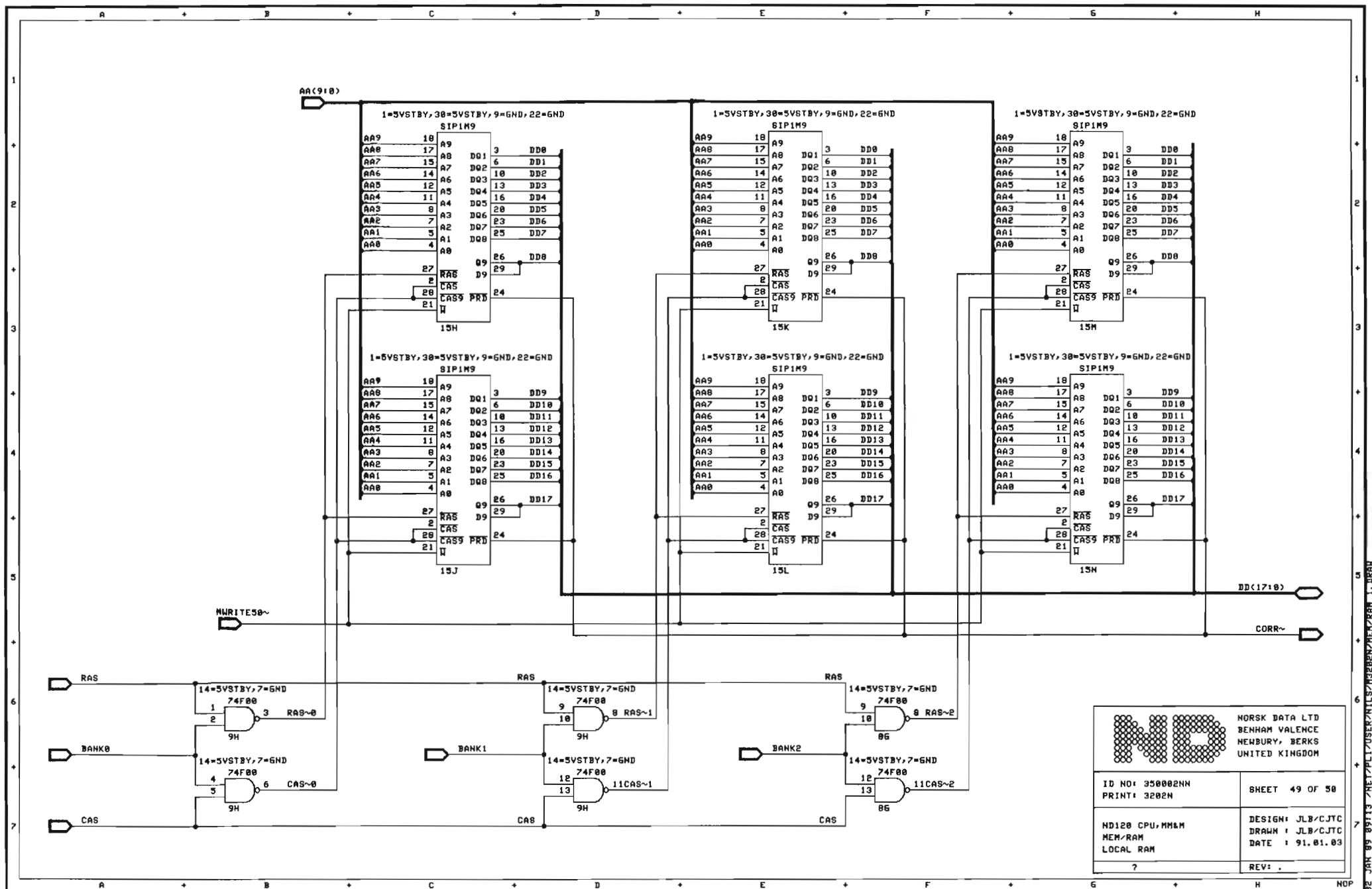


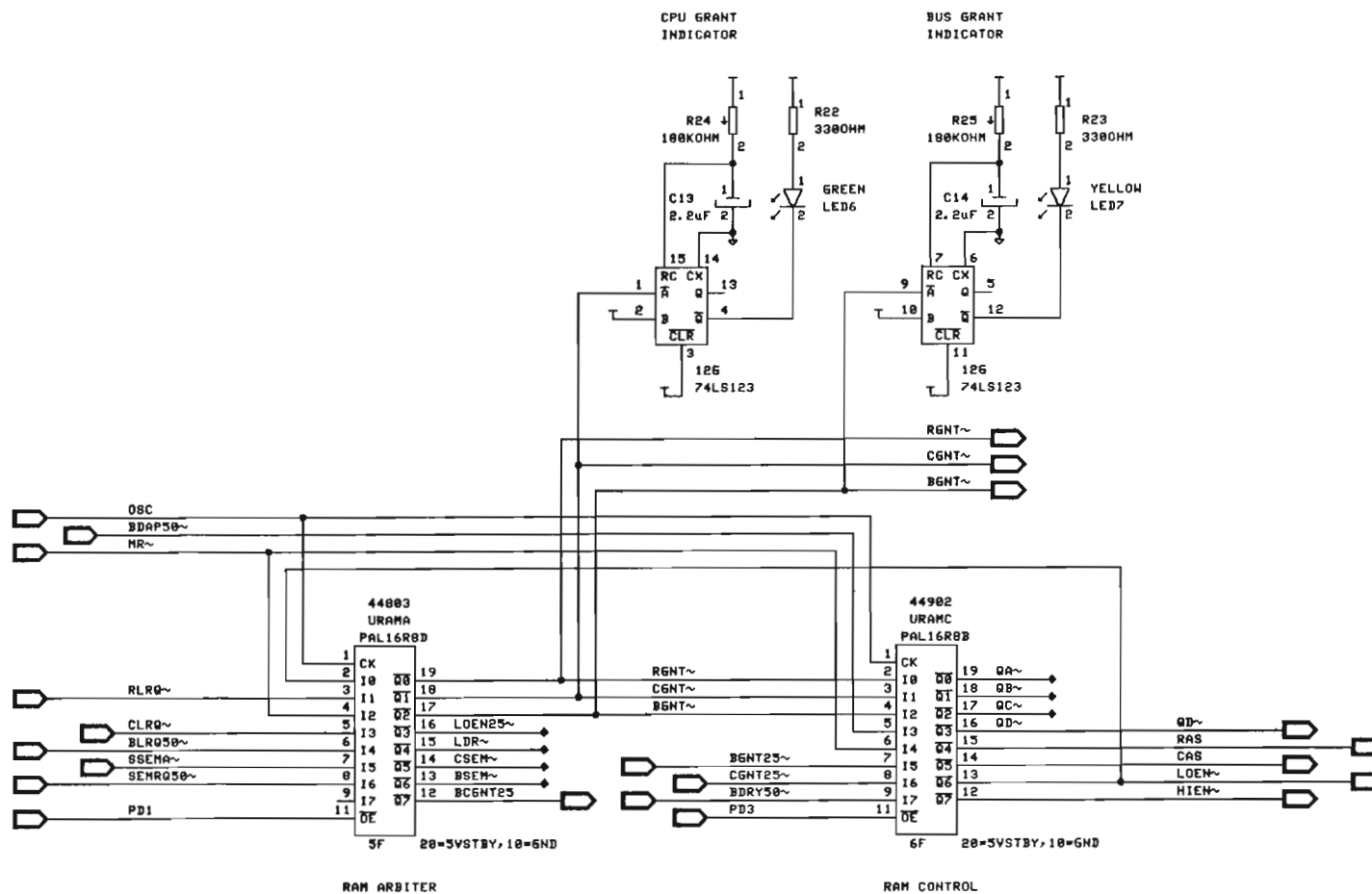



 <b>NORSK DATA LTD</b> BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 350002NN PRINT: 3202N	SHEET 48 OF 50
MD120 CPU, MM&M MEM/LBDIF LOCAL BD CONTROL	DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03
?	REV: .

32 JAN 89 09116 /NET/PL7USER/NIS/AG202H/NEW/LBDIF 1: DRAH







		NORSK DATA LTD BENHAM VALENCE NEWBURY, BERKS UNITED KINGDOM	
ID NO: 350002NH PRINT: 3202N		SHEET 50 OF 50	
MD120 CPU, MM&M MEM/RAMC LOCAL RAM CONTROL		DESIGN: JLB/CJTC DRAWN: JLB/CJTC DATE: 91.01.03	
?		REV:	