```
// ROM
                 // RAM:0000-RAM:07ff
                Port A goes to Real Time Clock chip MM58274
                PA.0 => RTC.DB0
                PA.1 => RTC.DB1
                PA.2 => RTC.DB2
                PA.3 => RTC.DB3
                PA.4 => RTC.A0
                PA.5 => RTC.A1
                PA.6 => RTC.A2
                Read or write to RTC is controlled by {\tt ROCLK\_n} and {\tt WRCLK\_n}
                Port B pin PB1 => RTC.WRCLK_n
                Port B pin P21 => RTC.ROCLK_n
                Port A also goes to IDB Bus:
                 PA.0-7 = IDB.0-7
               PORTA_RTC_IDB07
                                                                XREF[7]: RESET:00ea(R), FUN_0142:014d(R),
                                                                             FUN 0142:0157(R),
                                                                             caseD 10:01b7(R),
                                                                             caseD_6c:0213(R),
                                                                             FUN_025e:0268(R),
                                                                             FUN_025e:0297(R)
                  ?? 00h
0000 00
                Port B read/write signals
                PB.0 <= WMM_n
                PB.1 <= WRCLK n
                PB.2 <= ROCLK_n
PB.3 <= RMM_n (external signal out)
                PB.4 <= STAT3 (IDB11)
                PB.5 <= STAT4
                PB.6 <= READ (IDB13)
                PB.7 => STAT7 (IDB15)
              *PORTB_ND_SIGNALS
                                                                XREF[4]: FUN_023c:023e(W),
                                                                            FUN_025e:0260(W),
FUN_05d2:05ec(W),
                                                                             FUN_05fc:05fc(W)
0001 ff
                ??
                             FFh
                Port C
                Goes to STAT (?) and DISPLAY
                0 = STATO
                1 = STAT1
                2 = STAT2
                3 = DISP1
                4 = DISP2
                5 = DISP3
                6 = DISP4
                 7 = DISP5
               PORTC_STAT_DISP
                                                                XREF[15]: RESET:00e8(RW),
                                                                             FUN_0142:014b(RW),
                                                                             FUN 023c:023c(RW),
                                                                             FUN_023c:0240(RW),
                                                                              FUN_025e:0276(RW),
                                                                              FUN_025e:0278(RW),
                                                                             FUN_025e:027a(RW),
FUN 025e:0299(RW),
                                                                              FUN_025e:029b(RW),
                                                                              FUN_05d2:05ee(RW),
                                                                             FUN_05d2:05f0(RW),
                                                                             FUN 05fc:05fe(RW),
                                                                             FUN_05fc:0600(RW),
                                                                              FUN_05fc:0602(RW),
                                                                             FUN_05fc:0604(RW)
                 ??
0002 ff
                              FFh
```

```
Port D reads status information from the bus
                PD.0 <= PCR0
                PD.2 <= PONI
                PD.3 <= IONI
                PD.4 <= LHIT
                PD.5 <= LEV0
                PD.6 <= Fixed HIGH signal.
               PD.7 <= EMP_n
              PORTD_NDBUS
0003 ff
                               FFh
              DDRA
                                                              XREF[1]:
                                                                          RESET: 00de(W)
0004 ff
              ^ DDRB
                                                               XREF[1]:
                                                                          RESET: 00d8(W)
0005 ff
                               FFh
              DDRC
                                                              XREF[1]:
0006 ff
                               FFh
              DDRD
0007 ff
                               FFh
               DAT_0008
                                                              XREF[3]:
                                                                          RESET:0100(W), FUN_0142:017b(R),
                                                                          FUN_025e:0264(W)
0008 ff
                ??
                               FFh
               DAT_0009
                                                               XREF[1]:
                                                                          RESET: 00e2(W)
0009 ff
                               FFh
              * SPCR
000a ff
              SPSR
000b ff
                               FFh
000c ff
                               FFh
              BAUD
000d ff
                               FFh
              *SCCR1
000e ff
                               FFh
              SCCR2
000f ff
                               FFh
                                                                          FUN_011f:012b(RW)
              SCSR
                                                              XREF[1]:
0010 ff
                               FFh
              SCDAT
                                                              XREF[3]:
                                                                          FUN_011f:012f(RW),
                                                                          FUN 011f:013d(W),
                                                                          FUN_0142:018b(W)
0011 ff
                               FFh
              TCR
                                                                          RESET:00fe(W), FUN_011f:011f(R),
FUN_011f:0125(W),
                                                              XREF[8]:
                                                                           FUN_0142:014f(R),
                                                                           FUN_0142:0160(R),
                                                                           FUN_0142:016c(R),
                                                                          FUN_0142:0175(R),
caseD_34:01db(R)
0012 ff
                ??
              TSR
                                                              XREF[2]: FUN_011f:0129(W),
                                                                          FUN_0142:0153(RW)
0013 ff
                 ??
                              FFh
```

```
* ICHR
                                                                 XREF[25]:
                                                                              RESET:00e6(W), RESET:00ed(RW),
                                                                               RESET: 00f1(RW), RESET: 00f3(R),
                                                                               FUN_011f:0133(RW),
                                                                               FUN_0142:0145(RW),
                                                                               FUN_0142:0149(RW),
                                                                               FUN_0142:0171(R),
                                                                               FUN 0142:0173(W),
                                                                               FUN 0142:0181(RW),
                                                                               FUN_0142:0185(RW),
                                                                               FUN_0142:018f(R),
                                                                               FUN_01c0:01c6(R),
                                                                              caseD_2a:01d1(R),
caseD_5e:0208(R),
                                                                               caseD_5e:020b(RW),
                                                                               caseD_5e:020f(RW),
                                                                               caseD_6c:0222(R),
                                                                               caseD 6c:0225(RW),
                                                                               FUN_0238:0238(R), [more]
0014 ff
                 ??
                                FFh
               *ICLR
                                                                 XREF[5]:
                                                                               FUN 011f:0123(W),
                                                                               FUN 0142:0159(R),
                                                                               FUN_0142:0177(W),
                                                                               caseD_5e:0205(R),
                                                                               caseD_6c:021f(R)
0015 ff
                     22
                                FFh
                                                                  XREF[2]:
                                                                               FUN_0142:0168(W),
                                                                               FUN_0142:0195(R)
0016 ff
                                FFh
               OCLR
                                                                  XREF[2]:
                                                                               FUN_0142:015b(W),
                                                                               FUN_0142:015d(R)
0017 ff
                    ??
                                FFh
               * CHR
                                                                 XREF[4]:
                                                                               RESET:00fc(W), FUN_025e:02e0(R),
                                                                               FUN_025e:02e4(W),
                                                                               FUN_025e:0327(R)
0018 ff
                  ??
                                FFh
                                                                 XREF[11]:
                                                                              FUN_011f:0135(R),
                                                                               FUN_025e:02de(R),
                                                                               FUN_025e:02ef(W),
                                                                               FUN 025e:033f(RW),
                                                                               FUN_025e:034b(RW),
                                                                               FUN_025e:03b0(RW),
                                                                               FUN_025e:03b2(R),
                                                                               FUN_03cf:03eb(RW),
                                                                               FUN 03f4:03f9(R),
                                                                               FUN_041d:041f(W),
                                                                               FUN_041d:0507(RW)
0019 ff
                    ??
                                FFh
               * ACHR
                                                                 XREF[41:
                                                                              FUN 025e:0274(W).
                                                                               FUN_025e:02c0(RW),
                                                                               FUN_025e:02c2(R),
                                                                               FUN_025e:02ca(W)
001a ff
                    ??
                                FFh
               ACLR
                                                                              FUN_025e:02f5(W),
                                                                               FUN_025e:0316(W),
                                                                               FUN_041d:0436(R),
                                                                               FUN 041d:043e(R),
                                                                               FUN_041d:0446(R),
                                                                               FUN_041d:044e(R),
                                                                               FUN_041d:0473(W),
                                                                              FUN_041d:0496(R),
FUN 041d:049e(R),
                                                                               FUN_041d:04b6(W),
                                                                               FUN_041d:04bf(R),
                                                                               FUN_041d:04ce(R),
                                                                               FUN 041d:056e(W),
                                                                               FUN_057c:057c(R),
                                                                               FUN_057c:058a(R),
                                                                               FUN_0597:05a2(R),
                                                                               FUN_0597:05bb(R),
                                                                               FUN_0623:0626(W),
                                                                               FUN_0623:0628(RW),
                                                                               FUN_0623:062f(R), [more]
001b ff
                  ??
                              FFh
```

	_			
	DAT_001c		XREF[11]:	FUN_025e:02fa(W), FUN_025e:0320(W), FUN_041d:042e(R), FUN_041d:045e(R), FUN_041d:045e(R), FUN_041d:045e(R), FUN_041d:046e(R), FUN_041d:046e(R), FUN_041d:04a6(R), FUN_041d:04ab(W), FUN_041d:04ab(W), FUN_041d:04ab(W),
001c ff	??	FFh		
	DAT_001d		XREF[9]:	FUN_025e:02ff(W), FUN_025e:030a(R), FUN_0641:0645(W), FUN_0641:065c(W), FUN_0641:065c(R), FUN_0641:067f(RW), FUN_0641:0681(R), FUN_0641:0689(W), FUN_0641:0680(R)
001d ff	??	FFh		
001e ff	DAT_001e	FFh	XREF[9]:	caseD_a4:024d(W), caseD_a4:0259(RW), FUN_025e:027e(W), FUN_025e:028a(RW), FUN_03cf:03d3(W), FUN_03cf:03d9(RW), FUN_041d:0423(W), FUN_041d:047a(RW), FUN_041d:047a(RW), FUN_041d:0481(R)
0016.65	DAT_001f		XREF[20]:	FUN_025e:030c(W), FUN_025e:031(R), FUN_025e:031(R), FUN_025e:0369(W), FUN_025e:0369(W), FUN_03cf:03de(R), FUN_03cf:03de(R), FUN_041d:042a(W), FUN_041d:046e(R), FUN_041d:046e(R), FUN_041d:050c(R), FUN_041d:0516(R), FUN_041d:0533(R), FUN_041d:0534(R), FUN_041d:0534(R), FUN_041d:0556(R), FUN_0623:0624(W), FUN_0641:0641(W), FUN_0641:068b(R)
001f ff	??	FFh		
0020 ff	DAT_0020	DDL	XREF[6]:	FUN_0142:0142(R), caseD_2a:01d4(R), FUN_025e:029d(W), FUN_025e:02a5(R), FUN_025e:02ae(R), FUN_025e:02b7(R)
UUZU II	3.3	rrn		
0021 ff	DAT_0021	FFh	XREF[4]:	FUN_025e:029f(RW), FUN_025e:02a8(RW), FUN_025e:02ac(RW), FUN_025e:02cd(R)
0022 ff	DAT_0022	FFh	XREF[4]:	FUN_025e:02a1(RW), FUN_025e:02b1(RW), FUN_025e:02b5(RW), FUN_025e:02d1(R)
	DAT_0023		XREF[4]:	FUN_025e:02a3(RW), FUN_025e:02ba(RW), FUN_025e:02be(RW),
0023 ff	??	FFh		FUN_025e:02d5(R)
0023 11		E E 11		

0024 ff	PAT_0024 ?? FFh	XREF[6]:	FUN_03cf:03d7(W), FUN_0597:0597(RW), FUN_0597:0599(R), FUN_0597:05b0(RW), FUN_0597:05b2(R), FUN_0508:05cf(W)
0025 ff	DAT_0025 undefined1 FFh	XREF[5]:	FUN_025e:030f(W), FUN_025e:035d(R), FUN_025e:0393(R), FUN_025e:03b5(R), FUN_03f4:03fe(R)
0026 ff	DAT_0026 undefined1 FFh		FUN_025e:030f(W), FUN_025e:0393(R), FUN_03f4:03fe(R)
0020 11	DAT_0027		FUN_025e:032c(R), FUN_025e:034d(R), FUN_025e:0386(R)
0027 ff	undefined1 FFh DAT_0028		FUN_025e:032c(R), FUN_025e:034d(R)
0028 ff 0029 ff	undefined1 FFh DAT_0029 undefined1 FFh	XREF[1]:	FUN_025e:0395(W)
002a ff	DAT_002a undefined1 FFh	XREF[2]:	FUN_025e:035f(R), FUN_025e:0395(W)
	DAT_002b	XREF[3]:	FUN_025e:032e(W), FUN_025e:034f(R), FUN_025e:035f(R)
002b ff	undefined1 FFh DAT_002c		FUN_025e:032e(W), FUN_025e:034f(R)
002c ff 002d ff	undefined1 FFh DAT_002d undefined1 FFh	XREF[1]:	FUN_025e:02f3(R)
002e ff	DAT_002e undefined1 FFh	XREF[2]:	FUN_025e:02cf(W), FUN_025e:02f8(R)
002f ff	DAT_002f undefined1 FFh		FUN_025e:0314(R)
0030 ff 0031 ff 0032 ff 0033 ff	DAT_0030 undefined1 FFh ?? FFh ?? FFh ?? FFh	XREF[1]:	FUN_025e:031e(R)
0034 ff	DAT_0034 undefined1 FFh	XREF[1]:	FUN_025e:02cf(W)
0035 ff	DAT_0035 undefined1 FFh	XREF[8]:	FUN_041d:04eb(R), FUN_041d:04f5(R), FUN_041d:050e(R), FUN_041d:0518(R), FUN_041d:0535(R), FUN_041d:0535(R), FUN_041d:0536(R), FUN_041d:0558(R)
0036 ff	DAT_0036 undefined1 FFh	XREF[1]:	FUN_025e:02d3(W)
0037 ff	DAT_0037 undefined1 FFh	XREF[1]:	FUN_041d:054e(R)
0038 ff	DAT_0038 ?? FFh	XREF[3]:	FUN_041d:04db(R), FUN_041d:04fd(R), FUN_041d:0520(R)

	DAT_003a		XREF[3]:	FUN_025e:0341(R),
	_			FUN_025e:0356(R), FUN 025e:039d(R)
003a ff	??			10N_0230.0334(N)
003b ff	??	FFh		
	DAT_003c		XREF[2]:	FUN_025e:02d3(W),
003c ff	undefined1	FFh		FUN_041d:0502(R)
	DAT 003d		XREF[1]:	FUN 041d:056c(R)
003d ff	undefined1	FFh		
	DAT_003e		XREF[2]:	FUN_025e:02d7(W),
003e ff	undefined1	FFh		FUN_041d:056c(R)
003f ff	??			
0040 ff	??	FFh		
0041 ff		FFh		
0042 ff	??	FFh		
0043 ff	??	FFh		
	DAT_0044		XREF[1]:	FUN_025e:02d7(W)
0044 ff	undefined1	FFh		
	DAT_0045		XREF[4]:	
				FUN_025e:03a4(W), FUN_025e:03ab(R)
0045 ff	??	FFh		
	DAT_0046		XREF[6]:	FUN_0690:0698(W),
				FUN_0690:06a2(W),
				FUN_0690:06a8(W),
				FUN_06b0:06b0(W),
				FUN_06b0:06b4(RW),
0046 ff	??	FFh		FUN_06b0:06b6(R)
	DAT_0047		XREF[7]:	-
				FUN_025e:0339(W),
				FUN_025e:0370(R),
				FUN_025e:0378(W),
				FUN_025e:037f(RW), FUN_0690:0696(R),
				FUN_0690:06a6(R)
0047 ff	??	FFh		
	DAT_0048		XREF[3]:	FUN_05d2:05d4(R),
				FUN_05d2:05f2(RW), FUN_06b0:06b2(W)
0048 ff	??	FFh		101_0020.002(11)
	DAT_0049		XREF[2]:	FUN_05d2:05d2(W),
0049 ff	??	FFh		FUN_05d2:05ea(R)
004a ff		FFh		
004b ff		FFh		
	DAT_004c		XREF[3]:	caseD_a4:0251(W),
				FUN_025e:027c(R), FUN 025e:0290(W)
004c ff	??	FFh		1011_0200.0230()
004d ff	??	FFh		
004e ff	??	FFh		
004f ff	??	FFh		
0050 ff	??	FFh		
0051 ff	??	FFh		
0052 ff	??	FFh		
0053 ff	??	FFh		
0054 ff		FFh		
0055 ff	??	FFh		
0056 ff	??	FFh		
0057 ff		FFh		
0058 ff		FFh		
0059 ff		FFh		
005a ff		FFh		
005b ff		FFh		
005c ff		FFh		
005d ff		FFh		
005e ff		FFh		
005f ff	??	FFh		
0060 ff 0061 ff	?? ??	FFh FFh		
		**		

	5U3_35C.BI				
0062 ff	??	FFh			
0063 ff	??	FFh			
0064 ff	??	FFh			
0065 ff	??	FFh			
0066 ff	??	FFh			
0067 ff	??	FFh			
0068 ff	??	FFh			
0069 ff	??	FFh			
006a ff	??	FFh			
006b ff	??	FFh			
006c ff	??	FFh			
006d ff	??	FFh			
006e ff	??	FFh			
006f ff	??	FFh			
0070 ff	??	FFh			
0071 ff	??	FFh			
0072 ff	??	FFh			
0073 ff	??	FFh			
0074 ff	??	FFh			
0075 ff	??	FFh			
0076 ff	??	FFh			
0077 ff	??	FFh			
0078 ff	??	FFh			
0079 ff	??	FFh			
007a ff	??	FFh			
007b ff	??	FFh			
007c ff	??	FFh			
007d ff	??	FFh			
007e ff	??	FFh			
007f ff	??	FFh			
	DAT_0080		XREF [[1]:	FUN_0142:0194(R)
0080 01	undefined1	01h			
0081 01	??	01h			
0082 02	??	02h			
0083 00	??	00h			
0084 10	??	10h			
0085 05	??	05h			
0086 20	??	20h			
0087 06	??	06h			
0088 05	??	05h			
0089 07	??	07h			
0089 07 008a ff	??	FFh			
0004 11	::	FFII			
	DAT 008b		XREF	[1]:	FUN 0142:0194(R)
008b 01	undefined1	01h	•		
			XREF [[1]:	FUN 0142:01a2(R)
	DAT_008c	0.1 %			
008c 01	DAT_008c undefined1	UIII			,
008c 01	undefined1	OIN			
	undefined1		XREF [FUN_0142:0194(R)
008c 01	undefined1		XREF [
	undefined1		XREF [
008d 02	undefined1 DAT_008d undefined1	02h	XREF [
008d 02 008e 02	undefined1 DAT_008d undefined1 ??	02h 02h	XREF (
008d 02 008e 02 008f 04 0090 03	DAT_008d undefined1 ?? ?? ??	02h 02h 04h 03h	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08	undefined1 DAT_008d undefined1 ?? ??	02h 02h 04h 03h 08h	XREF [
008d 02 008e 02 008f 04 0090 03	DAT_008d undefined1 ?? ?? ?? ??	02h 02h 04h 03h	XREF [
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04	DAT_008d undefined1 ?? ?? ?? ?? ??	02h 02h 04h 03h 08h 04h	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05	Undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ??	02h 02h 04h 03h 08h 04h 10h 05h	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20	DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ??	02h 02h 04h 03h 08h 04h 10h 05h 20h	XREF		
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06	undefined1 DAT_008d	02h 02h 04h 03h 08h 04h 10h 05h 20h	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06	undefined1 DAT_008d	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFFh	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f	undefined1 DAT_008d	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFFh	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? char char	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFh 'o' 'N'	XREF		
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e	Undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ??	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFh 'O'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20	Undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ??	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFh 'O' 'N'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 009b 22 009c 4f	Undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? char char char char char	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFFH 'O' 'N' ''	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 009b 22 009c 4f 009d 46	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? char char char char char char	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFFH 'O' 'N' ''	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 009b 22 009c 4f 009d 46	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ??	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFh 'o' 'N' ''' 'F'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 0096 22 0096 4f 0096 46 0096 22	Undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ??	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFH '0' 'N' '"' 'F' '"'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4F 0099 4e 009a 20 009b 22 009c 4f 009d 46 009e 46 009f 22 009d 44	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFFh 'O' 'N' 'F' 'F' 'F' 'D'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 009b 22 009c 4f 009d 46 009e 46 009f 22 009d 46 009f 22 000d 44 00al 41	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? char char char char char char char char	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFFH '0' 'N' 'I' 'F' 'F' 'B' 'D' 'A'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4F 0099 4e 009a 20 009b 22 009c 4f 009d 46 009e 46 009f 22 009d 44	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFH 'O' 'N' ''' 'F' 'F' 'F' 'A' 'Y'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 009b 22 009c 4f 009d 46 009e 46 009f 22 009d 46 009f 22 000d 44 00al 41	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? char char char char char char char char	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFH '0' 'N' '"' 'F' 'F' 'T' 'A' 'Y' ':'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 009b 22 009c 4f 009d 46 009e 46 009f 22 009c 46 009f 22 00a0 44 00a1 41 00a2 59	Undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ??	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFFH 'O' 'N' 'F' 'F' 'F' 'Y' 'Y' 'Y'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 009b 22 009c 4f 009d 46 009e 46 009f 22 009d 44 00al 41 00al 59 00al 3a	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? char char char char char char char char	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFH '0' 'N' '"' 'F' 'F' 'T' 'A' 'Y' ':'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4F 0099 4e 009a 20 009b 22 009c 4f 009d 46 009e 46 009e 46 009f 22 00a0 44 00a1 41 00a2 59 00a3 3a 00a4 22	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? char char char char char char char char	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFFH 'O' 'N' 'F' 'F' 'F' 'Y' 'Y' 'Y'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 009b 22 009c 4f 009d 46 009e 46 009e 46 009f 22 00a0 44 00a1 41 00a2 59 00a3 3a 00a4 22 00a5 20	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? char char char char char char char char	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFFH '0' 'N' ''' 'F' 'F' 'T' 'Y' 'Y' ':'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 009b 22 009c 4f 009d 46 009e 46 009e 46 009f 22 00a0 44 00a1 41 00a2 59 00a3 3a 00a4 22 0056 20	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ??	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFFH 'O' 'N' '' 'F' 'F' '' '' 'Y' '' '' '' '' '' '' '' '' '' '	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 0096 22 0096 4f 009d 46 009e 46 009f 22 00a0 44 00a1 41 00a2 59 00a3 3a 00a4 22 00a5 20 00a6 20 00a7 54	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? char char char char char char char char	02h 02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFH 'O' 'N' ''' 'F' 'F' ''' 'A' ''Y' ':' ''' 'T'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 009b 22 009c 4f 009d 46 009e 46 009e 22 00a0 44 00a1 41 00a2 59 00a3 3a 00a4 22 00a5 20 00a6 20 00a7 54 00a8 49	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? char char char char char char char char	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFH 'O' 'N' ''' 'F' 'T' 'I' 'I'	XREF (
008d 02 008e 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 009b 22 009c 4f 009d 46 009e 46 009e 46 009e 22 00a0 44 00a1 41 00a2 59 00a3 3a 00a4 22 00a5 20 00a6 20 00a7 54 00a8 49	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFH '0' 'N' '"' 'E' 'F' 'E' ''' 'A' 'Y' 'T' 'M'	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 009b 22 009c 4f 009d 46 009e 46 009e 22 00ad 44 00al 41 00a2 59 00a3 3a 00a4 22 00a5 20 00a6 20 00a7 54 00a8 49 00a9 4d 00aa 45	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ??	02h 02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFH 'O' 'N' ''' 'F' ''' ''' ''' ''' ''' ''' ''	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 0096 22 0096 4f 009d 46 009e 46 009f 22 00a0 44 00a1 41 00a2 59 00a3 3a 00a4 22 00a5 20 00a6 20 00a7 54 00a8 49 00a9 4d 00aa 45 00ab 3a	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? char char char char char char char char	02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFH 'O' 'N' ''' 'F' 'F' 'T' ''' 'M' 'E' ''' ''' ''' ''' ''' ''' ''	XREF (
008d 02 008e 02 008f 04 0090 03 0091 08 0092 04 0093 10 0094 05 0095 20 0096 06 0097 ff 0098 4f 0099 4e 009a 20 0096 22 0096 4f 0096 46 0096 22 00a0 44 00a1 41 00a2 59 00a3 3a 00a4 22 00a5 20 00a6 20 00a6 20 00a7 54 00a8 49 00a9 4d 00aa 45 00ab 3a 00ac 22	undefined1 DAT_008d undefined1 ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? ?? char char char char char char char char	02h 02h 02h 04h 03h 08h 04h 10h 05h 20h 06h FFH 'O' 'N' ''' 'F' 'F' 'F' 'T' ''' ''' ''' ''' ''	XREF (

```
Ghidra - MC68705U3_35C.BIN
           00af <mark>20</mark>
                              char
           00b0 55
           00b1 54
                              char
           00b2 43
                              char
           00b3 3a
                              char
           00b5 41
                              char
           00b6 44
                              char
           00b7 44
                              char
           00b8 <mark>52</mark>
                              char
           00b9 45
           00ba 53
                              char
           00bb 53
                              char
           00bc 3a
                              char
           00be 50
                              char
                                         'P'
           00bf 20
                              char
           00c0 43
                              char
           00c1 4f
                              char
           00c3 4e
                              char
           00c4 54
                              char
           00c5 3a
                              char
           00c6 22
           00c7 59
           00c8 45
                              char
           00c9 41
                              char
           00ca 52
                              char
           00cc 22
                              char
           00cd 20
                              char
           00ce 20
                              char
           00cf 4d
                              char
           00d0 4f
           00d1 4e
                              char
                                         'N'
           00d2 54
                              char
           00d3 48
                              char
           00d4 3a
           00d5 22
                              char
                                                   FUNCTION
                          undefined RESET()
                                          <RETURN>
           undefined
                            A:1
                          RESET
                                                                        XREF[4]: 07f8(*), 07fa(*), 07fc(*),
                                                                                    07fe(*)
           00d6 a6 ff
                            LDA
                                         #0xff
           00d8 b7 05
00da b7 06
                              STA
                                         DDRB
                                         DDRC
                              STA
           00dc a6 00
                             LDA
                                         #0×0
           00de b7 04
                                         DDRA
           00e0 a6 78
                             LDA
                                         #0x78
           00e2 b7 09
                              STA
                                         DAT 0009
           00e4 a6 60
                              LDA
                                         #0x60
           00e6 b7 14
                              STA
                                         ICHR
           00e8 12 02
                                         0x1,PORTC_STAT_DISP
           00ea 06 00 04
                              BRSET
                                         0x3,PORTA_RTC_IDB07,LAB_00f1
           00ed 1f 14
                              BCLR
                                         0x7,ICHR
                              BRA
                                         LAB 00f3
           00ef 20 02
                          LAB_00f1
                                                                        XREF[1]:
           00f1 1e 14
                              BSET
                                         0x7,ICHR
                          LAB_00f3
                                                                        XREF[1]:
                                                                                    00ef(j)
           00f3 b6 14
           00f5 cd 02 3c
                                         FUN_023c
           00f8 a6 00
                              T.DA
                                         #0x0
                                         DAT 0045
           00fa b7 45
                              STA
           00fc b7 18
           00fe b7 12
                                         TCR
           0100 b7 08
                              STA
                                         DAT_0008
                                         switchD_01a4::caseD_a4
           0102 cd 02 4b
                              JSR
           0105 a6 38
                                         #0x38
                              LDA
           0107 cd 05 fc
                                         FUN_05fc
           010a a6 0c
                              LDA
                                         #0xc
                                         FUN_05fc
           010c cd 05 fc
                              JSR
           010f a6 06
                              LDA
                                         #0x6
                                         FUN_05fc
           0111 cd 05 fc
           0114 cd 06 18
                                         FUN_0618
                          LAB_0117
                                                                        XREF[1]: 0119(j)
           0117 2f 02
                                         LAB 011b
```

LAB_0117

	0441			
0111- 0- 00	LAB_011b	DIN 0116	XREF[2]:	0117(j), 011d(j)
011b 2e 02 011d 20 fc	BIL	FUN_011f LAB 011b		
0110 20 10	BNA	LAB_UIID		
	******	*******	******	****
	*	FUNCTION		*
	******	*******	*****	****
	undefined FU	_		
undefined	A:1	<return></return>		
	FUN_011f		XREF[2]:	RESET:011b(j), FUN_01c0:01ce(c)
011f b6 12	LDA	TCR		= FFh
0121 a4 c0 0123 b7 15	AND STA	#0xc0 ICLR		= FFh
0123 B7 13	SIA	ICLR		
	LAB 0125		XREF[1]:	FUN_0142:0151(j)
0125 b7 12	STA	TCR		= FFh
0127 a6 05	LDA	#0×5		
0129 b7 13	STA	TSR		= FFh
	LAB_012b		XREF[2]:	FUN_0142:0155(j),
				FUN_0142:0179(j)
012b 3a 10	DEC	SCSR		= FFh
012d 26 10	BNE	LAB_013f		
012f 3a 11	DEC	SCDAT		= FFh
0131 26 0c 0133 19 14	BNE	LAB_013f		
0135 19 14 0135 0a 19 07	BCLR	0x4,ICHR		
0133 0a 19 07 0138 cd 06 18	BRSET JSR	0x5,CLR,LAB_013f FUN 0618		= FFh undefined FUN 0618()
0136 cd 06 18 013b a6 06	LDA	#0×6		
013d b7 11	STA	SCDAT		
	LAB_013f		XREF[3]:	012d(j), 0131(j), 0135(j)
013f cc 02 5e	JMP	FUN_025e		
	Flow Overs			
	******	*******	******	****
	*	FUNCTION ************************************		****
undefined	undefined FUI A:1	<pre>N_UI42() <return></return></pre>		
underined	FUN 0142	(RETORN)	XREF[2]:	FUN 025e:0292(c),
	1011_0142		ARDI[2].	FUN 041d:0579(c)
0142 06 20 04	BRSET	0x3,DAT 0020,LAB 0149		= FFh
0145 1f 14	BCLR	0x7,ICHR		= FFh
0147 20 02	BRA	LAB 014b		
	LAB_0149		XREF[1]:	0142(j)
0149 le 14	BSET	0x7,ICHR		= FFh
	770 0141		uppp/11	01.47.(1)
014b 13 02	LAB_014b BCLR	A-1 DODEC CEAR DICD	XREF[1]:	0147(j) = FFh
014b 13 02 014d b6 00	LDA	0x1,PORTC_STAT_DISP PORTA RTC IDB07		
014d b0 00 014f b1 12	CMP	TCR		
0141 B1 12 0151 26 d2	BNE	LAB_0125		
0151 20 d2 0153 3a 13	DEC	TSR		= FFh
0155 26 d4	BNE	LAB 012b		
0157 b6 00	LDA	PORTA RTC IDB07		
0159 b8 15	EOR	ICLR		= FFh
015b b7 17	STA	OCLR		= FFh
015d 0c 17 0c	BRSET	0x6,OCLR,LAB_016c		= FFh
0160 b6 12	LDA	TCR		= FFh
0162 a4 3f	AND	#0x3f		
0164 al 00	CMP	#0×0		
0166 27 Od	BEQ	LAB_0175		
0168 b7 16	STA	OCHR		= FFh
016a 20 1d	BRA	LAB_0189		
	LAB 016c		XREF[1]:	015d(j)
016c 0c 12 0c	BRSET	0x6,TCR,LAB 017b		= FFh
016f a6 60	LDA	#0×60		
0171 ba 14	ORA	ICHR		= FFh
0173 b7 14	STA	ICHR		= FFh
	LAB_0175		XREF[3]:	0166(j), 0183(j), 0187(j)
0175 b6 12	LDA	TCR		= FFh
0177 b7 15	STA	ICLR		= FFh
0179 20 b0	BRA	LAB_012b		
	TAB 0171		VDPP[1]	0160(3)
017b b6 08	LAB_017b LDA	DAT 0008	XREF[1]:	016c(j) = FFh
017d a1 00	CMP	DAT_0008 #0×0		
017d al 00 017f 26 04	BNE	#UXU LAB 0185		
0171 26 04 0181 1a 14	BSET	0x5,ICHR		= FFh
0181 14 14 0183 20 f0	BRA	LAB 0175		
3103 LV LV	DIG			

				04.004.11
	LAB_0185		XREF[1]:	017f(j)
0185 1b 14	BCLR	0x5,ICHR		= FFh
0187 20 ec	BRA	LAB_0175		
	LAB 0189		XREF[1]:	016-(-)
0189 a6 50	LDA	#0×50	AREF[I].	0104())
018b b7 11	STA	SCDAT		
018d ae 80	LDX	#0×80		
018f 09 14 02		0x4,ICHR,LAB 0194		= FFh
0101 05 14 02 0192 ae 8b	LDX	#0x8b		
0132 de 0D	DDA	#ОХОБ		
	LAB 0194		XREF[2].	018f(j), 019f(j)
0194 f6	LDA	X=>DAT 008b		= 01h
		<u></u>		
0195 b1 16	CMP	OCHR		= FFh
0197 27 08	BEQ	LAB 01a1		
0199 al ff	CMP	#0xff		
019b 27 31	BEQ	LAB 01ce		
019d 5c	INCX	_		
019e 5c	INCX			
019f 20 f3	BRA	LAB 0194		
		_		
	LAB 01a1		XREF[1]:	0197(j)
01a1 5c	INCX			
01a2 fe	LDX	X=>DAT 008c		= 01h
01a3 58	ASLX	_		
	switchD 01a4:	:switchD		
01a4 dc 01 a7	_	0x1a7,X		
	switchD_01a4:	:caseD_0	XREF[1]:	01a4(j)
01a7 20 32	BRA	switchD_01a4::caseD_34		undefined caseD_34()
	Flow Overri			
	switchD_01a4:	:caseD_2	XREF[1]:	01a4(j)
01a9 20 26	BRA	switchD_01a4::caseD_2a		
	Flow Overri			
	switchD_01a4:	:caseD_4	XREF[1]:	01a4(j)
01ab 20 40	BRA	switchD_01a4::caseD_46		undefined caseD_46()
	Flow Overri			
	switchD_01a4:		XREF[1]:	01a4(j)
01ad 20 4e	BRA	switchD_01a4::caseD_56		
01ad 20 4e		<pre>switchD_01a4::caseD_56 .de: CALL_RETURN (CALL_TERMINATOR)</pre>		
01ad 20 4e				
01ad 20 4e	Flow Overri	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8	XREF[1]:	
01ad 20 4e 01af 20 50	Flow Overri switchD_01a4: BRA	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a	XREF[1]:	
	Flow Overri switchD_01a4: BRA	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8	XREF[1]:	01a4(j)
	Flow Overri switchD_01a4: BRA Flow Overri	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR)	XREF[1]:	01a4(j) undefined caseD_5a()
01af 20 50	Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4:	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a	XREF[1]:	Ola4(j) undefined caseD_5a() Ola4(j)
	Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e	<pre>XREF[1]:</pre> <pre>XREF[1]:</pre>	01a4(j) undefined caseD_5a()
01af 20 50	Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a	<pre>XREF[1]:</pre> <pre>XREF[1]:</pre>	Ola4(j) undefined caseD_5a() Ola4(j)
01af 20 50	switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR)	<pre>XREF[1]:</pre> <pre>XREF[1]:</pre>	Ola4(j) undefined caseD_5a() Ola4(j)
01af 20 50 01b1 20 52	switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4:	de: CALL_RETURN (CALL_TERMINATOR) ccaseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) ccaseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) ccaseD_c	<pre>XREF[1]:</pre> <pre>XREF[1]:</pre>	<pre>01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j)</pre>
01af 20 50	switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA	de: CALL_RETURN (CALL_TERMINATOR) ccaseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) ccaseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) ccaseD_c switchD_01a4::caseD_6c	<pre>XREF[1]: XREF[1]: XREF[1]:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5a()
01af 20 50 01b1 20 52	switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA	de: CALL_RETURN (CALL_TERMINATOR) ccaseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) ccaseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) ccaseD_c	<pre>XREF[1]: XREF[1]: XREF[1]:</pre>	<pre>01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j)</pre>
01af 20 50 01b1 20 52	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri	de: CALL_RETURN (CALL_TERMINATOR) ccaseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) ccaseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) ccaseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR)	<pre>XREF{1]: XREF{1]: XREF{1]:</pre>	<pre>01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c()</pre>
01af 20 50 01b1 20 52 01b3 20 5e	switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4:	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e	<pre>XREF[1]: XREF[1]: XREF[1]:</pre>	<pre>01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c()</pre>
01af 20 50 01b1 20 52	switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4:	de: CALL_RETURN (CALL_TERMINATOR) ccaseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) ccaseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) ccaseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR)	<pre>XREF{1]: XREF{1]: XREF{1]:</pre>	<pre>01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c()</pre>
01af 20 50 01b1 20 52 01b3 20 5e	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_30	<pre>XREF[1]: XREF[1]: XREF[1]:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c()
01af 20 50 01b1 20 52 01b3 20 5e	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30	<pre>XREF[1]: XREF[1]: XREF[1]:</pre>	<pre>01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) *****</pre>
01af 20 50 01b1 20 52 01b3 20 5e	switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30	<pre>XREF[1]: XREF[1]: XREF[1]:</pre>	<pre>01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** *</pre>
01af 20 50 01b1 20 52 01b3 20 5e	switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30	<pre>XREF[1]: XREF[1]: XREF[1]:</pre>	<pre>01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** *</pre>
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20	switchD_Ola4: BRA Flow Overri switchD_Ola4: BRA Flow Overri switchD_Ola4: BRA Flow Overri switchD_Ola4: BRA witchD_Ola4: BRA witchD_Ola4: BRA	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION	<pre>XREF[1]: XREF[1]: XREF[1]:</pre>	<pre>01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** *</pre>
01af 20 50 01b1 20 52 01b3 20 5e	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA WitchD_0la4: BRA WitchD_0la4: BRA How Overri switchD_0la4: BRA	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION **PD_10()	<pre>XREF{1}: XREF{1}: XREF{1}: XREF{1}:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j)
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20	switchD_Ola4: BRA Flow Overri switchD_Ola4: BRA Flow Overri switchD_Ola4: BRA Flow Overri switchD_Ola4: BRA witchD_Ola4: BRA witchD_Ola4: BRA	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION **PD_10()	<pre>XREF{1}: XREF{1}: XREF{1}: XREF{1}:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** * **** FUN_0142:01a4(j), 01bd(j),
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA WitchD_0la4: BRA WitchD_0la4: BRA How Overri switchD_0la4: BRA	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION **PD_10()	<pre>XREF{1}: XREF{1}: XREF{1}: XREF{1}:</pre>	Ola4(j) undefined caseD_5a() Ola4(j) undefined caseD_5e() Ola4(j) undefined caseD_6c() Ola4(j) **** * **** FUN_0142:01a4(j), Olbd(j), FUN_01c0:01cb(c),
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20 undefined	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA witchD_0la4: BRA switchD_0la4: BRA switchD_0la4: BRA switchD_0la4: BRA	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION ### COUNTY OF THE PROPOSED TO THE PROPO	<pre>XREF{1}: XREF{1}: XREF{1}: XREF{1}:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** * **** FUN_0142:01a4(j), 01bd(j),
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA witchD_0la4: BRA switchD_0la4: BRA switchD_0la4: BRA switchD_0la4: BRA	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION **PD_10()	<pre>XREF{1}: XREF{1}: XREF{1}: XREF{1}:</pre>	Ola4(j) undefined caseD_5a() Ola4(j) undefined caseD_5e() Ola4(j) undefined caseD_6c() Ola4(j) **** * **** FUN_0142:01a4(j), Olbd(j), FUN_01c0:01cb(c),
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20 undefined	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA **********************************	de: CALL_RETURN (CALL_TERMINATOR) ccaseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) ccaseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) ccaseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) ccaseD_e switchD_01a4::caseD_30 FUNCTION **PUNCTION **PUNCT	<pre>XREF[1]: XREF[1]: XREF[1]: XREF[4]:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** * * * * * * * * * * *
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20 undefined	switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA **********************************	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION ### FUNCTION ### FUNCTION ### CALL_RETURN (CALL_TERMINATOR) :caseD_10() ### CALL_RETURN (CALL_TERMINATOR) :caseD_10() **### CALL_RETURN (CALL_TERMINATOR) **## CALL_RETURN (CALL_TERMINATOR) **CASED_10 **# PORTA_RTC_IDB07 **CASED_12	<pre>XREF[1]: XREF[1]: XREF[1]: XREF[4]:</pre>	Ola4(j) undefined caseD_5a() Ola4(j) undefined caseD_5e() Ola4(j) undefined caseD_6c() Ola4(j) **** * **** FUN_0142:01a4(j), Olbd(j), FUN_01c0:01cb(c),
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20 undefined	switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA Flow Overri switchD_01a4: BRA **********************************	de: CALL_RETURN (CALL_TERMINATOR) ccaseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) ccaseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) ccaseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) ccaseD_e switchD_01a4::caseD_30 FUNCTION **FUNCTION **PORTA_RTC_IDB07	<pre>XREF[1]: XREF[1]: XREF[1]: XREF[4]:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** * * * * * * * * * * *
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20 undefined	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Low Overri switchD_0la4: BRA Low Overri switchD_0la4: AND	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION #UNCTION PORTA_RTC_IDB07 :caseD_10 PORTA_RTC_IDB07 :caseD_12 #0x3f	<pre>XREF{1}: XREF{1}: XREF{1}: XREF{4}: XREF[4]:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** * FUN_0142:01a4(j), 01bd(j), FUN_01c0:01cb(c), caseD_46:01f2(c) FUN_0142:01a4(j)
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20 undefined 01b7 b6 00 01b9 a4 3f	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA ********* * * ******** LDA switchD_0la4: AND switchD_0la4:	de: CALL_RETURN (CALL_TERMINATOR) ccaseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) ccaseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) ccaseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) ccaseD_e switchD_01a4::caseD_30 FUNCTION FUNCTION PORTA_RTC_IDB07 ccaseD_10 PORTA_RTC_IDB07 ccaseD_12 #0x3f ccaseD_14	<pre>XREF{1}: XREF{1}: XREF{1}: XREF{4}: XREF[4]:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** * * * * * * * * * * *
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20 undefined	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Low Overri switchD_0la4: BRA Low Overri switchD_0la4: AND	de: CALL_RETURN (CALL_TERMINATOR) ccaseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) ccaseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) ccaseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) ccaseD_e switchD_01a4::caseD_30 FUNCTION FUNCTION PORTA_RTC_IDB07 ccaseD_10 PORTA_RTC_IDB07 ccaseD_12 #0x3f ccaseD_14	<pre>XREF{1}: XREF{1}: XREF{1}: XREF{4}: XREF[4]:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** * FUN_0142:01a4(j), 01bd(j), FUN_01c0:01cb(c), caseD_46:01f2(c) FUN_0142:01a4(j)
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20 undefined 01b7 b6 00 01b9 a4 3f	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Low Overri switchD_0la4: BRA Low Overri switchD_0la4: AND switchD_0la4: AND switchD_0la4: CMP	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_C switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION #UNCTION PORTA_RTC_IDB07 :caseD_10 porta_RTC_IDB07 :caseD_12 #0x3f :caseD_14 #0x0	<pre>XREF[1]: XREF[1]: XREF[1]: XREF[4]: XREF[4]: XREF[1]:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** * **** FUN_0142:01a4(j), 01bd(j), caseD_46:01f2(c) FUN_0142:01a4(j) FUN_0142:01a4(j)
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20 undefined 01b7 b6 00 01b9 a4 3f 01bb a1 00	switchD_0la4: BRA Flow Overri switchD_0la4: CMP switchD_0la4: cMP	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION #D_10() <pre></pre>	<pre>XREF[1]: XREF[1]: XREF[1]: XREF[4]: XREF[4]: XREF[1]:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** * FUN_0142:01a4(j), 01bd(j), FUN_01c0:01cb(c), caseD_46:01f2(c) FUN_0142:01a4(j)
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20 undefined 01b7 b6 00 01b9 a4 3f	switchD_0la4: BRA Flow Overri switchD_0la4: CMP switchD_0la4: cMP	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_C switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION #UNCTION PORTA_RTC_IDB07 :caseD_10 porta_RTC_IDB07 :caseD_12 #0x3f :caseD_14 #0x0	<pre>XREF[1]: XREF[1]: XREF[1]: XREF[4]: XREF[4]: XREF[1]:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** * **** FUN_0142:01a4(j), 01bd(j), caseD_46:01f2(c) FUN_0142:01a4(j) FUN_0142:01a4(j)
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20 undefined 01b7 b6 00 01b9 a4 3f 01bb a1 00	switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA Flow Overri switchD_0la4: BRA ********** undefined case A:1 switchD_0la4: LDA switchD_0la4: AND switchD_0la4: CMP switchD_0la4: BNE	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION ### PUNCTION ### PUNCTION PORTA_RTC_IDB07 :caseD_10 porta_RTC_IDB07 :caseD_12 #0x3f :caseD_14 #0x0 :caseD_16 switchD_01a4::caseD_10	<pre>XREF[1]: XREF[1]: XREF[1]: XREF[4]: XREF[1]: XREF[1]: XREF[1]:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_6c() 01a4(j) **** * **** FUN_0142:01a4(j), 01bd(j), FUN_01c0:01cb(c), caseD_46:01f2(c) FUN_0142:01a4(j) FUN_0142:01a4(j) FUN_0142:01a4(j)
01af 20 50 01b1 20 52 01b3 20 5e 01b5 20 20 undefined 01b7 b6 00 01b9 a4 3f 01bb a1 00	switchD_0la4: BRA Flow Overri switchD_0la4: CMP switchD_0la4: cMP	de: CALL_RETURN (CALL_TERMINATOR) :caseD_8 switchD_01a4::caseD_5a de: CALL_RETURN (CALL_TERMINATOR) :caseD_a switchD_01a4::caseD_5e de: CALL_RETURN (CALL_TERMINATOR) :caseD_c switchD_01a4::caseD_6c de: CALL_RETURN (CALL_TERMINATOR) :caseD_e switchD_01a4::caseD_30 FUNCTION ### PUNCTION ### PUNCTION PORTA_RTC_IDB07 :caseD_10 porta_RTC_IDB07 :caseD_12 #0x3f :caseD_14 #0x0 :caseD_16 switchD_01a4::caseD_10	<pre>XREF[1]: XREF[1]: XREF[1]: XREF[4]: XREF[1]: XREF[1]: XREF[1]:</pre>	01a4(j) undefined caseD_5a() 01a4(j) undefined caseD_5e() 01a4(j) undefined caseD_6c() 01a4(j) **** * **** FUN_0142:01a4(j), 01bd(j), caseD_46:01f2(c) FUN_0142:01a4(j) FUN_0142:01a4(j)

```
FUNCTION
              undefined FUN_01c0()
              A:1 <RETURN>
caseD_1a (01c0+1)
undefined
                                                         XREF[4,1]: caseD_2a:01d9(c),
              FUN 01c0
                                                                    caseD_34:01eb(c),
caseD_56:01ff(c),
                                                                    caseD_5a:0203(c),
                                                                    FUN_0142:01a4(j)
01c0 cd 02 38
               JSR FUN_0238
              caseD_1e (01c3+2)
                                                         XREF[1,1]: FUN_0142:01a4(j),
              switchD_01a4::caseD_1c
                                                                    FUN_0142:01a4(j)
               JSR
01c3 cd 02 38
                        FUN_0238
                                                         XREF[11,1]: caseD 2a:01d4(j),
              LAB_01c6
                                                                    caseD_34:01db(j),
                                                                    caseD_46:01fb(j),
                                                                    caseD_5e:0205(j),
                                                                    caseD 5e:020d(j),
                                                                    caseD 5e:0211(j),
                                                                     caseD_6c:021f(j),
                                                                     caseD_6c:0227(j),
                                                                     caseD_6c:022b(j),
                                                                     caseD 6c:0232(i).
                                                                    caseD_6c:0236(j),
                                                                     FUN_0142:01a4(j)
01c6 b6 14
                LDA
                                                        XREF[0,1]: FUN_0142:01a4(j)
01c8 cd 02 3c
                JSR FUN_023c
              caseD_26 (01cb+2)
                                                         XREF[1,1]: FUN_0142:01a4(j),
              switchD_01a4::caseD_24
                                                                   FUN_0142:01a4(j)
01cb cd 01 b7
                           switchD 01a4::caseD 10
                 JSR
              caseD_28 (01ce+1)
                                                       XREF[1,1]: FUN_0142:019b(j),
                                                                    FUN_0142:01a4(j)
              01ce cc 01 1f
                                     FUNCTION
              undefined caseD_2a()
undefined
                A:1
                             <RETURN>
              caseD_2c (01d1+2)
                                                       XREF[2,1]: FUN_0142:01a4(j),
              switchD_01a4::caseD_2a
                                                                    FUN_0142:01a9(c),
                                                         FUN_0142:01a4(j)
01d1 08 14 03
                         0x4,ICHR,switchD_01a4::caseD_30
                                                  XREF[0,1]: FUN_0142:01a4(j)
              caseD_2e (01d4+1)
01d4 06 20 ef
                BRSET
                         0x3,DAT_0020,LAB_01c6
                                                        XREF[3]: FUN_0142:01a4(j),
FUN_0142:01b5(j), 01d1(j)
              switchD 01a4::caseD 30
                LDA
01d7 a6 01
                                               XREF[1]: FUN_0142:01a4(j)
              switchD_01a4::caseD_32
01d9 20 e5
              BRA FUN_01c0
-- Flow Override: CALL_RETURN (CALL_TERMINATOR)
                                     FUNCTION
              A:1 <RETURN
caseD_36 (01db+2)
switchD_01a4::caseD_34
undefined
                             <RETURN>
                                                        FUN_0142:01a4(j)
01db 0e 12 e8
                BRSET
              caseD_38 (01de+1)
                                                         XREF[0,1]: FUN_0142:01a4(j)
01de a6 02
                           #0x2
                 LDA
              caseD_3a (01e0+1)
                                                         XREF[0,1]: FUN 0142:01a4(j)
01e0 cd 02 38
                JSR
                        FUN_0238
                                                         XREF[1,1]: FUN_0142:01a4(j),
              caseD 3e (01e3+2)
              switchD 01a4::caseD 3c
                                                                  FUN_0142:01a4(j)
01e3 cd 02 38
                JSR FUN_0238
              caseD_40 (01e6+1)
                                                         XREF[0,1]: FUN_0142:01a4(j)
                JSR switchD_01a4::caseD_a4
01e6 cd 02 4b
              switchD 01a4::caseD 42
                                                         XREF[1]: FUN 0142:01a4(j)
```

01eb 20 d3	switchD_01a4::caseD_44 BRA FUN_01c0		<pre>FUN_0142:01a4(j) undefined FUN_01c0()</pre>
	Flow Override: CALL_RETURN (CALL_TERMINA		
	***********	*****	***
	* FUNCTION ************************************	*****	****
	undefined caseD_46()		
undefined	A:1 <return></return>		
	switchD_01a4::caseD_46	XREF[2]:	FUN_0142:01a4(j), FUN 0142:01ab(c)
01ed a6 02	LDA #0x2		
	D 4 (01 C10)	wppp// 11	TTT 01.40 01 4/1)
	caseD_4a (01ef+2) switchD 01a4::caseD 48	XKEF[1,1]:	FUN_0142:01a4(j), FUN 0142:01a4(j)
01ef cd 02 38	JSR FUN_0238		undefined FUN_0238()
01.00 1.01.17	caseD_4c (01f2+1)	XREF[0,1]:	FUN_0142:01a4(j)
0112 Cd 01 b/	JSR switchD_01a4::caseD_10		
	caseD_50 (01f5+2)	XREF[1,1]:	FUN_0142:01a4(j),
	switchD_01a4::caseD_4e		FUN_0142:01a4(j)
01f5 cd 02 38	JSR FUN_0238 caseD 52 (01f8+1)	XREF[0.1]:	undefined FUN_0238() FUN 0142:01a4(j)
01f8 cd 02 4b		(1,7-).	undefined caseD_a4()
01fb 20 c9	switchD_01a4::caseD_54 BRA LAB_01c6	XREF[1]:	FUN_0142:01a4(j)
	**********	*****	***
	* FUNCTION ************************************	*****	****
	undefined caseD_56()		
undefined	A:1 <return></return>	WDDD (0.)	TTT 0140 01 4/1)
	switchD_01a4::caseD_56	XKEF[2]:	FUN_0142:01a4(j), FUN 0142:01ad(c)
01fd a6 04	LDA #0x4		= '''
	switchD 01a4::caseD 58	VDPP[1].	FUN_0142:01a4(j)
01ff 20 bf	BRA FUN_01c0	AREF[I].	undefined FUN_01c0()
	Flow Override: CALL_RETURN (CALL_TERMINA		

	Flow Override: CALL_RETURN (CALL_TERMINA' * FUNCTION		****
	**************************************	******	*
undefined	**************************************	******	*
undefined	**************************************	******	*
	* FUNCTION *** undefined caseD_5a() A:1 <return> switchD_01a4::caseD_5a</return>	******	* ****
undefined	* FUNCTION undefined caseD_5a() A:1 <return></return>	******	* **** FUN_0142:01a4(j),
0201 a6 08	* FUNCTION undefined caseD_5a() A:1 <return> switchD_01a4::caseD_5a LDA #0x8 switchD_01a4::caseD_5c</return>	XREF[2]:	* **** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j)
	* FUNCTION * FUNCTION undefined caseD_5a() A:1	XREF[2]:	* **** FUN_0142:01a4(j), FUN_0142:01af(c)
0201 a6 08	* FUNCTION undefined caseD_5a() A:1 <return> switchD_01a4::caseD_5a LDA #0x8 switchD_01a4::caseD_5c</return>	XREF[2]:	* **** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j)
0201 a6 08	* FUNCTION undefined caseD_5a() A:1 <return> switchD_01a4::caseD_5a LDA #0x8 switchD_01a4::caseD_5c BRA FUN_01c0 Flow Override: CALL_RETURN (CALL_TERMINA)</return>	XREF[2]: XREF[1]: TOR)	* **** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j) undefined FUN_01c0()
0201 a6 08	**************************************	XREF[2]: XREF[1]: TOR)	* **** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j) undefined FUN_01c0()
0201 a6 08	* FUNCTION undefined caseD_5a() A:1 <return> switchD_01a4::caseD_5a LDA #0x8 switchD_01a4::caseD_5c BRA FUN_01c0 Flow Override: CALL_RETURN (CALL_TERMINATION) * FUNCTION</return>	XREF[2]: XREF[1]: TOR)	* **** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j) undefined FUN_01c0()
0201 a6 08	* FUNCTION undefined caseD_5a() A:1 <return> switchD_01a4::caseD_5a LDA #0x8 switchD_01a4::caseD_5c BRA FUN_01c0 Flow Override: CALL_RETURN (CALL_TERMINA) * FUNCTION undefined caseD_5e() A:1 <return></return></return>	XREF[2]: XREF[1]: TOR)	* **** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j) undefined FUN_01c0() **** * *
0201 a6 08 0203 20 bb	* FUNCTION undefined caseD_5a() A:1 <return> switchD_01a4::caseD_5a LDA #0x8 switchD_01a4::caseD_5c BRA FUN_01c0 Flow Override: CALL_RETURN (CALL_TERMINA) * FUNCTION undefined caseD_5e() A:1 <return> caseD_60 (0205+2)</return></return>	XREF[2]: XREF[1]: TOR)	* **** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j) undefined FUN_01c0()
0201 a6 08 0203 20 bb	*** FUNCTION undefined caseD_5a() A:1	XREF[2]: XREF[1]: TOR)	* **** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j) undefined FUN_01c0() **** * * * FUN_0142:01a4(j), FUN_0142:01b1(c), FUN_0142:01a4(j))
0201 a6 08 0203 20 bb	* FUNCTION undefined caseD_5a() A:1 <return> switchD_01a4::caseD_5a LDA #0x8 switchD_01a4::caseD_5c BRA FUN_01c0 Flow Override: CALL_RETURN (CALL_TERMINA) * FUNCTION undefined caseD_5e() A:1 <return> caseD_60 (0205+2) switchD_01a4::caseD_5e BRCLR 0x6,ICLR,LAB_01c6</return></return>	XREF[2]: XREF[1]: TOR) XREF[2,1]:	**** FUN_0142:01a4(j), FUN_0142:01a4(j) undefined FUN_01c0() **** * FUN_0142:01a4(j), FUN_0142:01b1(e), FUN_0142:01a4(j) = FFh
0201 a6 08 0203 20 bb	### FUNCTION undefined caseD_5a() A:1	XREF[2]: XREF[1]: TOR) XREF[2,1]: XREF[0,1]:	* **** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j) undefined FUN_01c0() **** * * * FUN_0142:01a4(j), FUN_0142:01b1(c), FUN_0142:01a4(j))
0201 a6 08 0203 20 bb undefined	### FUNCTION undefined caseD_5a() A:1	XREF[2]: XREF[1]: TOR) XREF[2,1]: XREF[0,1]:	**** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j) undefined FUN_01c0() **** * FUN_0142:01a4(j), FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh
0201 a6 08 0203 20 bb undefined 0205 0d 15 be 0208 0a 14 04	**************************************	XREF[2]: XREF[1]: TOR) XREF[2,1]: XREF[0,1]:	* **** FUN_0142:01a4(j), FUN_0142:01a4(j) undefined FUN_01c0() **** * FUN_0142:01a4(j), FUN_0142:01a4(j), FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh FUN_0142:01a4(j)
0201 a6 08 0203 20 bb undefined	### FUNCTION undefined caseD_5a() A:1	XREF[2]: XREF[1]: TOR) XREF[2,1]: XREF[0,1]:	**** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j) undefined FUN_01c0() **** * FUN_0142:01a4(j), FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh
0201 a6 08 0203 20 bb undefined 0205 0d 15 be 0208 0a 14 04 020b 1a 14	## FUNCTION undefined caseD_5a() A:1	XREF[2]: XREF[1]: TOR) XREF[2,1]: XREF[0,1]:	* **** FUN_0142:01a4(j), FUN_0142:01a4(j) undefined FUN_01c0() **** * FUN_0142:01a4(j), FUN_0142:01a4(j), FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh FUN_0142:01a4(j)
0201 a6 08 0203 20 bb undefined 0205 0d 15 be 0208 0a 14 04	### FUNCTION undefined caseD_5a() A:1	XREF[2]: XREF[1]: XREF[2,1]: XREF[0,1]: XREF[1]:	* **** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j) undefined FUN_01c0() **** * * * * * * * * * * * * * * *
0201 a6 08 0203 20 bb undefined 0205 0d 15 be 0208 0a 14 04 020b 1a 14	## FUNCTION undefined caseD_5a() A:1	XREF[2]: XREF[1]: XREF[2,1]: XREF[0,1]: XREF[1]:	* **** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j) undefined FUN_01c0() **** * * * * * * * * * * * * * * *
0201 a6 08 0203 20 bb undefined 0205 0d 15 be 0208 0a 14 04 020b 1a 14	* FUNCTION undefined caseD_5a() A:1	XREF[2]: XREF[1]: TOR) XREF[2,1]: XREF[0,1]: XREF[1]: XREF[1]:	**** FUN_0142:01a4(j), FUN_0142:01a4(j) UN_0142:01a4(j) UN_0142:01a4(j), FUN_0142:01a4(j), FUN_0142:01a4(j) = FFh
0201 a6 08 0203 20 bb undefined 0205 0d 15 be 0208 0a 14 04 020b 1a 14	## FUNCTION undefined caseD_5a() A:1	XREF[2]: XREF[1]: TOR) XREF[2,1]: XREF[0,1]: XREF[1]: XREF[1]: XREF[2]:	**** FUN_0142:01a4(j), FUN_0142:01a4(j) undefined FUN_01c0() **** * FUN_0142:01a4(j), FUN_0142:01a4(j), FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh
0201 a6 08 0203 20 bb undefined 0205 0d 15 be 0208 0a 14 04 020b 1a 14	## FUNCTION undefined caseD_5a() A:1	XREF[2]: XREF[1]: TOR) XREF[2,1]: XREF[0,1]: XREF[1]: XREF[1]:	* **** FUN_0142:01a4(j), FUN_0142:01af(c) FUN_0142:01a4(j) undefined FUN_01c0() **** * * * * * * * * * * * * * * *

	***********	******	***
	* FUNCTION ************************************		*
	undefined caseD_6c()		
undefined	A:1 <return> switchD_01a4::caseD_6c</return>	XREF[3]:	FUN_0142:01a4(j),
0213 b6 00	LDA PORTA_RTC_IDB07		FUN_0142:01b3(c), 021d(j)
0215 a4 3f	switchD_01a4::caseD_6e AND #0x3f	XREF[1]:	FUN_0142:01a4(j)
0217 a1 21	switchD_01a4::caseD_70 CMP #0x21	XREF[1]:	FUN_0142:01a4(j)
0219 27 12	<pre>switchD_01a4::caseD_72 BEQ</pre>	XREF[1]:	FUN_0142:01a4(j)
021b a1 00	switchD_01a4::caseD_74 CMP #0x0	XREF[1]:	FUN_0142:01a4(j)
021d 26 f4	<pre>switchD_01a4::caseD_76 BNE</pre>	XREF[1]:	FUN_0142:01a4(j)
	caseD_7a (021f+2) switchD_01a4::caseD_78		FUN_0142:01a4(j), FUN_0142:01a4(j)
021f 0d 15 a4			= FFh
	caseD_7c (0222+1) BRSET 0x6,ICHR,switchD_01a4::caseD_82		FUN_0142:01a4(j) = FFh
0225 1c 14	switchD_01a4::caseD_7e BSET 0x6,ICHR	XREF[1]:	FUN_0142:01a4(j) = FFh
0227 20 9d	switchD_01a4::caseD_80 BRA LAB_01c6	XREF[1]:	FUN_0142:01a4(j)
0229 1d 14	switchD_01a4::caseD_82 BCLR 0x6,ICHR	XREF[2]:	FUN_0142:01a4(j), 0222(j) = FFh
022b 20 99	switchD_01a4::caseD_84 BRA LAB_01c6	XREF[1]:	FUN_0142:01a4(j)
	<pre>caseD_88 (022d+2) switchD_01a4::caseD_86 BRSBT</pre>	XREF[2,1]:	FUN_0142:01a4(j), 0219(j), FUN_0142:01a4(j) = FFFh
	caseD_8a (0230+1)	XREF[0,1]:	FUN_0142:01a4(j)
0230 18 14	BSET 0x4,ICHR caseD 8c (0232+1)	VDEE(A 11.	= FFh FUN_0142:01a4(j)
0232 20 92	BRA LAB_01c6		100_011210141()/
0234 19 14	caseD_8e (0234+1) LAB_0234 BCLR 0x4,ICHR	XREF[1,1]:	022d(j), FUN_0142:01a4(j) = FFh
	caseD_90 (0236+1)	XREF[0,1]:	FUN_0142:01a4(j)
0236 20 8e	BRA LAB_01c6		
	***********	*****	***
	* FUNCTION ************************************	*****	
	undefined FUN_0238()		
undefined	A:1 <return> caseD_92 (0238+1) FUN_0238</return>	XREF[6,1]:	FUN_01c0:01c0(c), FUN_01c0:01c3(c), caseD_34:01e0(c), caseD_34:01e3(c), caseD_46:01ef(c), caseD_46:01ef(c), FUN_0142:01a4(j)
0238 ba 14	ORA ICHR caseD 94 (023a+1)	XREF(0.1)	= FFh FUN 0142:01a4(j)
023a a4 7f	AND #0x7f	AREF[0,1].	101-01-12.014-())
	* FUNCTION ***********************************		*
undefined	A:1 <return></return>	VDPP[2 11-	DESET.00f5(a) FIN 01-0.01-0/-1
023c 11 02	caseD_96 (023c+1) FUN_023c BCLR 0x0,PORTC STAT DISP	AKEF[2,1]:	RESET:00f5(c), FUN_01c0:01c8(c), FUN_0142:01a4(j) = FFh
	caseD_98 (023e+1)	XREF[0,1]:	FUN_0142:01a4(j)
023e b7 01	STA PORTB_ND_SIGNALS caseD 9a (0240+1)	YREF(A 11.	= FFh FUN 0142:01a4(j)
0240 10 02	BSET 0x0, PORTC_STAT_DISP	ARE [U, 1]:	= FFh

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0242 a6 20	caseD_9c (0242+1)	XREF[0,1]:	FUN_0142:01a4(j)
0242 a6 20	LDA #0x20		
	LAB 0244	XREF[2]:	0245(j), 0248(j)
0244 5a	DECX		
	switchD_01a4::caseD_9e	XREF[1]:	FUN_0142:01a4(j)
0245 26 fd	BNE LAB_0244		
	switchD 01a4::caseD a0	XREF[1]:	FUN 0142:01a4(j)
0247 4a	DECA		
	caseD_a2 (0248+1)	XREF[0,1]:	FUN_0142:01a4(j)
0248 26 fa	BNE LAB_0244		
024a 81	RTS		
	*********	*****	***
	* FUNCTION		*
	***********	******	***
1.61	undefined caseD_a4() A:1 <return></return>		
undefined	A:1 <return> switchD 01a4::caseD a4</return>	XREF[4]:	RESET:0102(c), FUN_0142:01a4(j),
			caseD_34:01e6(c),
			caseD_46:01f8(c)
024b a6 06	LDA #0x6		
	switchD_01a4::caseD_a6	VDPP[1].	FUN_0142:01a4(j)
024d b7 1e	STA DAT 001e	ARDE [1].	= FFh
	=11.1		
	switchD_01a4::caseD_a8	XREF[1]:	FUN_0142:01a4(j)
024f a6 30	LDA #0x30		
	switchD 01a4::caseD aa	VDPP(1).	EUN 0140-01-44-)
0251 b7 4c		AREF[1]:	<pre>FUN_0142:01a4(j) = FFh</pre>
	= 11 1		
	switchD_01a4::caseD_ac	XREF[4]:	<pre>FUN_0142:01a4(j), 0254(j),</pre>
			0257(j), 025b(j)
0253 5a	DECX caseD_ae (0254+1)	XREF[0.1].	FUN 0142:01a4(j)
0254 26 fd	BNE switchD_01a4::caseD_ac	11121 (0/1)	1011_011210141()/
0256 4a	DECA		
0057.06.6	switchD_01a4::caseD_b0	XREF[1]:	FUN_0142:01a4(j)
0257 26 fa	BNE switchD_01a4::caseD_ac		
	switchD_01a4::caseD_b2	XREF[1]:	FUN_0142:01a4(j)
0259 3a 1e	DEC DAT_001e		= FFh
		WDDD(1)	TTT 0140 01 4/1)
025b 26 f6	<pre>switchD_01a4::caseD_b4 BNE</pre>	AREF[I]:	FUN_0142:01a4(j)
	switchD_01a4::caseD_b6	XREF[1]:	FUN_0142:01a4(j)
025d 81	RTS		
	**********	******	***
	* FUNCTION		*
	**********	*****	***
1.61	undefined FUN_025e()		
undefined	A:1 <return> caseD_b8 (025e+1)</return>	XREF[1.1]:	FUN 011f:013f(c),
	FUN_025e		FUN_0142:01a4(j)
025e b6 14	LDA ICHR		= FFh
	caseD_ba (0260+1)	XREF[0,1]:	FUN_0142:01a4(j)
0260 b7 01	STA PORTB_ND_SIGNALS caseD bc (0262+1)	XREF[0.11.	= FFh FUN 0142:01a4(j)
0262 a6 00	LDA #0x0		0112.0141()/
	caseD_be (0264+1)	XREF[0,1]:	FUN_0142:01a4(j)
0264 b7 08	STA DAT_0008		= FFh
	caseD c0 (0266+1)	VDPP[1 1].	026e(j), FUN 0142:01a4(j)
	LAB 0266	AREF[I,I].	020e(j), ron_0142.0144(j)
0266 2f 0a	BIH LAB_0272		
	caseD_c2 (0268+1)	XREF[0,1]:	FUN_0142:01a4(j)
0268 b6 00	LDA PORTA_RTC_IDB07	wannin 11	PIDI 0140-01 4/1
026a a4 3f	caseD_c4 (026a+1) AND #0x3f	XREF[0,1]:	FUN_0142:01a4(j)
1204 41 51	caseD_c6 (026c+1)	XREF[0,1]:	FUN_0142:01a4(j)
026c a1 00	CMP #0×0		_
	caseD_c8 (026e+1)	XREF[0,1]:	FUN_0142:01a4(j)
026e 27 f6	BEQ LAB_0266 caseD ca (0270+1)	עסקקוח 11.	FUN 0142:01a4(j)
0270 20 20	BRA LAB_0292	AMERIU, I]:	
	_		
	caseD_cc (0272+1)	XREF[1,1]:	0266(j), FUN_0142:01a4(j)
0070 6 00	LAB_0272		
0272 a6 00	LDA #0x0		

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	0274 b	' 1a	caseD_ce (0274+		XREF[0,1]:	FUN_0142:01a4(j) = FFh
			caseD_d0 (0276+	1)	XREF[0,1]:	FUN_0142:01a4(j)
	0276 12	. 02	BSET caseD_d2 (0278+	0x1,PORTC_STAT_DISP	XREF[0,1]:	= FFh FUN_0142:01a4(j)
	0278 11	. 02	BCLR caseD d4 (027a+	0x0,PORTC_STAT_DISP	XREF(0.1):	= FFh FUN 0142:01a4(j)
	027a 10	02	BSET	0x0,PORTC_STAT_DISP		= FFh
	027c be	4c	caseD_d6 (027c+ LDA		XREF[0,1]:	FUN_0142:01a4(j) = FFh
	007 15		caseD_d8 (027e+	1)	XREF[0,1]:	FUN_0142:01a4(j)
	027e b		caseD_da (0280+	1)	XREF[0,1]:	= FFh FUN_0142:01a4(j)
	0280 a6	05	LDA	#0x5		
			caseD_dc (0282+	1)		0285(j), 0288(j), 028c(j),
	0282 26	11	LAB_0282 BIL	switchD_01a4::caseD_ee		FUN_0142:01a4(j)
	0284 5		DECX			
			switchD_01a4::	caseD_de	XREF[1]:	FUN_0142:01a4(j)
	0285 26	fb	BNE	LAB_0282		
	0287 4a		switchD_01a4::	caseD_e0	XREF[1]:	FUN_0142:01a4(j)
			caseD_e2 (0288+		XREF[0,1]:	FUN_0142:01a4(j)
	0288 26	f8	BNE caseD_e4 (028a+		XREF[0,1]:	FUN_0142:01a4(j)
	028a 3a	1e	DEC caseD_e6 (028c+	DAT_001e		= FFh
	028c 26	f4	BNE		AREF[U,I]:	FUN_0142:01a4(j)
	028e a6	01	caseD_e8 (028e+ LDA		XREF[0,1]:	FUN_0142:01a4(j)
	0290 b		caseD_ea (0290+	1)	XREF[0,1]:	FUN_0142:01a4(j) = FFh
	0290 D	4C	STA	DAT_004C		
			caseD_ec (0292+ LAB 0292	1)	XREF[1,1]:	0270(j), FUN_0142:01a4(j)
	0292 co	01 42	JMP			undefined FUN_0142()
			Flow Overrid			
			switchD_01a4::	caseD ee	XREF[21:	FUN 0142:01a4(j), 0282(j)
	0295 as	0.8				= -
	0295 ae	. 08	LDX	#0x8		_
	0295 ae	. 08		#0x8		FUN_0142:01a4(j), 02c6(j), 02dc(j)
		08	LDX switchD_01a4::	#0x8		FUN_0142:01a4(j), 02c6(j),
	0297 be	i 00	LDX switchD_01a4:: LDA switchD_01a4::	<pre>#0x8 caseD_f0 portA_rtc_idb07 caseD_f2</pre>	XREF[3]:	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j)
		i 00	LDX switchD_01a4:: LDA switchD_01a4::	#0x8 caseD_f0 porta_rtc_idb07	XREF[3]:	FUN_0142:01a4(j), 02c6(j), 02dc(j)
	0297 be	00	LDX switchD_01a4:: LDA switchD_01a4:: BCLR switchD_01a4::	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4	<pre>XREF[3]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j)
	0297 b6	00	LDX switchD_01a4:: LDA switchD_01a4:: BCLR switchD_01a4:: ESET	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP	<pre>XREF[3]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh
	0297 b6	00 00 02 02	LDX switchD_01a4:: LDA switchD_01a4:: BCLR switchD_01a4::	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6	<pre>XREF[3]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh FUN_0142:01a4(j)
	0297 be 0299 13	00 00 02 02	LDX switchD_01a4:: LDA switchD_01a4:: BCLR switchD_01a4:: BSET switchD_01a4:: STA	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh
	0297 be 0299 13	02 02 20	LDX switchD_01a4:: LDA switchD_01a4:: BCLR switchD_01a4:: BSET switchD_01a4:: STA switchD_01a4::	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh FUN_0142:01a4(j)
	0297 b6 0299 13 029b 12	02 02 20	LDX switchD_01a4:: LDA switchD_01a4:: BCLR switchD_01a4:: BSET switchD_01a4:: STA switchD_01a4::	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFH FUN_0142:01a4(j) = FFH FUN_0142:01a4(j) = FFH FUN_0142:01a4(j)
	0297 b6 0299 13 029b 12	02 02 20 21	LDX switchD_01a4:: LDA switchD_01a4:: BCLR switchD_01a4:: SSTA switchD_01a4:: LSR	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh FUN_0142:01a4(j) = FFh
	0297 be 0299 13 029b 12 029d bi 029f 34 02al 34	00 02 02 20 21 22	LDX switchD_01a4:: LDA switchD_01a4:: BCLR switchD_01a4:: STA switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: switchD_01a4:: switchD_01a4:: switchD_01a4:: switchD_01a4:: switchD_01a4::	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fc	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j), 02dc(j) FUN_0142:01a4(j) = FFh
	0297 be 0299 13 029b 12 029d bi 029f 34 02al 34	02 02 20 21	LDX switchD_01a4:: LDA switchD_01a4:: BCLR switchD_01a4:: SSTA switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: LSR	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fa DAT_0022	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh
	0297 be 0299 13 029b 12 029d b 029f 34 02a1 34 02a3 34	000 002 002 002 002 002 002 002 002 002	LDX switchD_01a4:: BCLR switchD_01a4:: BSET switchD_01a4:: STA switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: STA switchD_01a4:: STA switchD_01a4:: STA switchD_01a4:: STA switchD_01a4:: STA	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fa DAT_0022 caseD_fc DAT_0023 caseD_fe	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh
	0297 be 0299 13 029b 12 029d b 029f 34 02a1 34 02a3 34	20 04	LDX switchD_01a4:: LDA switchD_01a4:: ECLR switchD_01a4:: STA switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: STA	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fa DAT_0022	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j)
	0297 b6 0299 13 029b 12 029d b7 029f 34 02a1 34 02a3 36	00 00 00 00 00 00 00 00 00 00 00 00 00	LDX switchD_01a4:: LDA switchD_01a4:: ECLR switchD_01a4:: STA switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: BSET switchD_01a4:: STA	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fc DAT_0023 caseD_fe 0x0,DAT_0020,LAB_02ac	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh
	0297 b6 0299 13 029b 12 029d b7 029f 36 02a1 36 02a3 36 02a8 16 02aa 26	20 04 21 02	LDX switchD_01a4:: LDA switchD_01a4:: ECLR switchD_01a4:: STA switchD_01a4:: LSR	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fc DAT_0023 caseD_fe 0x0,DAT_0020,LAB_02ac 0x7,DAT_0021 LAB_02ae	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh O2a5(j)
	0297 b6 0299 13 029b 12 029d b1 029f 34 02a1 34 02a3 34 02a5 00 02a8 16	20 04 21 02	LDX switchD_01a4:: LDA switchD_01a4:: ECLR switchD_01a4:: STA switchD_01a4:: LSR	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fc DAT_0023 caseD_fe 0x0,DAT_0020,LAB_02ac 0x7,DAT_0021	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh
	0297 b6 0299 13 029b 12 029d b1 029f 34 02a1 34 02a3 36 02a8 16 02aa 26 02ac 15	20 04 21 02	LDX switchD_01a4:: LDA switchD_01a4:: BCLR switchD_01a4:: STA switchD_01a4:: LSR	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fc DAT_0023 caseD_fe 0x0,DAT_0020,LAB_02ac 0x7,DAT_0021 LAB_02ae 0x7,DAT_0021	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh O142:01a4(j) = FFh O2a5(j) = FFh O2aa(j)
	0297 b6 0299 13 029b 12 029d b1 029f 34 02a1 34 02a3 36 02a8 16 02aa 26 02ac 1f	20 04 21 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 24	LDX switchD_01a4:: ECLR switchD_01a4:: BSET switchD_01a4:: STA switchD_01a4:: LSR switchD_01a4:: ESR switchD_01a4:: ESR switchD_01a4:: LSR switchD_01a4:: LSR	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fc DAT_0022 caseD_fe 0x0,DAT_0020,LAB_02ac 0x7,DAT_0021 LAB_02ae 0x7,DAT_0021 0x1,DAT_0020,LAB_02b5	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh O2a5(j) = FFh O2aa(j) = FFh
	0297 b6 0299 13 029b 12 029d b1 029f 34 02a1 34 02a3 36 02a8 16 02aa 26 02ac 15	20 04 21 02 04 21 02 04 22 04 22 04 22 04	LDX switchD_01a4:: ECLR switchD_01a4:: BSET switchD_01a4:: STA switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: ERRET BRA LAB_02ac BCLR LAB_02ac BRSET BRSET BRSET BRSET BRET	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fc DAT_0023 caseD_fe 0x0,DAT_0020,LAB_02ac 0x7,DAT_0021 LAB_02ae 0x7,DAT_0021	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh O142:01a4(j) = FFh O2a5(j) = FFh O2aa(j)
	0297 b6 0299 13 029b 12 029d b5 029f 34 02a1 36 02a3 34 02a5 00 02a8 16 02aa 20 02ac 15 02ae 02 02b1 16	20 04 21 02 04 21 02 04 22 04 22 04 22 04	LDX switchD_01a4:: BCLR switchD_01a4:: BSET switchD_01a4:: STA switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: BSET BSET BRA LAB_02ac BCLR LAB_02ac BCLR LAB_02ac BCLR LAB_02ac BRSET BSET BSET BSET BSET BSET BSET BSET B	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fc DAT_0023 caseD_fe 0x0,DAT_0020,LAB_02ac 0x7,DAT_0021 LAB_02ae 0x7,DAT_0021 0x1,DAT_0020,LAB_02b5 0x7,DAT_0022	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh O142:01a4(j) = FFh O2a5(j) = FFh O2a6(j) = FFh O2a6(j) = FFH
	0297 b6 0299 13 029b 12 029d b7 029f 34 02a1 34 02a3 36 02a8 16 02aa 26 02ac 15 02ae 02 02b1 16 02b3 26	20 04 21 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 24	LDX switchD_01a4:: LDA switchD_01a4:: BCLR switchD_01a4:: STA switchD_01a4:: LSR switchD_01a4:: BRSET BRA LAB_02ac BCLR	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fe 0x0,DAT_0023 caseD_fe 0x7,DAT_0021 LAB_02ae 0x7,DAT_0021 0x1,DAT_0020,LAB_02b5 0x7,DAT_0022 LAB_02b7	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh O2a5(j) = FFh 02a6(j) = FFh 02ac(j)
	0297 b6 0299 13 029b 12 029d b5 029f 34 02a1 36 02a3 34 02a5 00 02a8 16 02aa 20 02ac 15 02ae 02 02b1 16	20 04 21 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 22 24 24	LDX switchD_01a4:: BCLR switchD_01a4:: BSET switchD_01a4:: STA switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: BSET BSET BRA LAB_02ac BCLR LAB_02ac BCLR LAB_02ac BCLR LAB_02ac BRSET BSET BSET BSET BSET BSET BSET BSET B	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fe 0x0,DAT_0023 caseD_fe 0x7,DAT_0021 LAB_02ae 0x7,DAT_0021 0x1,DAT_0020,LAB_02b5 0x7,DAT_0022 LAB_02b7	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh O142:01a4(j) = FFh O2a5(j) = FFh O2a6(j) = FFh O2a6(j) = FFH
	0297 b6 0299 13 029b 12 029d b5 029f 36 02a1 36 02a2 36 02a8 16 02a2 15 02a2 15 02a2 02b5 15	20 04 21 22 24 22 22 22 22 22	LDX switchD_01a4:: ECLR switchD_01a4:: BSET switchD_01a4:: STA switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: ERSET BRA LAB_02ac BCLR LAB_02ac	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fe 0x0,DAT_0023 caseD_fe 0x7,DAT_0021 LAB_02ae 0x7,DAT_0021 0x1,DAT_0020,LAB_02b5 0x7,DAT_0022 LAB_02b7	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh O142:01a4(j) = FFh O2a5(j) = FFh O2a6(j) = FFh O2ac(j) = FFh
	0297 b6 0299 13 029b 12 029d b5 029f 36 02a1 36 02a2 36 02a8 16 02a2 15 02a2 15 02a2 02b5 15	20 04 21 22 24 24	LDX switchD_01a4:: ECLR switchD_01a4:: BSET switchD_01a4:: STA switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: ESR switchD_01a4:: LSR switchD_01a4:: LSR switchD_01a4:: ERA LAB_02ac BCLR LAB_02ac BRSET BRA	#0x8 caseD_f0 PORTA_RTC_IDB07 caseD_f2 0x1,PORTC_STAT_DISP caseD_f4 0x1,PORTC_STAT_DISP caseD_f6 DAT_0020 caseD_f8 DAT_0021 caseD_fa DAT_0022 caseD_fe 0x0,DAT_0023 caseD_fe 0x7,DAT_0021 LAB_02ae 0x7,DAT_0021 0x1,DAT_0020,LAB_02b5 0x7,DAT_0022 LAB_02b7	<pre>XREF[3]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]: XREF[1]:</pre>	FUN_0142:01a4(j), 02c6(j), 02dc(j) FUN_0142:01a4(j) = FFh O142:01a4(j) = FFh O2a5(j) = FFh O2a6(j) = FFh O2ac(j) = FFh

02bc 20 02	BRA	LAB_02c0		
	LAB_02be		XREF[1]:	02b7(j)
02be 1f 23	BCLR	0x7,DAT_0023		= FFh
	LAB_02c0		XREF[1]:	02bc(j)
02c0 3c 1a	INC	ACHR		= FFh
02c2 b6 1a	LDA	ACHR		= FFh
02c4 a1 08	CMP	#0x8		
02c6 26 cf	BNE	switchD_01a4::caseD_f0		
02c8 a6 00	LDA	#0×0		
02ca b7 1a	STA	ACHR		= FFh
02cc 5a	DECX			
02cd b6 21	LDA	DAT 0021		= FFh
02cf e7 2d	STA	0x2d, X=>DAT 0034		= FFh
02d1 b6 22	LDA	DAT 0022		= FFh
02d3 e7 35	STA	0x35,X=>DAT 003c		= FFh
02d5 b6 23	LDA	DAT 0023		
02d7 e7 3d	STA	0x3d, X=>DAT 0044		= FFh
02d9 9f	TXA			
02da a1 00	CMP	#0×0		
02dc 26 b9	BNE	switchD 01a4::caseD f0		
02de b6 19	LDA	CLR		= FFh
02e0 b1 18	CMP	CHR		
02e2 27 09	BEQ	LAB 02ed		
02e4 b7 18	STA	CHR		= FFh
02e6 cd 06 18	JSR	FUN 0618		
02e9 a6 00	LDA	#0×0		
02eb b7 47	STA	DAT 0047		
		-		
	LAB 02ed		XREF[1]:	02e2(j)
02ed a6 00	LDA	#0×0		-
02ef b7 19	STA	CLR		= FFh
02f1 ae 00	LDX	#0×0		
02f3 e6 2d	LDA	DAT 002d, X		= FFh
02f5 b7 1b	STA	ACLR		= FFh
02f7 5c	INCX			
02f8 e6 2d	LDA	0x2d, X=>DAT 002e		= FFh
02fa b7 1c	STA	DAT_001c		= FFh
02fc cd 06 41	JSR	- FUN 0641		undefined FUN 0641()
02ff b7 1d	STA	 DAT 001d		= FFh
0301 a1 00	CMP	#0×0		
0303 26 03	BNE	LAB 0308		
0305 cc 04 1d	JMP	FUN_041d		undefined FUN 041d()
	Flow Overri	de: CALL_RETURN (CALL_TERMINATOR)		
	LAB_0308		XREF[1]:	0303(j)
0308 ae 01	LDX	#0×1		
030a b6 1d	LDA	DAT_001d		= FFh
030c bf 1f	STX	DAT_001f		= FFh
	LAB_030e		XREF[1]:	0325(j)
030e 54	LSRX			
030f e7 25	STA	DAT_0025, X		= FFh
0311 be 1f	LDX	DAT_001f		= FFh
0313 5c	INCX			
0314 e6 2d	LDA	0x2d, X=>DAT_002f		= FFh
0316 b7 1b	STA	ACLR		= FFh
0318 5c	INCX			
0319 9f	TXA			
031a a1 09	CMP	#0×9		
031c 27 09	BEQ	LAB_0327		
031e e6 2d	LDA	0x2d, X=>DAT_0030		= FFh
0320 b7 1c	STA	DAT_001c		= FFh
0322 cd 06 41		FUN_0641		undefined FUN_0641()
0325 20 e7	BRA	LAB_030e		
	LAB 0327		XREF[1]:	031c(j)
0327 0a 18 1f	BRSET	0x5,CHR,LAB 0349		0316(J) = FFh
032a ae 03	LDX	#0×3		
	LAB 032c		XREF[1]:	0335(j)
032c e6 25	LDA	0x25,X=>DAT 0028		= FFh
032e e7 29	STA	0x29, X=>DAT 002c		= FFh
0330 al 20	CMP	#0×20		
0332 26 6e	BNE	LAB 03a2		
0334 5a	DECX	_		
0335 2a f5	BPL	LAB 032c		
0337 a6 00	LDA	#0×0		
0339 b7 47	STA	DAT 0047		= FFh
033b a6 60	LDA	#0×60		
033d b7 45	STA	DAT_0045		= FFh
033f la 19	BSET	0x5,CLR		= FFh
0341 Oe 3a 02	BRSET	0x7,DAT_003a,LAB_0346		= FFh
0344 20 65	BRA	LAB_03ab		

	LAB 0346		VDDD[2].	0341(j), 0356(j), 039d(j)
0346 cc 04 d6		LAB 04d6	AREF[3]:	0341(j), 0336(j), 039d(j)
	LAB_0349		XREF[1]:	0327(j)
0349 ae 03	LDX	#0×3		
034b 1a 19	BSET	0x5,CLR		= FFh
	LAB 034d		XREF[1]:	0254(5)
034d e6 25	LAB_U34d LDA	0x25, X=>DAT 0028	AREF[I];	0354(J) = FFh
034f e1 29	CMP	0x29,X=>DAT 002c		= FFh
0351 26 08	BNE	LAB_035b		
0353 5a	DECX			
0354 2a f7	BPL	LAB_034d		
0356 0e 3a ed		0x7,DAT_003a,LAB_0346		= FFh
0359 20 50	BRA	LAB_03ab		
	LAB 035b		XREF[1]:	0351(i)
035b ae 01	LDX	#0×1		0001()/
035d b6 25	LDA	DAT 0025		= FFh
		_		
	LAB_035f		XREF[1]:	
035f e1 29	CMP	0x29, X=>DAT_002a		= FFh
0361 27 06	BEQ	LAB_0369		
0363 5c	INCX			
0364 9f 0365 a1 04	TXA	#04		
0365 al 04 0367 26 f6	CMP BNE	#0x4 LAB 035f		
0307 20 10	DNE	LAB_0331		
	LAB 0369		XREF[1]:	0361(i)
0369 bf 1f	STX	DAT 001f		= FFh
036b a6 04	LDA	#0×4		
036d b0 1f	SUB	DAT_001f		= FFh
036f 97	TAX			
0070 16 47	LAB_0370 LDA	D3.00.4.7	XREF[1]:	038f(j) = FFh
0370 b6 47 0372 a1 28	CMP	DAT_0047 #0x28		
0374 26 04	BNE	LAB 037a		
0376 a6 00	LDA	#0×0		
0378 b7 47	STA	DAT 0047		= FFh
		_		
	LAB_037a		XREF[1]:	0374(j)
037a a6 18	LDA	#0x18		
037c cd 05 fc		FUN_05fc		
037f 3c 47	INC	DAT_0047		= FFh
0381 a6 27 0383 cd 06 90	LDA JSR	#0x27 FUN 0690		
0385 cd 00 90 0386 e6 25	LDA	0x25, X=>DAT 0027		= FFh
0388 cd 05 d2		FUN 05d2		
038b 5c	INCX	_		
038c 9f	TXA			
038d a1 04	CMP	#0×4		
038f 26 df	BNE	LAB_0370		
0391 ae 00	LDX	#0×0		
	LAB 0393		XREF[1]:	0305(4)
0393 e6 25	LDA	DAT 0025,X	AREF[I].	= FFh
0395 e7 29	STA	DAT 0029, X		= FFh
0397 5c	INCX			
0398 9f	TXA			
0399 a1 04	CMP	#0×4		
039b 26 f6	BNE	LAB_0393		
039d 0e 3a a6		0x7,DAT_003a,LAB_0346		= FFh
03a0 20 09	BRA	LAB_03ab		
	LAB 03a2		XREF[1]:	0222751
03a2 a6 00	LDA	#0x0	AREF[I].	0332())
03a4 b7 45	STA	DAT 0045		= FFh
03a6 a6 40	LDA	#0×40		
03a8 cd 06 90	JSR	FUN_0690		
	LAB_03ab		XREF[3]:	0344(j), 0359(j), 03a0(j)
03ab b6 45	LDA	DAT_0045		= FFh
03ad cd 06 90 03b0 12 19	JSR	FUN_0690		
03b0 12 19 03b2 0a 19 12	BSET	0x1,CLR 0x5,CLR,LAB 03c7		= FFh = FFh
03b2 0a 19 12 03b5 b6 25	LDA	DAT 0025		= FFh
03b3 b0 23 03b7 a1 20	CMP	#0x20		
03b9 27 07	BEQ	LAB_03c2		
03bb ae b5	LDX	#0xb5		
03bd cd 06 0c	JSR	FUN_060c		
03c0 20 05	BRA	LAB_03c7		

				001.0.11
02-2	LAB_03c2	#0b	XREF[1]:	03b9(j)
03c2 ae be 03c4 cd 06 0c	LDX JSR	#0xbe FUN 060c		
0301 04 00 00	001	100_0000		
	LAB_03c7		XREF[2]:	03b2(j), 03c0(j)
03c7 a6 35	LDA	#0x35		
03c9 97	TAX			
03ca cd 03 cf	JSR	FUN_03cf		
03cd 20 25	BRA	FUN_03f4		undefined FUN_03f4()
	Flow Over:			
	******	********	*****	****
	*	FUNCTION		*
	******	********	*****	****
	undefined FU	N_03cf()		
undefined	A:1	<return></return>		
	FUN_03cf		XREF[2]:	FUN_025e:03ca(c),
03cf bf 1f	STX	DAT 001f		FUN_03f4:0417(c) = FFh
03d1 a6 09	LDA	#0x9		
03d3 b7 1e	STA	DAT 001e		= FFh
03d5 a6 01	LDA	#0×1		
03d7 b7 24	STA	DAT_0024		= FFh
03d9 3a 1e	LAB_03d9 DEC	DAT 001e	XREF[2]:	03ed(j), 03f2(j) = FFh
03db 26 01	BNE	LAB 03de		
03dd 81	RTS	EAD_03de		
	LAB_03de		XREF[1]:	03db(j)
03de be 1f	LDX	DAT_001f		= FFh
03e0 f6	LDA	Х		
03e1 cd 06 23	JSR	FUN_0623		
03e4 a1 ff 03e6 26 07	CMP	#0xff LAB 03ef		
03e8 cd 05 97	BNE JSR	FUN 0597		
03eb 14 19	BSET	0x2,CLR		= FFh
03ed 20 ea	BRA	LAB_03d9		
	LAB_03ef		XREF[1]:	
03ef cd 05 d2 03f2 20 e5	JSR	FUN_05d2		
0312 20 es	BRA	LAB_03d9		
	******	********	*****	****
	*	FUNCTION		*
	*	FUNCTION		*
undefined	* *********** undefined FU	FUNCTION ************************************		*
undefined	* ********** undefined FU A:1	FUNCTION	******	*
undefined	* *********** undefined FU	FUNCTION ************************************	******	*
	* ******** undefined FU A:1 FUN_03f4 LDA	FUNCTION N_03f4() <return></return>	******	*
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19	* ********** undefined FU A:1 FUN_03f4 LDA JSR BRSET	FUNCTION ***********************************	******	* **** FUN_025e:03cd(c)
03f4 a6 40 03f6 cd 06 90	* ******** undefined FU A:1 FUN_03f4 LDA JSR	FUNCTION N_03f4() <return> #0x40 FUN_0690</return>	******	* FUN_025e:03cd(c) undefined FUN_0690()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19	* ********** undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX	FUNCTION ***********************************	**************************************	* FUN_025e:03cd(c) undefined FUN_0690() = FFh
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe	FUNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0</return>	******	* **** FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j)
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19	* ********** undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX	#UNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X</return>	**************************************	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA	FUNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0</return>	**************************************	* **** FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j)
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR	#UNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X</return>	**************************************	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP	FUNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4</return>	**************************************	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE	FUNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe</return>	**************************************	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA	FUNCTION N_03f4() <return> \$0x40 FUN_0690 0x5,CLR,LAB_0415 \$0x0 DAT_0025,X FUN_05d2 \$0x4 LAB_03fe \$0x40</return>	**************************************	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD	FUNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7</return>	**************************************	* **** FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR	FUNCTION N_03f4() <return> \$0x40 FUN_0690 0x5,CLR,LAB_0415 \$0x0 DAT_0025,X FUN_05d2 \$0x4 LAB_03fe \$0x40</return>	**************************************	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 0406 ab 07 0406 cd 06 90	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA	FUNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690</return>	**************************************	* **** FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 0409 ab 07 040d cd 06 90 0410 a6 3a	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA JSR LDA JSR	#UNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a</return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP ENE LDA ADD JSR LDA JSR LDA ADD JSR LDA JSR LDA	#UNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a FUN_05d2</return>	**************************************	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 0409 ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA JSR LDA JSR LDA	#UNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a FUN_05d2 #0x3d</return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA JSR LDA ADD JSR LDA JSR	FUNCTION N_03f4() <return> \$0x40 FUN_0690 0x5,CLR,LAB_0415 \$0x0 DAT_0025,X FUN_05d2 \$0x4 LAB_03fe \$0x40 \$0x7 FUN_0690 \$0x3a FUN_05d2 \$0x3d FUN_03cf</return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 0409 ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA JSR LDA ADD JSR LDA JSR	#UNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a FUN_05d2 #0x3d</return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA JSR LDA ADD JSR LDA JSR	FUNCTION N_03f4() <return> \$0x40 FUN_0690 0x5,CLR,LAB_0415 \$0x0 DAT_0025,X FUN_05d2 \$0x4 LAB_03fe \$0x40 \$0x7 FUN_0690 \$0x3a FUN_05d2 \$0x3d FUN_03cf</return>	<pre>XREF[1]:</pre> <pre>XREF[1]:</pre> <pre>XREF[1]:</pre>	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2() 03f9(j) undefined FUN_03cf()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA	FUNCTION N_03f4() <return> \$0x40 FUN_0690 0x5,CLR,LAB_0415 \$0x0 DAT_0025,X FUN_05d2 \$0x4 LAB_03fe \$0x40 \$0x7 FUN_0690 \$0x3a FUN_05d2 \$0x3d FUN_05d2 FUN_05d2</return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2() undefined FUN_05d2() vundefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA	#UNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a FUN_05d2 #0x3d FUN_05d2</return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2() undefined FUN_05d2() vundefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2 0415 ae 3d 0417 cd 03 cf 041a cc 05 79	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA JSR LDA JSR LDA JSR LDA JSR LDA ADD JSR LDA JSR LOB JMP	FUNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a FUN_05d2 #0x3d FUN_05d2 #0x3d FUN_05fe LAB_0579 FUNCTION N_041d()</return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2() undefined FUN_05d2() vundefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA	FUNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a FUN_05d2 #0x3d FUN_05d2 #0x3d FUN_05fe LAB_0579 FUNCTION N_041d()</return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2() undefined FUN_05d2() vundefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2 0415 ae 3d 0417 cd 03 cf 041a cc 05 79	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA LDA JSR LDA	#0x40 #0x40 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a FUN_05d2 #0x3d FUN_05d2 #0x3d FUN_03cf LAB_0579 FUNCTION N_041d() <return></return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2() undefined FUN_05d2() vundefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2 0415 ae 3d 0417 cd 03 cf 041a cc 05 79	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA JSR LDA JSR LDA JSR LDA ADD JSR LDA ADD JSR LDA	FUNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a FUN_05d2 #0x3d FUN_05d2</return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2() undefined FUN_05d2() vundefined FUN_05d2() ***** FUN_025e:0305(c)
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2 0415 ae 3d 0417 cd 03 cf 041a cc 05 79	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA LDA JSR LDA	#0x40 #0x40 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a FUN_05d2 #0x3d FUN_05d2 #0x3d FUN_03cf LAB_0579 FUNCTION N_041d() <return></return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2() undefined FUN_05d2() vundefined FUN_05d2()
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2 0415 ae 3d 0417 cd 03 cf 041a cc 05 79 undefined 041d a6 01 041d a6 01	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP ENE LDA ADD JSR LDA JSR LDA JSR LDA JSR LDA JSR LDA	FUNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a FUN_05d2 #0x3d FUN_05d2 #0x3d FUN_05d2 #0x10 CLR</return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2() undefined FUN_05d2() vundefined FUN_05d2() ***** FUN_025e:0305(c)
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2 0413 ac 3d 0417 cd 03 cf 041a cc 05 79 undefined 041d a6 01 041f b7 19 0421 a6 00 0423 b7 1e 0425 a6 00	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA JSR LDA JSR LDA JSR LDA LDA LDA ADD JSR LDA LDA LDA LDA JSR LDA JSR LDA LDA JSR JMP	FUNCTION N_03f4() <return> #0x40 FUN_0690 0x5,CLR,LAB_0415 #0x0 DAT_0025,X FUN_05d2 #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a FUN_05d2 #0x3d FUN_05d2 #0x1 CLB #0x10 CRETURN></return>	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2() undefined FUN_05d2() ***** * FUN_025e:0305(c) = FFh
03f4 a6 40 03f6 cd 06 90 03f9 0a 19 19 03fc ae 00 03fe e6 25 0400 cd 05 d2 0403 5c 0404 9f 0405 al 04 0407 26 f5 0409 a6 40 040b ab 07 040d cd 06 90 0410 a6 3a 0412 cd 05 d2 0415 ae 3d 0417 cd 03 cf 041a cc 05 79 undefined 041d a6 01 041d a6 01 041f b7 19 0421 a6 00 0423 b7 1e	undefined FU A:1 FUN_03f4 LDA JSR BRSET LDX LAB_03fe LDA JSR INCX TXA CMP BNE LDA ADD JSR LDA JSR LDA JSR LDA JSR LDA LDA LDA ADD JSR LDA LDA LDA LDA JSR JMP	#0x40 #0x40 #0x40 #0x40 #0x0 DAT_0025,X #0x4 LAB_03fe #0x4 LAB_03fe #0x40 #0x7 FUN_0690 #0x3a FUN_05d2 #0x3d FUN_05d2 #0x3d FUN_05d2 #0x3d FUN_05d2	XREF[1]: XREF[1]:	* FUN_025e:03cd(c) undefined FUN_0690() = FFh 0407(j) = FFh undefined FUN_05d2() undefined FUN_05d2() undefined FUN_05d2() ***** * FUN_025e:0305(c) = FFh

	LAB_042a		XREF[1]:	
042a bf 1f 042c a6 5f	STX LDA	DAT_001f #0x5f		= FFh
042e 03 1c 02	BRCLR	0x1,DAT 001c,LAB 0433		
0431 a6 db	LDA	#0xdb		
	LAB_0433		XREF[1]:	042e(j)
0433 cd 05 d2	JSR	FUN_05d2		
0436 01 1b 02 0439 a6 db	BRCLR	0x0, ACLR, LAB_043b		= FFh
0439 ab db	LDA	#0xdb		
	LAB 043b		XREF[1]:	0436(i)
043b cd 05 d2	JSR	FUN_05d2		undefined FUN_05d2()
043e 05 1b 02	BRCLR	0x2,ACLR,LAB_0443		= FFh
0441 a6 db	LDA	#0xdb		
	LAB_0443		XREF[1]:	
0443 cd 05 d2 0446 09 1b 02	JSR	FUN_05d2		
0446 09 16 02 0449 a6 db	BRCLR LDA	0x4,ACLR,LAB_044b #0xdb		= FFh
0113 00 00	2511	" ortab		
	LAB 044b		XREF[1]:	0446(j)
044b cd 05 d2	JSR	FUN_05d2		
044e 0d 1b 02	BRCLR	0x6, ACLR, LAB_0453		= FFh
0451 a6 db	LDA	#0xdb		
	0.50			
0452 -4 05 42	LAB_0453	THE 05 40	XREF[1]:	044e(j) undefined FUN 05d2()
0453 cd 05 d2 0456 0f 1c 02	JSR BRCLR	FUN_05d2 0x7,DAT 001c,LAB 045b		
0450 01 1C 02	LDA	#0xdb		
0.000 000 000	2511	" ortab		
	LAB_045b		XREF[1]:	0456(j)
045b cd 05 d2	JSR	FUN_05d2		
045e 0b 1c 02	BRCLR	0x5,DAT_001c,LAB_0463		= FFh
0461 a6 db	LDA	#0xdb		
	TAD 0462		XREF[1]:	045-7-1
0463 cd 05 d2	LAB_0463 JSR	FUN 05d2	AREF[I];	undefined FUN 05d2()
0466 09 1c 02	BRCLR	0x4,DAT 001c,LAB 046b		
0469 a6 db	LDA	#0xdb		
	LAB_046b		XREF[1]:	0466(j)
046b cd 05 d2	JSR	FUN_05d2		
046e be 1f	LDX	DAT_001f		= FFh
0470 5c 0471 e6 2d	INCX	02-4 W		
0471 e0 2u 0473 b7 1b	LDA STA	0x2d,X ACLR		
0475 5c	INCX	110211		
0476 e6 2d	LDA	0x2d, X		
0478 b7 1c	STA	DAT_001c		= FFh
047a 3c 1e	INC	DAT_001e		= FFh
047c a6 0c	LDA	#0xc		
047e cd 06 90	JSR	FUN_0690		
0481 b6 1e 0483 a1 01	LDA CMP	DAT_001e #0x1		= FFh
0485 23 a3	BLS	LAB 042a		
0487 a6 18	LDA	#0×18		
0489 cd 06 90	JSR	FUN_0690		
048c a6 5f	LDA	#0x5f		
048e 01 1c 02	BRCLR	0x0,DAT_001c,LAB_0493		= FFh
0491 a6 30	LDA	#0x30		
	LAB 0493		XREF[1]:	048e(j)
0493 cd 05 d2	_	FUN 05d2	ANDE [1].	undefined FUN 05d2()
0496 03 1b 02	BRCLR	0x1,ACLR,LAB 049b		= FFh
0499 a6 31	LDA	#0×31		
	LAB_049b		XREF[1]:	
049b cd 05 d2		FUN_05d2		
049e Of 1b 02		0x7,ACLR,LAB_04a3		= FFh
04a1 a6 32	LDA	#0x32		
	LAB 04a3		XREF[1]:	049e(i)
04a3 cd 05 d2	_	FUN 05d2		undefined FUN 05d2()
04a6 0d 1c 02	BRCLR	0x6,DAT_001c,LAB_04ab		= FFh
04a9 a6 33	LDA	#0x33		
	LAB_04ab	0510	XREF[1]:	04a6(j)
04ab cd 05 d2	JSR	FUN_05d2		
04ae a6 1f 04b0 cd 06 90	LDA JSR	#0x1f FUN 0690		
04b0 cd 06 90 04b3 5c	INCX	-00000		
04b4 e6 2d	LDA	0x2d,X		
04b6 b7 1b	STA	ACLR		= FFh

	003_350.6	••••		
04b8 5c	INCX			
04b9 e6 2d	LDA	0x2d,X		
04bb b7 1c	STA	DAT_001c		= FFh
04bd ae 9c	LDX	#0x9c		
04bf Of 1b 02	BRCLR	0x7, ACLR, LAB_04c4		= FFh
04c2 ae 98	LDX	#0x98		
	LAB_04c4		XREF[1]:	04bf(j)
04c4 cd 06 0c	JSR	FUN_060c		
04c7 a6 24	LDA	#0x24		
04c9 cd 06 90	JSR	FUN_0690		
04cc ae 9c	LDX	#0x9c		
04ce 09 1b 02	BRCLR	0x4,ACLR,LAB_04d3		= FFh
04d1 ae 98	LDX	#0x98		
	73D 0430		uppp/11	04 (1)
04d3 cd 06 0c	LAB_04d3 JSR	FUN 060c	XREF[1]:	04ce(j) undefined FUN 060c()
0403 60 00 06	0010	101-0000		
	LAB 04d6		XREF[1]:	FUN 025e:0346(j)
04d6 a6 40	LDA	#0×40		= -
04d8 cd 06 90	JSR	FUN 0690		
04db 0e 38 04	BRSET	0x7,DAT 0038,LAB 04e2		= FFh
04de ae c7	LDX	#0xc7		
04e0 20 02	BRA	LAB_04e4		
	LAB_04e2		XREF[1]:	04db(j)
04e2 ae a0	LDX	#0xa0		
04.4.1.05.5	LAB_04e4		XREF[1]:	04e0(j)
04e4 cd 06 0c	JSR	FUN_060c		
04e7 ae 00	LDX	#0×0		
04e9 bf 1f	STX	DAT_001f		= FFh
04eb e6 35	LDA	DAT_0035, X		= FFh
04ed cd 06 23	JSR	FUN_0623		
04f0 cd 05 d2	JSR	FUN_05d2		
04f3 be 1f	LDX	DAT_001f		= FFh
04f5 e6 35	LDA	DAT_0035, X		= FFh
04f7 cd 06 23	JSR	FUN_0623		
04fa cd 05 d2	JSR	FUN 05d2		
04fd Of 38 Oc	BRCLR	0x7,DAT 0038,LAB 050c		= FFh
0500 ae a5	LDX	#0xa5		
0502 0e 3c 02	BRSET	0x7,DAT 003c,LAB 0507		
0505 ae ad	LDX	#0xad		
	LAB_0507		XREF[1]:	0502(j)
0507 18 19	BSET	0x4,CLR		= FFh
0509 cd 06 0c	JSR	FUN_060c		
	LAB 050c		XREF[1]:	04fd(j)
050c be 1f	LDX	DAT 001f	AREI [I].	= FFh
050e e6 35	LDA	DAT 0035, X		
0510 cd 06 23	JSR	FUN 0623		
		FUN 05d2		
0513 cd 05 d2	JSR	-		
0516 be 1f	LDX	DAT_001f		= FFh
0518 e6 35	LDA	DAT_0035, X		= FFh
051a cd 06 23	JSR	FUN_0623		
051d cd 05 d2	JSR	FUN_05d2		
0520 Oe 38 Ob	BRSET	0x7,DAT_0038,LAB_052e		
0520 0e 38 0b 0523 ae cd	BRSET LDX	0x7,DAT_0038,LAB_052e #0xcd		undefined FUN_05d2() = FFh
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c	BRSET LDX JSR	0x7, DAT_0038, LAB_052e #0xcd FUN_060c		undefined FUN_05d2() = FFh undefined FUN_060c()
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f	BRSET LDX	0x7,DAT_0038,LAB_052e #0xcd		undefined FUN_05d2() = FFh
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c	BRSET LDX JSR	0x7, DAT_0038, LAB_052e #0xcd FUN_060c		undefined FUN_05d2() = FFh undefined FUN_060c()
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052b 5c	BRSET LDX JSR LDX	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f		undefined FUN_05d2() = FFh undefined FUN_060c()
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c	BRSET LDX JSR LDX INCX	0x7, DAT_0038, LAB_052e #0xcd FUN_060c		undefined FUN_05d2() = FFh undefined FUN_060c()
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052b 5c	BRSET LDX JSR LDX INCX INCX BRA	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f	VDDD(1).	undefined FUN_05d2() = FFh undefined FUN_060c() = FFh
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052b 5c 052c 20 20	BRSET LDX JSR LDX INCX INCX ERA LAB_052e	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e	XREF[1]:	undefined FUN_05d2() = FFh undefined FUN_060c() = FFh
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052b 5c 052c 20 20	BRSET LDX JSR LDX INCX INCX ERA LAB_052e LDA	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a	XREF[1]:	<pre>undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j)</pre>
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052a 5c 052b 5c 052b 5c 052c 20 20	BRSET LDX JSR LDX INCX INCX INCX ERA LAB_052e LDA JSR	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2	XREF[1]:	<pre>undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2()</pre>
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052a 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0533 be 1f	BRSET LDX JSR LDX INCX INCX ERA LAB_052e LDA JSR LDA LDA LDA LDA	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f	XREF[1]:	<pre>undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh</pre>
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052b 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0533 be 1f 0533 e6 35	BRSET LDX JSR LDX INCX INCX ERA LAB_052e LDA JSR LDX LDA LDA LDA	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X	XREF[1]:	<pre>undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh = FFh</pre>
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0533 be 1f 0533 be 1f 0533 cd 06 23	BRSET LDX JSR LDX INCX INCX INCX BRA LAB_052e LDA JSR LDX LDX LDA JSR	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, x FUN_0623	XREF[1]:	<pre>undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh = FFh undefined FUN_0623()</pre>
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 0528 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0535 be 1f 0535 e6 35 0537 cd 06 23 0537 cd 06 23	BRSET LDX JSR LDX INCX INCX INCX BRA LAB_052e LDA JSR LDX	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2	XREF[1]:	<pre>undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh</pre>
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052b 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0533 be 1f 0535 e6 35 0537 cd 06 23 0534 cd 05 d2 0535 cd 05 d2	BRSET LDX JSR LDX INCX INCX INCX BRA LAB_052e LDA JSR LDX	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 DAT_001f	XREF[1]:	<pre>undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh = FFh undefined FUN_06d2() undefined FUN_05d2() = FFh</pre>
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052b 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0535 e6 35 0537 cd 06 23 053a cd 05 d2 053d be 1f 053d be 1f 053d e6 35	BRSET LDX JSR LDX INCX INCX INCX BRA LAB_052e LDA JSR LDX	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2	XREF[1]:	<pre>undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh = FFh undefined FUN_0623() undefined FUN_05d2()</pre>
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0533 be 1f 0535 e6 35 0537 cd 06 23 053a cd 05 d2 053d be 1f 0535 e6 35	BRSET LDX JSR LDX INCX INCX INCX BRA LAB_052e LDA JSR LDX	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 DAT_001f	XREF[1]:	<pre>undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh = FFh undefined FUN_0623() undefined FUN_05d2() = FFh = FFh</pre>
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052b 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0535 e6 35 0537 cd 06 23 053a cd 05 d2 053d be 1f 053d be 1f 053d e6 35	BRSET LDX JSR LDX INCX INCX INCX BRA LAB_052e LDA JSR LDX LDA	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 DAT_001f DAT_05d2 DAT_001f DAT_0035, X	XREF[1]:	undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh = FFh undefined FUN_0623() undefined FUN_05d2() = FFh undefined FUN_05d2() = FFh undefined FUN_05d2()
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0533 be 1f 0535 e6 35 0537 cd 06 23 053a cd 05 d2 053d be 1f 0535 e6 35	BRSET LDX JSR LDX INCX INCX INCX BRA LAB_052e LDA JSR LDX LDA JSR LDX LDA JSR LDA JSR JSR LDX LDA JSR LDX LDA JSR LDX LDA JSR	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, x FUN_0623 FUN_05d2 DAT_001f DAT_0035, x FUN_0623 FUN_0642 FUN_0642 FUN_0642 FUN_0642 FUN_0642 FUN_0642 FUN_0642 FUN_0643 FUN_0643	XREF[1]:	undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh = FFh undefined FUN_0623() undefined FUN_05d2() = FFh undefined FUN_05d2() = FFh undefined FUN_05d2()
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 0528 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0533 be 1f 0533 be 1f 0535 cd 06 23 0530 cd 05 d2 0536 be 1f 0546 6 35	BRSET LDX JSR LDX INCX INCX INCX BRA LAB_052e LDA JSR LDX	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 FUN_0623 FUN_05d2 FUN_0623 FUN_0623 FUN_0623 FUN_0623 FUN_0623 FUN_0623 FUN_0623	XREF[1]:	undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh = FFh undefined FUN_05d2() = FFh = FFh undefined FUN_05d2() = FFh = FFh undefined FUN_05d2()
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052b 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0533 be 1f 0533 be 1f 0533 cd 05 d2 0533 be 1f 0536 c6 35 0537 cd 06 23 053d be 1f 053f e6 35 0541 cd 06 23	BRSET LDX JSR LDX INCX INCX INCX BRA LAB_052e LDA JSR LDX LDA JSR LDA LDA JSR LDA	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 FUN_0623 FUN_05d2 FUN_0623 FUN_05d2 FUN_0623 FUN_0623 FUN_0623 FUN_0623 FUN_0623 FUN_05d2 #0x3a	XREF[1]:	undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh = FFh undefined FUN_05d2() = FFh = FFh undefined FUN_05d2() = FFh = FFh undefined FUN_05d2()
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052b 5c 052c 20 20 0530 cd 05 d2 0533 be 1f 0533 be 1f 0535 e6 35 0537 cd 06 23 0536 cd 05 d2 0536 be 1f 0536 e6 35 0537 cd 06 23 0536 cd 05 d2 0536 be 1f 0537 e6 35	BRSET LDX JSR LDX INCX INCX INCX BRA LAB_052e LDA JSR LDX LDA JSR	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 FUN_0623 FUN_05d2		undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh undefined FUN_0623() undefined FUN_05d2() = FFh undefined FUN_05d2() = FFh undefined FUN_05d2() = FFh undefined FUN_05d2() = FFh
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0533 be 1f 0535 e6 35 0537 cd 06 23 0534 be 1f 0536 e6 35 0541 cd 06 23 0544 cd 05 d2 0547 a6 3a 0549 cd 05 d2	BRSET LDX JSR LDX INCX INCX BRA LAB_052e LDA JSR LDX LDA JSR LDA JSR	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 FUN_05d2 FUN_05d2 FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 #0x3a FUN_05d2 #0x3a FUN_05d2 DAT_001f	<pre>XREF[1]:</pre> <pre>XREF[1]:</pre>	undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh = FFh undefined FUN_05d2() = FFh = FFh undefined FUN_05d2() = FFh undefined FUN_05d2() = FFh The FFh undefined FUN_05d2() = FFh Undefined FUN_05d2() = FFh
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 0528 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0533 be 1f 0533 be 1f 0533 be 1f 0533 cd 05 d2 0536 cd 05 d2 0536 cd 05 d2 0537 cd 06 23 053a cd 05 d2 053d be 1f 0535 e6 35 0537 cd 06 23 0534 cd 05 d2 0540 cd 05 d2 0540 cd 05 d2 0540 cd 05 d2	BRSET LDX JSR LDX INCX INCX INCX BRA LAB_052e LDA JSR LDX LDA JSR	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 FUN_0623 FUN_05d2		<pre>undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh undefined FUN_0623() undefined FUN_05d2() = FFh undefined FUN_05d2() = FFh undefined FUN_05d2() = FFh</pre>
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 052a 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0533 be 1f 0535 e6 35 0537 cd 06 23 0534 be 1f 0536 e6 35 0541 cd 06 23 0544 cd 05 d2 0547 a6 3a 0549 cd 05 d2	BRSET LDX JSR LDX INCX INCX BRA LAB_052e LDA JSR LDX LDA	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 FUN_05d2 FUN_05d2 FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 #0x3a FUN_05d2 #0x3a FUN_05d2 DAT_001f		undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh = FFh undefined FUN_05d2() = FFh = FFh undefined FUN_05d2() = FFh undefined FUN_05d2() = FFh The FFh undefined FUN_05d2() = FFh Undefined FUN_05d2() = FFh
0520 0e 38 0b 0523 ae cd 0525 cd 06 0c 0528 be 1f 0528 5c 052b 5c 052c 20 20 052e a6 3a 0530 cd 05 d2 0533 be 1f 0533 be 1f 0533 be 1f 0533 cd 05 d2 0536 cd 05 d2 0536 cd 05 d2 0537 cd 06 23 053a cd 05 d2 053d be 1f 0535 e6 35 0537 cd 06 23 0534 cd 05 d2 0540 cd 05 d2 0540 cd 05 d2 0540 cd 05 d2	BRSET LDX JSR LDX INCX INCX INCX BRA LAB_052e LDA JSR LDX LDX LDA JSR LDX LDA	0x7, DAT_0038, LAB_052e #0xcd FUN_060c DAT_001f LAB_054e #0x3a FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 DAT_001f DAT_0035, X FUN_05d2 DAT_001f DAT_0035, X FUN_0623 FUN_05d2 DAT_001f DAT_0035, X FUN_0643 FUN_05d2 #0x3a FUN_05d2 #0x3a FUN_05d2 #0x3a FUN_05d2 #0x3a FUN_05d2 DAT_001f		undefined FUN_05d2() = FFh undefined FUN_060c() = FFh 0520(j) undefined FUN_05d2() = FFh = FFh undefined FUN_05d2() = FFh = FFh undefined FUN_05d2() undefined FUN_05d2() = FFh = FFh undefined FUN_05d2() = FFh 052c(j) = FFh

Ghidra - MC6870	5U3 35C.E	BIN		
0558 e6 35	LDA	DAT 0035, X		= FFh
055a cd 06 23	JSR	FUN 0623		
055d cd 05 d2		FUN 05d2		
0560 a6 57		_		
		#0×57		
0562 cd 06 90		FUN_0690		
0565 a6 20	LDA	#0x20		
0567 cd 05 d2		FUN_05d2		
056a ae 00	LDX	#0×0		
	LAB_056c		XREF[1]:	-
056c e6 3d	LDA	DAT_003d, X		= FFh
056e b7 1b	STA	ACLR		= FFh
0570 cd 05 7c	JSR	FUN_057c		
0573 5c	INCX			
0574 9f	TXA			
0575 al 08	CMP	#0x8		
0577 25 f3	BCS	LAB_056c		
	LAB_0579		XREF[1]:	FUN_03f4:041a(j)
0579 cc 01 42	JMP	FUN_0142		undefined FUN_0142()
	Flow Overr			

	*******		******	****
	*	FUNCTION		*

	undefined FUN			
undefined		<return></return>		
	FUN_057c		XREF[1]:	FUN_041d:0570(c)
057c 04 1b 07		0x2,ACLR,LAB_0586		= FFh
057f a6 2e	LDA	#0x2e		
	0504			0500444
0581 cd 05 d2	LAB_0581 JSR	FUN 05d2	XREF[1]:	
		-		
0584 20 04	BRA	LAB_058a		
	TAR 0506		VDDD(11.	057-(-)
0586 a6 7c	LAB_0586 LDA	#0x7c	XREF[1]:	0576(J)
0588 20 f7	BRA	LAB 0581		
0388 20 17	DKA	LAB_0301		
	LAB 058a		XREF[1]:	0584(i)
058a 00 1b 06	BRSET	0x0,ACLR,LAB 0593	111111 [1].	= FFh
058d a6 2e	LDA	#0x2e		
0004 40 20	2511	"ONLO		
	LAB 058f		XREF[1]:	0595(i)
058f cd 05 d2	_	FUN 05d2	* * *	undefined FUN 05d2()
0592 81	RTS	-		
	LAB_0593		XREF[1]:	058a(j)
0593 a6 7c	LDA	#0x7c		
0595 20 f8	BRA	LAB_058f		
	******	*******	******	****
	*	FUNCTION		*
		********	******	****
	undefined FUN	-		
undefined	A:1	<return></return>		
	FUN_0597		XREF[1]:	FUN_03cf:03e8(c)
0597 3c 24	INC	DAT_0024		= FFh
0599 b6 24	LDA	DAT_0024		= FFh
059b a1 03	CMP	#0x3		
059d 26 03	BNE	LAB_05a2		
059f cd 05 c8	JSR	FUN_05c8		
	05 0			050.1411
05 0 04 11 07	LAB_05a2	0.0.3070.730.05	XREF[1]:	059d(j)
05a2 04 1b 07	BRSET	0x2,ACLR,LAB_05ac		= FFh
05a5 a6 30	LDA	#0x30		
	LAB 05a7		XREF[1]:	0520(3)
05a7 cd 05 d2	_	FUN 05d2	AKEF[I].	undefined FUN 05d2()
05aa 20 04	BRA	LAB 05b0		
	LAB 05ac		XREF[1]:	05a2(j)
05ac a6 31	LDA	#0×31		
05ae 20 f7	BRA	LAB_05a7		
		=		
	LAB_05b0		XREF[1]:	05aa(j)
05b0 3c 24	INC	DAT_0024		= FFh
05b2 b6 24	LDA	DAT_0024		= FFh
05b4 a1 03	CMP	#0x3		
05b6 26 03	BNE	LAB_05bb		
05b8 cd 05 c8	JSR	FUN_05c8		
	LAB_05bb		XREF[1]:	05b6(j)
05bb 00 1b 06 05be a6 30	LAB_05bb BRSET LDA	0x0,ACLR,LAB_05c4 #0x30	XREF[1]:	05b6(j) = FFh

```
LAB_05c0
                                                           XREF[1]: 05c6(j)
05c0 cd 05 d2
                             FUN_05d2
                  JSR
05c3 81
                  RTS
               LAB_05c4
                                                           XREF[1]:
                             #0x31
05c4 a6 31
                  LDA
05c6 20 f8
                  BRA
                             LAB_05c0
               * FUNCTION *
               undefined FUN_05c8()
undefined
                              <RETURN>
                A:1
               FUN_05c8
                                                           XREF[2]: FUN_0597:059f(c),
                                                                       FUN_0597:05b8(c)
05c8 a6 2e
                   LDA
                            #0x2e
                            FUN_05d2
#0x0
05ca cd 05 d2
                  JSR
05cd a6 00
                  LDA
05cf b7 24
                   STA
                          DAT_0024
05d1 81
                  RTS
                                       FUNCTION
               undefined FUN_05d2()
undefined
                              <RETURN>
                A:1
               FUN_05d2
                                                           XREF[33]: FUN_025e:0388(c),
                                                                       FUN_03f4:0400(c),
                                                                       FUN_03f4:0412(c),
                                                                       FUN_041d:0433(c),
                                                                       FUN_041d:043b(c),
                                                                       FUN_041d:0443(c),
                                                                       FUN_041d:044b(c),
                                                                       FUN_041d:0453(c),
                                                                       FUN 041d:045b(c),
                                                                       FUN 041d:0463(c),
                                                                       FUN_041d:0493(c),
                                                                       FUN_041d:049b(c),
                                                                       FUN 041d:04a3(c).
                                                                       FUN_041d:04ab(c),
                                                                       FUN_041d:04f0(c),
                                                                       FUN_041d:04fa(c),
                                                                       FUN 041d:0513(c),
                                                                       FUN_041d:051d(c),
                                                                       FUN_041d:0530(c), [more]
05d2 b7 49
                             DAT_0049
05d4 b6 48
                  LDA
                             DAT_0048
05d6 al 28
                  CMP
                             #0x28
05d8 26 07
                             LAB_05e1
05da a6 00
                             FUN_06b0
05dc cd 06 b0
                  JSR
05df 20 09
                  BRA
                             LAB_05ea
              LAB_05e1
                                                           XREF[1]:
                                                                      05d8(j)
05e1 a1 68
                             #0x68
05e3 26 05
                   BNE
                             LAB_05ea
05e5 a6 40
                  LDA
                             #0x40
05e7 cd 06 b0
                             FUN_06b0
                  JSR
               LAB_05ea
                                                           XREF[2]:
                                                                       05df(j), 05e3(j)
                LDA
05ea b6 49
                             DAT_0049
05ec b7 01
                             PORTB_ND_SIGNALS
0x3,PORTC_STAT_DISP
                  STA
                BSET
05ee 16 02
05f0 17 02
                             0x3,PORTC_STAT_DISP
05f2 3c 48
                  INC
                             DAT_0048
05f4 a6 05
                 LDA
                             #0x5
               LAB_05f6
                                                           XREF[1]:
                                                                       05f7(j)
                DECA
05f6 4a
05f7 26 fd
                  BNE
                             LAB 05f6
05f9 a6 5f
                  LDA
                             #0x5f
05fb <mark>81</mark>
                  RTS
                                      FUNCTION
               undefined FUN_05fc()
                          <RETURN>
undefined
               FUN_05fc
                                                           XREF[6]: RESET:0107(c), RESET:010c(c),
                                                                       RESET:0111(c), FUN 025e:037c(c),
                                                                       FUN 0618:061a(c),
                                                                       FUN_06b0:06b8(c)
05fc b7 01
                             PORTB_ND_SIGNALS
```

```
Ghidra - MC68705U3_35C.BIN
          05fe 15 02
                                     0x2,PORTC_STAT_DISP
                                     0x3,PORTC_STAT_DISP
0x3,PORTC_STAT_DISP
          0600 16 02
                            BSET
          0602 17 02
                            BCLR
                                     0x2,PORTC_STAT_DISP
          0604 14 02
                            BSET
                          LDA
          0606 a6 05
                        LAB_0608
                                                                  XREF[1]: 0609(j)
          0608 4a
                         DECA
          0609 26 fd
                           BNE
                                    LAB_0608
                         * FUNCTION *
                        undefined FUN_060c()
           undefined
                         A:1
                                       <RETURN>
                        FUN_060c
                                                                  XREF[8]: FUN 025e:03bd(c),
                                                                             FUN 025e:03c4(c),
                                                                             FUN_041d:04c4(c),
                                                                             FUN_041d:04d3(c),
                                                                             FUN_041d:04e4(c),
                                                                             FUN 041d:0509(c),
                                                                             FUN 041d:0525(c), 0615(j)
          060d a1 22
                           CMP
                                     #0x22
          060f 27 06
                          BEQ
                                     LAB_0617
          0611 cd 05 d2
                                     FUN_05d2
                           JSR
                          INCX
          0614 5c
          0615 20 f5
                                     FUN_060c
                                                                  XREF[1]: 060f(j)
                        LAB_0617
          0617 81
                           RTS
                                             FUNCTION
                        undefined FUN_0618()
                                      <RETURN>
                        FUN_0618
                                                                 XREF[3]: RESET:0114(c), FUN_011f:0138(c),
                                                                             FUN 025e:02e6(c)
                           LDA #0x1
JSR FUN_05fc
LDA --
                         LDA
          0618 a6 01
          061a cd 05 fc
                        LAB_061f
                                                                 XREF[1]: 0620(j)
                        DECA
          061f 4a
          0620 26 fd
                                    LAB_061f
          0622 81
                            RTS
                        * FUNCTION *
                        undefined FUN_0623()
                                 <RETURN>
          undefined
                         A:1
                        FUN_0623
                                                                  XREF[9]: FUN 03cf:03e1(c).
                                                                             FUN_041d:04ed(c),
                                                                             FUN_041d:04f7(c),
                                                                             FUN_041d:0510(c),
                                                                             FUN 041d:051a(c),
                                                                             FUN_041d:0537(c),
                                                                             FUN_041d:0541(c),
                                                                             FUN_041d:0550(c),
                                                                             FUN_041d:055a(c)
          0623 5c
          0624 bf 1f
                                      DAT_001f
          0626 b7 1b
                                      ACLR
          0628 1f 1b
                           BCLR
                                      0x7,ACLR
          062a ae 00
                          LDX
                                      #0x0
                                                                             0637(j)
                        LAB_062c
          062c d6 07 59
                                      DAT_0759, X
          062f b1 1b
                           CMP
                                      ACLR
                                     LAB_063c
          0631 27 09
                          BEQ
INCX
          0633 5c
                          INCX
CMP
BNE
          0635 al ff
                                      #0xff
          0637 26 f3
                                     LAB 062c
          0639 a6 20
                                     #0x20
                           LDA
          063b <mark>81</mark>
                           RTS
                        LAB_063c
                                                                  XREF[1]: 0631(j)
                          INCX
          063c 5c
          063d d6 07 59
                            LDA
                                     0x759,X=>DAT 075a
```

	******	*********	****	****
	*	FUNCTION		*
	********	**********	*****	****
	undefined FU			
undefined	A:1	<return></return>		
undelined	FUN_0641	CRETORN/	XREF[2]:	FUN_025e:02fc(c), FUN_025e:0322(c)
0641 bf 1f	STX	DAT_001f		= FFh
0643 a6 00	LDA	#0×0		
0645 b7 1d	STA	DAT 001d		= FFh
0647 ae 00	LDX	#0×0		
	LAB 0649		XREF[1]:	0654(j)
0649 d6 06 bc		DAT 06bc, X		= 08h
		 _		
064c b1 1c	CMP	DAT 001c		
064e 27 08	BEQ	LAB 0658		
0650 5c	INCX	mm_0000		
0651 5c	INCX			
0652 al fe	CMP	#0xfe		
0654 26 f3		LAB 0649		
	BNE	-		
0656 20 23	BRA	LAB_067b		
	LAB_0658		XREF[1]:	064e(j)
0658 5c	INCX			
0659 d6 06 bc	LDA	0x6bc, X=>DAT_06bd		= 80h
065c b7 1d	STA	DAT_001d		= FFh
065e 0e 1d 1e	BRSET	0x7,DAT_001d,LAB_067f		= FFh
0661 97	TAX			
	LAB_0662		XREF[1]:	0679(j)
0662 d6 06 fd	LDA	0x6fd, X=>DAT_077d		= 4Eh
				= 80h
0665 al ff	CMP	#0xff		
0667 26 Oa	BNE	LAB 0673		
0669 5c	INCX	=***		
066a d6 06 fd	LDA	0x6fd,X=>DAT 077e		
066d a1 ff	CMP	#0xff		
066f 27 Oa	BEQ	LAB 067b		
0671 20 13	BRA	LAB 0686		
06/1 20 13	BKA	LAB_0000		
	0.000			0.000.11
	LAB_0673		XREF[1]:	
0673 b1 1b	CMP	ACLR		= FFh
0675 27 0e	BEQ	LAB_0685		
0677 5c	INCX			
0678 5c	INCX			
0679 20 e7	BRA	LAB_0662		
	LAB_067b		XREF[2]:	0656(j), 066f(j)
067b a6 20	LDA	#0x20		
067d 20 Oc	BRA	LAB_068b		
	LAB 067f		XREF[1]:	065e(j)
067f 1f 1d	BCLR	0x7,DAT 001d		= FFh
0681 b6 1d	LDA	DAT 001d		= FFh
0683 20 06	BRA	LAB 068b		
		=		
	LAB 0685		XREF[1]:	0675(i)
0685 5c	INCX			*****(3)
3000 00	INOA			
	TAR NEGE		XREF[1]:	0671(4)
0.000 10 00 01	LAB_0686	0.001.000.000	AREF[1]:	
0686 d6 06 fd		0x6fd,X=>DAT_077e		= 09h
0689 b7 1d	STA	DAT_001d		= FFh
	LAB_068b		XREF[2]:	067d(j), 0683(j)
068b be 1f	LDX	DAT_001f		= FFh
068d b1 1d	CMP	DAT_001d		= FFh
068f <mark>81</mark>	RTS			

	*******	*****	******	****
	*	FUNCTI		*
	************* undefined FU		********	****
undefined	A:1			
	FUN_0690		XREF[12]:	FUN_025e:0383(c), FUN_025e:03a8(c), FUN_025e:03ad(c), FUN_03f4:03f6(c), FUN_03f4:040d(c), FUN_041d:0427(c), FUN_041d:047e(c), FUN_041d:0489(c),
				FUN_041d:04b0(c), FUN_041d:04c9(c), FUN_041d:04d8(c),
0690 al 28	CMP	#0x28		FUN_041d:0562(c)
0692 2b 12	BMI	LAB_06a6		
0694 a0 40	SUB	#0x40		
0696 bb 47 0698 b7 46	ADD STA	DAT_0047 DAT 0046		= FFh = FFh
069a a1 28	CMP	#0x28		
069c 2b 02	BMI	LAB_06a0		
069e a0 28	SUB	#0x28		
	LAB 06a0		XREF[1]:	069c(i)
06a0 ab 40	ADD	#0×40	VEDE[1]:	0000()/
06a2 b7 46	STA	DAT_0046		= FFh
06a4 20 0c	BRA	LAB_06b2		
	LAB 06a6		XREF[1]:	0692(j)
06a6 bb 47	ADD	DAT_0047		= FFh
06a8 b7 46	STA	DAT_0046		= FFh
06aa a1 28 06ac 2b 04	CMP BMI	#0x28 LAB 06b2		
06ae a0 28	SUB	#0x28		
undefined	*********** undefined FUI		***********	****
	FUN_06b0		XREF[2]:	FUN_05d2:05dc(c), FUN 05d2:05e7(c)
06b0 b7 46	STA	DAT_0046		= FFh
	LAB_06b2		XREF[2]:	FUN_0690:06a4(j), FUN_0690:06ac(j)
06b2 b7 48	STA	DAT_0048		= FFh
06b4 1e 46 06b6 b6 46	BSET LDA	0x7,DAT_0046 DAT 0046		= FFh = FFh
06b8 cd 05 fc	JSR	FUN 05fc		
06bb 81	RTS			
06bc 08	DAT_06bc undefine	d1 08h	XREF[1]:	FUN_0641:0649(R)
06bd 80	DAT_06bd undefine	d1 80h	XREF[1]:	FUN_0641:0659(R)
	DAT_06be		XREF[1]:	FUN_0641:0649(R)
06be	undefine			
06bf 80 06c0 Of	??	80h 0Fh		
06c0 01 06c1 80	??	80h		
06c2 8f	??	8Fh		
06c3 80	??	80h		
06c4 ef 06c5 80	??	EFh 80h		
06c6 ff	??	FFh		
06c7 80	??	80h		
06c8 00 06c9 00	??	00h 00h		
06c9 00 06ca d5	??	D5h		
06cb 54	??	54h T		
06cc d4	??	D4h		
06cd 4c 06ce 22	??	4Ch L 22h "		
06cf 2c	??	2Ch ,		
06d0 40	??	40h @		
06d0 40 06d1 cc	??	40h @ CCh		
06d0 40	??	40h @ CCh		

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06d4 1c	??	1Ch	
06d5 1e	??	1Eh	
06d6 1d	??	1Dh	
06d7 26	??	26h	&
06d8 41	??	41h	A
06d9 46	??	46h	F
06da	??	0Dh	
06db 16	??	16h	
06dc 03	??	03h	
06dd b1	??	B1h	
06de 05	??	05h	
06df 08	??	08h	
06e0 09	??	09h	
06e1 b4	??	B4h	
06e2 Oc	??	0Ch	
06e3 10	??	10h	
06e4 02	??	02h	
06e5 af	??	AFh	
06e6 16	??	16h	
06e7 da	??	DAh	_
06e8 54	??	54h	T
06e9 32	??	32h	2
06ea 55	??	55h	U
06eb 38	??	38h	8
06ec 61	??	61h	а
06ed ce	??	CEh	
06ee 80	??	80h	
06ef ad	??	ADh	
06f0 42	??	42h	В
06f1 d6	??	D6h	
06f2 88	??	88h	
06f3 ab	??	ABh	
06f4 95	??	95h	
06f5 c0	??	COh	
06f6 aa	??	AAh	
06f7 aa	??	AAh	
06f8 c1	??	C1h	
06f9 c8	??	C8h	
06fa c2	??	C2h	
06fb cb	??	CBh	
06fc fe	??	FEh	
06fd 00	??	00h	
06fe 20	??	20h	
06ff 20	??	20h	
0700 2e	??	2Eh	
0701 80	??	80h	
0702 2c	??	2Ch	,
0703 ff	??	FFh	
0704 ff	??	FFh	
0705 02	??	02h	
0706 37	??	37h	7
0707 0b	??	0Bh	
0708 33	??	33h	3
		4.01	J
0709 19	??	19h	
070a 32	??	32h	2
070b ff	??	FFh	
070c ff	??	FFh	
070d 0 b	??	0Bh	
070e 35	??	35h	5
070f 1b	??	1Bh	
0710 36	??	36h	6
0711 ff	??	FFh	
0712 ff	??	FFh	
0713 0b	??	0Bh	
0714 39	??	39h	9
0715 la	??	1Ah	
0716 30	??	30h	0
	??	1Bh	-
0717 1b		38h	8
	22		
0718 38	??		
0718 38 0719 ff	??	FFh	
0718 38 0719 ff 071a ff	??	FFh FFh	
0718 38 0719 ff 071a ff 071b 10	?? ?? ??	FFh FFh 10h	
0718 38 0719 ff 071a ff 071b 10 071c 54	?? ?? ??	FFh FFh 10h 54h	T
0718 38 0719 ff 071a ff 071b 10 071c 54 071d 38	3.5 3.5 3.5 3.5 3.5	FFh FFh 10h 54h 38h	T 8
0718 38 0719 ff 071a ff 071b 10 071c 54	?? ?? ??	FFh FFh 10h 54h	
0718 38 0719 ff 071a ff 071b 10 071c 54 071d 38	3.5 3.5 3.5 3.5 3.5	FFh FFh 10h 54h 38h	8
0718 38 0719 ff 071a ff 071b 10 071c 54 071d 38 071e 49	?? ?? ?? ?? ??	FFh FFh 10h 54h 38h 49h	8
0718 38 0719 ff 071a ff 071b 10 071c 54 071d 38 071e 49 071f b0	?? ?? ?? ?? ?? ??	FFh FFh 10h 54h 38h 49h B0h	8 I
0718 38 0719 ff 071a ff 071b 10 071c 54 071d 38 071e 49 071f b0 0720 4a 0721 ff	?? ?? ?? ?? ?? ?? ??	FFh FFh 10h 54h 38h 49h B0h 4Ah FFh	8 I
0718 38 0719 ff 071a ff 071b 10 071c 54 071d 38 071e 49 071f b0 0720 4a 0721 ff	?? ?? ?? ?? ?? ?? ?? ??	FFh FFh 10h 54h 38h 49h B0h 4Ah FFh	8 I J
0718 38 0719 ff 071a ff 071a ff 071b 10 071c 54 071d 38 071e 49 071f b0 0720 4a 0721 ff 0722 ff 0723 3a	?? ?? ?? ?? ?? ?? ?? ?? ??	FFh FFh 10h 54h 38h 49h B0h 4Ah FFh FFh 3Ah	8 I J
0718 38 0719 ff 071a ff 071a ff 071b 10 071c 54 071d 38 071e 49 071f b0 0720 4a 0721 ff 0722 ff 0723 ff 0724 44	33 33 33 33 33 33 33 33 33 33 33 33 33	FFh FFh 10h 54h 38h 49h B0h 4Ah FFh FFh 3Ah 44h	8 I J
0718 38 0719 ff 071a ff 071a ff 071b 10 071c 54 071d 38 071e 49 071f b0 0720 4a 0721 ff 0722 ff 0723 3a 0724 44 0725 3b	33 33 33 33 33 33 33 33 33 33 33 33 33	FFh FFh 10h 54h 38h 49h B0h 4Ah FFh 5Ah 3Ah 44h 3Bh	8 I J : D
0718 38 0719 ff 071a ff 071a ff 071b 10 071c 54 071d 38 071e 49 071f b0 0720 4a 0721 ff 0722 ff 0723 3a 0724 44 0725 3b 0726 42	33 33 33 33 33 33 33 33 33 33 33 33 33	FFh FFh 10h 54h 38h 49h B0h 4Ah FFh FFh 3Ah 44h 3Bh 42h	8 I J
0718 38 0719 ff 071a ff 071a ff 071b 10 071c 54 071d 38 071e 49 071f b0 0720 4a 0721 ff 0722 ff 0723 3a 0724 44 0725 3b	33 33 33 33 33 33 33 33 33 33 33 33 33	FFh FFh 10h 54h 38h 49h B0h 4Ah FFh 5Ah 3Ah 44h 3Bh	8 I J : D

Ghidra .	- MC68705U3	35C BI	N			
Official	0729 10	_000.DI	10h			
	072a 59	??	59h	Y		
	072b 44	??	44h	D		
	072c 58	??	58h	х		
	072d ff	??	FFh			
	072e ff	??	FFh			
	072f a8	??	A8h			
	0730 43	??		C		
	0731 ab 0732 47	??	ABh 47h	G		
	0733 ff	??	FFh	9		
	0734 ff	??	FFh			
	0735 11	??	11h			
	0736 3f	??	3Fh	?		
	0737 aa	??	AAh			
	0738 4f	??	4Fh	0		
	0739 ae 073a 51	??	AEh 51h	Q		
	073b ff	??	FFh	¥		
	073c ff	??	FFh			
	073d ff	??	FFh			
	073e ff	??	FFh			
	073f ff	??	FFh			
	0740 ff 0741 ff	??	FFh			
	0741 II 0742 ff	??	FFh FFh			
	0743 aa	??	AAh			
	0744 55	??		U		
	0745 c6	??	C6h			
	0746 57	??	57h	W		
	0747 ff	??	FFh			
	0748 ff 0749 2b	??	FFh 2Bh	+		
	074a 53	??	53h	S		
	074b 80	??	80h	-		
	074c 46	??	46h	F		
	074d a8	??	A8h			
	074e 45	??	45h	E		
	074f ff	??	FFh			
	0750 ff 0751 81	??	FFh 81h			
	0752 50	??	50h	P		
	0753 83	??	83h			
	0754 41	??	41h	A		
	0755 85	??	85h			
	0756 52	??	52h	R		
	0757 ff 0758 ff	??	FFh FFh			
	0730 11	••	1111			
	DAT	_0759			XREF[1]:	FUN_0623:062c(R)
	0759 00	undefined1	00h			
	075a 20	2_075a undefined1	20h		XREF[1]:	FUN_0623:063d(R)
	0734 20	underrheur	2011			
	DAT	_075b			XREF[1]:	FUN_0623:062c(R)
	075b 77	undefined1	77h			_
	075c 30		30h	0		
	075d 11	??	11h	1		
	075e 31 075f 6b	??	31h 6Bh			
	0760 32	??	овп 32h	2		
	0761 3b	??	3Bh			
	0762 33	??	33h	3		
	0763 1d	??	1Dh			
	0764 34	??	34h			
	0765 3e	??	3Eh	>		
	0766 35 0767 7e	??	35h 7Eh	5 ~		
	0768 36	??	36h			
	0769 13	??	13h			
	076a 37	??	37h	7		
	076b 7 f	??	7Fh			
	076c 38	??	38h	8		
	076d 3f 076e 39	??	3Fh 39h	?		
	076f 50	??	50h	P P		
	0770 ff	??	FFh			
	0771 51	??	51h	Q		
	0772 ff	??	FFh			
	0773 54	??	54h	T		
	0774 ff 0775 55	??	FFh 55h	Ū		
	0776 ff	??	FFh	•		
	0777 4f	??	4Fh	0		

Ghidra - MC68705				
0778 20	??	20h		
0779 66	??	66h f		
077a 20	??	20h		
077b ff	??	FFh		
077c cf	??	CFh		
077d 4e	DAT_077d undefined	1 4Fb	XREF[1]:	FUN_0641:0662(R)
077d 4e	underined	1 4011		
	DAT_077e		XREF[2]:	FUN_0641:066a(R), FUN_0641:0686(R)
077e 09	undefined	1 09h		
	DAT 077f		XREF[1]:	FUN 0641:0662(R)
077f 80	undefined	1 80h		1011_001110002(11)
0780 00	??	00h		
0781 c5	??	C5h		
0782 00	??	00h		
0783 00	??	00h		
0784 20	??	20h		
0785 ff	??	FFh		
0786 ff	??	FFh		
0787 ff	??	FFh		
0788 ff	??	FFh		
0789 ff	??	FFh		
078a ff	??	FFh		
078b ff	??	FFh		
078c ff	??	FFh		
078d ff	??	FFh		
078e ff	??	FFh		
078f ff	??	FFh		
0790 ff	??	FFh		
0791 ff	??	FFh		
0792 ff	??	FFh		
0793 ff	??	FFh		
0794 ff	??	FFh		
0795 ff	??	FFh		
0796 ff	??	FFh		
0797 ff	??	FFh		
0798 ff	??	FFh		
0799 ff	??	FFh		
079a ff	??	FFh		
079b ff	??	FFh		
079c ff	??	FFh		
079d ff	??	FFh		
079e ff 079f ff	??	FFh FFh		
07a0 ff	??	FFh		
07a1 ff	??	FFh		
07a2 ff	??	FFh		
07a3 ff	??	FFh		
07a4 ff	??	FFh		
07a5 ff	??	FFh		
07a6 ff	??	FFh		
07a7 ff	??	FFh		
07a8 ff	??	FFh		
07a9 ff	??	FFh		
07aa ff	??	FFh		
07ab ff	??	FFh		
07ac ff	??	FFh		
07ad ff	??	FFh		
07ae ff	??	FFh		
07af ff	??	FFh		
07b0 ff	??	FFh		
07b1 ff	??	FFh		
07b2 ff	??	FFh		
07b3 ff	??	FFh		
07b4 ff 07b5 ff	??	FFh FFh		
07b6 ff	??	FFh		
07b7 ff	??	FFh		
07b8 ff	??	FFh		
07b9 ff	??	FFh		
07ba ff	??	FFh		
07bb ff	??	FFh		
07bc ff	??	FFh		
07bd ff	??	FFh		
07be ff	??	FFh		
07bf ff	??	FFh		
07c0 ff	??	FFh		
07c1 ff	??	FFh		
07c2 ff	??	FFh		
07c3 ff	??	FFh		
07c4 ff	??	FFh		

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07c6 ff	??	FFh
07c7 ff	??	FFh
07c8 ff	??	FFh
07c9 ff	??	FFh
07ca ff	??	FFh
07cb ff	??	FFh
07cc ff	??	FFh
07cd ff	??	FFh
07ce ff	??	FFh
07cf ff	??	FFh
07d0 ff	??	FFh
07d1 ff	??	FFh
07d2 ff	??	FFh
07d2 ff	??	FFh
07d4 ff	??	FFh
07d5 ff	??	FFh
07d6 ff	??	FFh
07d7 ff	??	FFh
07d8 ff	??	FFh
07d9 ff	??	FFh
07da ff	??	FFh
07db ff	??	FFh
07dc ff	??	FFh
07dd ff	??	FFh
07de ff	??	FFh
07df ff	??	FFh
07e0 ff	??	FFh
07e1 ff	??	FFh
07e2 ff	??	FFh
07e3 ff	??	FFh
07e4 ff	??	FFh
07e5 ff	??	FFh
07e6 ff	??	FFh
07e7 ff	??	FFh
07e8 ff	??	FFh
07e9 ff	??	FFh
07ea ff	??	FFh
07eb ff	??	FFh
07ec ff	??	FFh
07ed ff	??	FFh
07ee ff	??	FFh
07ef ff	??	FFh
07f0 ff	??	FFh
07f1 ff	??	FFh
07f2 ff	??	FFh
07f3 ff	??	FFh
07f4 ff	??	FFh
07f5 ff	??	FFh
07f6 ff	??	FFh
07f7 ff	??	FFh
07f8 00 d6	addr	RESET
07fa 00 d6	addr	RESET
07fc 00 d6	addr	RESET
07fe 00 d6	addr	RESET