By Chixun ZHANG Fall 2022

Chixun ZHANG

Room 401, Block F, Building 21, Binhe Huayuan Community, Longhua District, Shenzhen, Guangdong Province, 518000, P. R. China E-mail:Zhcx13534105151@163.com | Contact: +86 13534105151

Education Backgrounds

☆Xidian University (Xi'an, China)

09/2018-Present

School of Telecommunications Engineering

Degree: Bachelor of Engineering (Pending 06/2022)

Major: Telecommunications Engineering (GPA: 3.6/4.0 Weighted Average Score: 81.8/100)

Core Courses: Stochastic Signal Analysis, Digital Signal Processing, Circuit Signals and Systems Experiment, Fundamentals of Analog Electronic Technology, The Basis of RF Circuit, Principles of Communications, etc.

Standardized Tests

IELTS (Overall: **7.0** Listening: 8.0 Reading: 8.0 Writing: 6.5 Speaking: 6.0)

GRE (Total: **319** Verbal: 150 44% Quantitative: **169** 94% AW: 3.5 37%)

October 17, 2021

Research Experiences

★ Optical Signal Processing: Application of Apodizing Masks to Optimize Image Quality

03/2021-06/2021

- Generated a point spread function for an aperture-based imaging system by 2-D Fast Fourier Transform and 2D Gaussian distribution;
- Conducted a flat-top signal (uniformly illuminated circular unobstructed aperture) and a pupil-masked two-dimensional Fourier transform and pass through an inverse oscillation filter
- Utilized Matlab and Octave Online to simulate the signals;
- Compare them by analyzing the centrality of the spectrum, frequency distribution, and energy distribution;
- Wrote a paper and published in EI compendex as the independent first author;

Publication

• Chixun ZHANG, A Method for Generating PSF Based on 2-D Fast Fourier Transform, has been accepted by the International Conference on Signal Processing and Machine Learning (CONF-SPML 2021), will be published in conference proceeding and submitted to EI Compendex, CPCI (Web of Science) and other databases for indexing.

Internship Experiences

Shenzhen iSoftstone Information Technology Co., Ltd. (Intern)

07/2019-08/2019

- Participated in the auxiliary source test of controller board hardware;
- Tested the precision, ripple, power-on and power-off waveform, load capacity, overcurrent protection and other aspects of auxiliary source.

Shenzhen Huazhi Information Technology Co., Ltd. (Software Design Engineer Assistant)

07/2021-08/2021

- Participated in the products research, development, and design work of the company;
- Developed practical working ability and operational skills.

Awards & Scholarships

Second Prize of Mathematical Contest in Modeling (University)	06/2020
Third Prize of "Spark Cup" Extracurricular Academic Science and Technology Works (University)	11/2019
Third-class Scholarship (University)	9/2021

Computer Skills

C programming language; Assembly language; Matlab; Python