## **Boolean Algebra**

Opening up a computer, a terminal, or practically any other "computerized" item reveals boards containing little black rectangles. These little black rectangles are the integrated circuit (IC) chips that perform the logic of the computer. Each IC can be represented by a Boolean Algebra equation, and vice versa. The representation that is used depends on the context. Boolean Algebra provides a convenient representation and notation for simplifying and solving equations. Digital Electronics provides a layout that can then be implemented with IC chips.

The operators used in these categories are listed in the description of the Digital Electronics category. The logic gates are usually used in Digital Electronics questions; the algebraic equations, symbols and truth tables, in Boolean Algebra. Of course, it is crucial to be able to translate between a digital electronics circuit and its Boolean Algebra notation. The order of operator precedence is NOT; AND and NAND; XOR and EQUIV; OR and NOR. Binary operators with the same level of precedence are evaluated from left to right.

## References

Handbooks for building circuits using IC are available in most electronics and home computer stores. These contain many example circuits. A formal description of the material can be found in textbooks covering discrete mathematics, finite mathematics and compute hardware and architecture. Example texts include:

Lipschutz, Seymour. Essential Computer Mathematics – Shaum's Outline Series, McGraw Hill (1982), Chapters 6, 7 and 8.

Mano, M. Morris. Digital Logic and Computer Design, Prentice-Hall (1979).

Preparata, Franco P. *Introduction to Computer Engineering*, Harper & Row (1985).

## **Boolean Algebra Identities**

1. 
$$A+B=B+A$$
  $A*B=B*A$  (Communicative Property)  
2.  $A+(B+C)=(A+B)+C$   $A*(B*C)=(A*B)*C$  (Associative Property)  
3.  $A*(B+C)=A*B+A*C$  (Distributive Property)  
4.  $\overline{A+B}=\overline{A}*\overline{B}$  (DeMorgan's Law)  
5.  $\overline{A*B}=\overline{A}+\overline{B}$  (DeMorgan's Law)  
6.  $A+0=A$   $A*0=0$   
7.  $A+1=1$   $A*1=A$   
8.  $A+\overline{A}=1$   $A*\overline{A}=0$   
9.  $A+A=A$   $A*A=A$   
10.  $\overline{\overline{A}}=A$ 

11. 
$$A + \overline{A} * B = A + B$$

12. 
$$(A+B)*(A+C) = A+B*C$$

13. 
$$(A+B)*(C+D) = A*C + A*D + B*C + B*D$$

14. 
$$A*(A+B) = A$$

15. 
$$A \oplus B = A * \overline{B} + \overline{A} * B$$

16. 
$$\overline{A \oplus B} = \overline{A} \oplus B = A \oplus \overline{B}$$

## **Sample Programs**

Simplify the following expression as much as possible:

 $\overline{A(A+B)} + B\overline{A}$ 

Using various elementary identities, the expression simplifies as follows:

$$\overline{A(A+B)} + B\overline{A} = \overline{A(A+B)} * \overline{B} * \overline{A}$$

$$= A(A+B) * (\overline{B} + A)$$

$$= (A+AB)(\overline{B} + A)$$

$$= A(1+B)(\overline{B} + A)$$

$$= A(1)(\overline{B} + A)$$

$$= A(\overline{B} + A)$$

$$= A\overline{B} + AA$$

$$= A\overline{B} + A$$

$$= A(\overline{B} + 1)$$

$$= A(1) = A$$

Find all ordered pairs (A, B) that make the following expression TRUE.

$$\overline{\overline{A+B}} + \overline{A} * B$$

$$\overline{A+B} + \overline{A} * B = (\overline{A+B})(\overline{AB})$$

$$= (A+B)(A+\overline{B})$$

$$= AA + A\overline{B} + BA + B\overline{B}$$

$$= A + A(B+\overline{B}) + 0$$

$$= A + A(1) = A + A = A$$

This yields the solutions (1, 0) and (1, 1). This problem, like most Boolean Algebra problems, could also be solved by drawing a truth table with the following seven column headings: A, B, A+B,

$$\overline{A+B}, \overline{AB}, \overline{A+B}+\overline{AB}, \overline{A+B}+\overline{AB}$$
.

Simplify the following expression to one that uses only two operators.

$$(\overline{\overline{A} + \overline{B}} * \overline{C}) + (A*\overline{(B + \overline{C})})$$

The evaluation is as follows:

$$(\overline{\overline{A} + \overline{B}} * \overline{C}) + (A*(\overline{B + \overline{C}}))$$

$$= (A*B*\overline{C}) + (A*\overline{B}*C)$$

$$= A*(B*\overline{C} + \overline{B}*C)$$

$$= A*(B \oplus C)$$

To realize this equation as a circuit, two gates are used: an XOR (input is B and C) and an AND (inputs are A and the output of the XOR gate). The output of the AND is the output of the circuit.