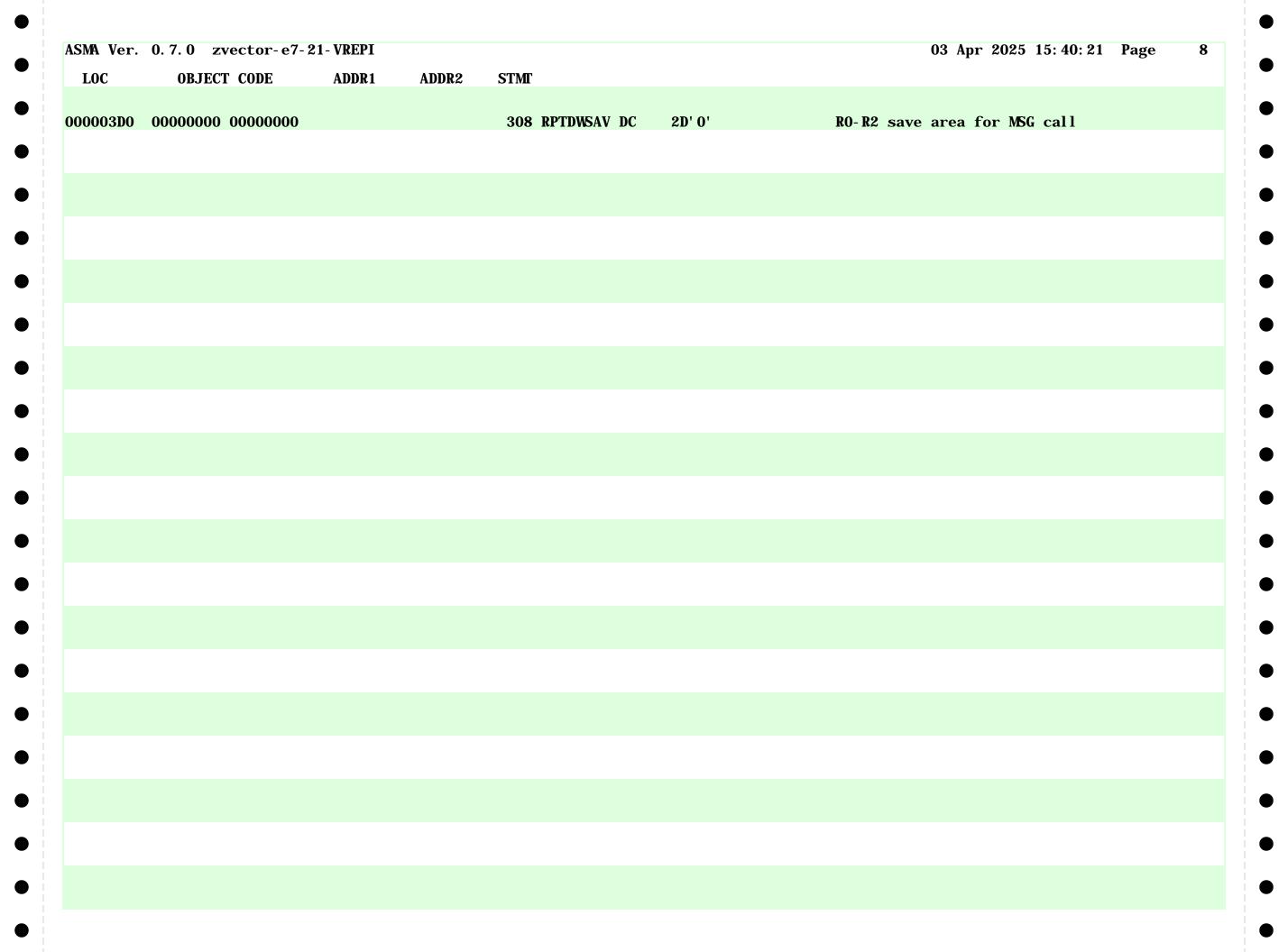
SMA Ver.	0. 7. 0 zvector-e7-	- 21 - VREPI		03 Apr 2025 15: 40: 21 Page 1
LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				2 ************************************
				4 * Zvector E7 instruction tests for VRI-a instruction: 5 *
				6 * E745 VREPI - Vector Replicate Immediate 7 *
				8 * James Wekel February 2025 9 ************************************
				11 *******************
				12 * 13 * basic instruction tests 14 *
				15 ************************************
				18 * Exceptions are not tested. 19 *
				20 * PLEASE NOTE that the tests are very SIMPLE TESTS designed to catch 21 * obvious coding errors. None of the tests are thorough. They are 22 * NOT designed to test all aspects of any of the instructions. 23 *
				24 ************************************
				27 * * 28 * * Zvector E7 instruction tests for VRI-a instruction: 29 * *
				30 * * E745 VREPI - Vector Replicate Immediate 31 * * 32 * * #
				33 * * # This tests only the basic function of the instruction. 34 * * # Exceptions are NOT tested. 35 * * #
				36 * * * 37 * mainsize 2 38 * numcpu 1
				39 * sysclear 40 * archlyl z/Arch
				41 * 42 * loadcore "\$(testpath)/zvector-e7-21-VREPI.core" 0x0 43 *
				44 * diag8cmd enable # (needed for messages to Hercules console) 45 * runtest 5 46 * diag8cmd disable # (reset back to default)
				47 * 48 * *Done 49 * 50 **********************************
				50 ********************

MA Ver.	0. 7. 0 zvector- e7-	21-VREPI			03 Apr 2025 15: 40: 2	21 Page 2
LOC	OBJECT CODE	ADDR1	ADDR2	STMI		
				52 *****	*******************	****
				<b>53</b> *	FCHECK Macro - Is a Facility Bit set?	
				54 * 55 *	If the facility bit is NOT set, an message is issued and	ıd
				56 * 57 *	the test is skipped.	
				<b>58</b> *	Fcheck uses R0, R1 and R2	
				59 * 60 * eg	FCHECK 134, 'vector-packed-decimal'	
				60 * eg. 61 *****	*******************	*****
				62 63	MACRO FCHECK &BITNO, &NOTSETMSG	
				64 · * 65 · *	&BITNO: facility bit number to check	
				66	&NOTSETMSG: 'facility name' LCLA &FBBYTE Facility bit in Byte	
				67 68	LCLA &FBBIT Facility bit within Byte	
				69	LCLA &L(8)	
				70 &L(1) 71	SetA 128, 64, 32, 16, 8, 4, 2, 1 bit positions within byte	
				72 &FBBYT	SETA &BITNO/8	
				73 &FBBIT 74 .*	SETA &L((&BITNO-(&FBBYTE*8))+1) MNOTE 0, 'checking Bit=&BITNO: FBBYTE=&FBBYTE, FBBIT=&FB	BBIT'
				75 76	B X&SYSNDX	
				77 *	Fcheck data area	
				78 * 79 SKT&SYS	skip messgae OX DC C' Skipping tests: '	
				80	DC C&NOTSETMSG	
				81 82 SKL&SYS	DC C' (bit &BITNO) is not installed.'  OX EQU *-SKT&SYSNDX	
				83 *	facility bits	
				84 85 FB&SYSI	DS FD gap K DS 4FD	
				86 87 *	DS FD gap	
				88 X&SYSNI		
				89 90	LA RO, ((X&SYSNDX-FB&SYSNDX)/8)-1 STFLE FB&SYSNDX get facility bits	
				91	S v	
				92 93	XGR RO, RO IC RO, FB&SYSNDX+&FBBYTE get fbit byte	
				94	N RO, =F' &FBBIT' is bit set?	
				95 96 *	BNZ XC&SYSNDX	
				97 * facil 98 *	y bit not set, issue message and exit	
				99	LA RO, SKL&SYSNDX message length	
				100 101	LA R1, SKT&SYSNDX message address BAL R2, MSG	
				102		
				103 104 XC&SYSI	B EOJ K EQU *	
				105	MEND	

ASMA Ver.	0. 7. 0 zvector- e7- 2	1-VREPI					03 Apr 2025 15: 40: 21 Page	3
LOC	OBJECT CODE	ADDR1	ADDR2	STM				
				107 ****** 108 * 109 *****	Low c	ore PSWs	***********	
00000000		00000000 00000000	0000219F	110 ZVE7TST 111	START		Low core addressability	
		00000140	00000000	112 113 SVOLDPS	W EQU	ZVE7TST+X' 140'	z/Arch Supervisor call old PSW	
00000000 000001A0 000001A8	00000001 80000000 00000000 00000200	00000000	000001A0	115 116 117	ORG DC DC	ZVE7TST+X' 1A0' X' 000000018000000 AD(BEGIN)	z/Architecure RESTART PSW	
000001110	0000000 0000000			11,	DC	nD (DDd111)		
000001B0 000001D0 000001D8	00020001 80000000 00000000 0000DEAD	000001B0	000001D0	119 120 121	ORG DC DC	ZVE7TST+X' 1D0' X' 000200018000000 AD(X' DEAD')	z/Architecure PROGRAM CHECK PSW 00'	
000001E0		000001E0	00000200	123	ORG	ZVE7TST+X' 200'	Start of actual test program	
				125 ******	*****	*******	**********	
				126 * 127 ******	*****	The actual "ZVE"	7TST" program itself	
				128 * 129 * Arch 130 * Regis		e Mode: z/Arch		
				131 * 132 * R0 133 * R1-		work) work)		
				134 * R5 135 * R6-1	Ť		ble - current test base	
				136 * R8 137 * R9	F	irst base registe	r	
				138 * R10 139 * R11	T	econd base registe hird base register 7TEST call return	r	
				140 * R12 141 * R13	<b>E</b>	7TESTS register work)		
				142 * R14 143 * R15		ubroutine call econdary Subroutii	ne call or work	
				144 * 145 *****			***********	
00000200 00000200		00000200 00001200		147 148	USI NG USI NG	BEGIN, R8 BEGIN+4096, R9	FIRST Base Register SECOND Base Register	
00000200		00001200		149		BEGIN+8192, R10	THIRD Base Register	
00000200	0580			151 BEGIN	BALR		Initalize FIRST base register	
00000202 00000204	0680 0680			152 153	BCTR BCTR		Initalize FIRST base register Initalize FIRST base register	
	4190 8800 4190 9800		00000800 00000800	155 156 157	LA LA	R9, 2048(, R8) R9, 2048(, R9)	Initalize SECOND base register Initalize SECOND base register	

ASMA Ver.	0. 7. 0 zvector- e7-	21- VREPI					03 Apr 2025 15: 40: 21 Page	6
LOC	OBJECT CODE	ADDR1	ADDR2	STMI				
				225 ******	*****	*******	************	
				226 * result	not a	s expected:		
				227 *	i ssue	e message with to	est number, instruction under test	
				228 *	< * * * * * * * * * * * * * * * * * * *	and instruction	n 12 ************************************	
		00000304	0000001	230 FAILMSG		*		
00000304	45F0 8126	00000304	00000326	231	BAL	R15, RPTERROR		
0000001	1010 0120		0000000	701	2.12	2010, 22 12220020		
							***************	
				234 * contine 235 *******	iue aft	ter a failed test	t ****************	
		00000308	00000001	236 FAILCONT	r <b>foi</b> i	*		
00000308	5800 82D4	00000308	0000001 000004D4	237	L	R0, =F'1'	set failed test indicator	
0000030C	5000 8E00		00001000	238	ST	RO, FAILED	See Turreu cose mureucor	
				239		•		
00000310	41C0 C004		00000004	240	LA	R12, 4(0, R12)	next test address	
00000314	47F0 80D4		000002D4	241	В	NEXTE7		
				243 ******	*****	******	************	
				244 * end of	f testi	ng; set ending j	psw	
				245 ******	******	*******	*************	
		00000318	00000001	246 ENDTEST	EQU	*		
00000318	5810 8E00		00001000	247	L	R1, FAILED	did a test fail?	
0000031C 0000031E	1211 4780 82A8		000004A8	248 249	LTR BZ	R1, R1	No ovit	
0000031E	4780 82A8 47F0 82C0		000004A8 000004C0	249 250	BZ B	EOJ FAI LTEST	No, exit Yes, exit with BAD PSW	
00000022	1/10 0200		0000400	200	D		105, CALC WICH DAD IOW	

ASMA Ver.	0. 7. 0 zvector- e	7-21-VREPI						03 Apr 2025 15: 40: 21 Page 7
LOC	OBJECT CODE	ADDR1	ADDR2	STMI				
								**********
				253 254	* *****	<b>RPTER</b> * * * * *	<b>PROR</b> :******************	<b>Report instruction test in error</b> **********************************
				234				
00000326	50F0 81C8		000003C8		RPTERROR		R15, RPTSAVE	Save return address
000032A	5050 81CC		000003CC	257 258	*	ST	R5, RPTSVR5	Save R5
000032E	4820 5004		0000004	<b>259</b>		LH	R2, TNUM	get test number and convert
00000332	4E20 8E82		00001082	260		CVD	R2, DECNUM	
	D211 8E6C 8E56 DE11 8E6C 8E82		00001056 00001082	261 262		MVC ED	PRT3, EDIT PRT3, DECNUM	
	D202 8E18 8E79		00001032	263		MVC	PRTNUM(3), PRT3+13	fill in message with test #
20000040	D007 0F00 7004	00001000	0000001	<b>264</b>		3.570		<u> </u>
00000348	D207 8E33 500A	00001033	000000A	265 266	*	MVC	PRTNAME, OPNAME	fill in message with instruction
000034E	4820 5008		00000008	<b>267</b>		LH	R2, I2	get i2 and convert
00000352	4E20 8E82		00001082	268		CVD	R2, DECNUM	
00000356 0000035C	D211 8E6C 8E56 DE11 8E6C 8E82		00001056 00001082	269 270		MVC ED	PRT3, EDIT PRT3, DECNUM	
	D204 8E45 8E77		00001032	271		MVC	PRTI 2(5), PRT3+11	fill in message with i2 field
2000000	1000			272	*	TED	DO DO	· ·
00000368 0000036A	1222 4780 817C		0000037C	273 274		LTR BZ	R2, R2 I 2ZERO	is I2 zero
0000036E	4740 8186		00000376	275		BL	I 2NEG	is I2 negative
	D200 8E44 82DC		000004DC	276		MVC	PRTSGN(1), =C'+'	<u>c</u>
00000378	47F0 818C		0000038C	277 278		В	DOM4	
000037C					I 2ZERO	DS	ОН	
0000037C	D200 8E44 82DD		000004DD	280		MVC	PRTSGN(1), =C''	
00000382	47F0 818C		0000038C	281 282		В	DOM4	
00000386					I 2NEG	DS	ОН	
00000386	D200 8E44 82DE	00001044	000004DE	284	<b>*</b>	MVC	<b>PRTSGN(1)</b> , =C'-'	
000038C				285 286		DS	ОН	
0000038C	E320 5007 0076		0000007	287		LB	R2, M3	get m34 and convert
00000392	4E20 8E82		00001082	288		CVD	R2, DECNUM	
	D211 8E6C 8E56 DE11 8E6C 8E82		00001056 00001082	289 290		MVC ED	PRT3, EDIT PRT3, DECNUM	
	D201 8E53 8E7A		0000107A	291		MVC	PRTM3(2), PRT3+14	fill in message with m4 field
				292		T1 T	r l n	
				293 294		use H	lercules Diagnose to	r Message to console
	9002 81D0		000003D0	<b>295</b>		STM	RO, R2, RPTDWSAV	save regs used by MSG
	4100 004E		0000004E	296		LA	RO, PRTLNG	message length
000003B0 000003B4	4110 8E08 4520 81E0		00001008 000003E0	297 298		LA BAL	R1, PRTLINE R2, MSG	messagfe address call Hercules console MSG display
	9802 81D0		000003E0	299		LM	RO, R2, RPTDWSAV	restore regs
00003BC	5850 81CC		000003CC	301		L	R5, RPTSVR5	Restore R5
000003C0	58F0 81C8		000003C8	302		L	R15, RPTSAVE	Restore return address
00003C4	07FF			303		BR	R15	Return to caller
00003C8	0000000			305	RPTSAVE	DC	F' 0'	R15 save area
	00000000					DC	F' 0'	R5 save area



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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
				310 ******* 311 * 312 * 313 ******	Issue	HERCULES MESSAGE poin R2 = return address	**************************************
000003E0 000003E4	4900 82D8 07D2		000004D8	315 MSG 316	CH BNHR	RO, =H' O' R2	Do we even HAVE a message? No, ignore
000003E6	9002 821C		0000041C	318	STM	RO, R2, MSGSAVE	Save registers
000003EA 000003EE 000003F2	4900 82DA 47D0 81F6 4100 005F		000004DA 000003F6 0000005F	320 321 322	CH BNH LA	RO, =AL2(L' MSGMSG) MSGOK RO, L' MSGMSG	Message length within limits? Yes, continue No, set to maximum
000003F6 000003F8 000003FA	1820 0620 4420 8228		00000428	324 MSGOK 325 326	LR BCTR EX	R2, R0	Copy length to work register Minus-1 for execute Copy message to O/P buffer
000003FE 00000402	4120 200A 4110 822E		0000000A 0000042E	328 329	LA LA	R2, 1+L' MSGCMD(, R2) R1, MSGCMD	Calculate true command length Point to true command
00000406 0000040A	83120008 4780 8216		00000416	331 332	DC BZ	X' 83', X' 12', X' 0008' MSGRET	Issue Hercules Diagnose X'008' Return if successful
0000040E 00000410	1222 4780 8216		00000416	333 334 335	LTR BZ	R2, R2 MSGRET	Is Diag8 Ry (R2) 0? an error occurred but coninue
00000414	0000			336 337	DC	Н' О'	CRASH for debugging purposes
00000416 0000041A	9802 821C 07F2		0000041C	339 MSGRET 340	LM BR	RO, R2, MSGSAVE R2	Restore registers Return to caller
0000041C 00000428	00000000 00000000 D200 8237 1000	00000437	00000000	342 MSGSAVE 343 MSGMVC	DC MVC	3F' 0' MSGMSG(0), 0(R1)	Registers save area Executed instruction
	D4E2C7D5 D6C8405C 40404040 40404040			345 MSGCMD 346 MSGMSG 347	DC DC	C' MSGNOH * ' CL95' '	*** HERCULES MESSAGE COMMAND *** The message text to be displayed

ASMA Ver.	0. 7. 0 zvector-e7-2	21 - VREPI						03	Apr 2025 15: 40: 21	Page	10
LOC	OBJECT CODE	ADDR1	ADDR2	STM							
				350	******* * ******	******* Normal *****	**************************************	**************************************	**************************************		
00000400	0000001 0000000			050	EQ IDCIU	D.C.	OBLOL VI 000000	00100000001 AB(0)			
	00020001 80000000				<b>EOJPSW</b>			00180000000', AD(0)			
000004A8	B2B2 8298		00000498	355	EOJ	LPSWE	EOJPSW	Normal compl	etion		
000004B0	00020001 80000000			357	<b>FAI LPSW</b>	DC	OD' O' , X' 000200	0180000000' , AD(X' BA	AD')		
000004C0	B2B2 82B0		000004B0	359	FAI LTEST	LPSWE	<b>FAILPSW</b>	Abnormal ter	rmi nati on		
				361 362 363	******* * *****	****** Worki r *****	**************************************	*******	**********************	*****	
000004C4	0000000			365	CTLRO	DS	F	CRO			
000004C8	00000000			366		DS	F				
000004CC	00000040			368		LTORG	, El 0.41	Literals pool			
000004D0 000004D4	00000040 000020FC 00000001			369 370 371			=F' 64' =A(E7TESTS) =F' 1'				
000004D8 000004DA 000004DC				372 373 374			=H'0' =AL2(L'MSGMSG) =C'+'				
000004DD 000004DE				375 376 377			=C' ' =C' - '				
				378 379	*	some o	constants				
		00000400		380		EQU	1024	One KB			
		00001000 00010000 00100000	00000001 00000001 00000001	382 383		EQU EQU EQU	(4*K) (64*K) (K*K)	Size of one page 64 KB 1 MB	2		
		AABBCCDD 000000DD	00000001 00000001		REG2PATT REG2LOW		X' AABBCCDD' X' DD'	Polluted Register (last byte above)	pattern		

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LOC	OBJECT CODE	ADDR1	ADDR2	STM			
				433 *	E7TES	T DSECT	*************
00000000	00000000			436 E7TI 437 TSUI		, A(0)	pointer to test
0000004	0000			438 TNUN 439		H' 00' X' 00'	Test Number
	00			440 441 M3	DC	HL1' 00'	m3 field
	0000			442 I2 443	DC	XL2' 00'	i 2 used
00000014 00000018	40404040 40404040 00000000 00000000			444 OPNA 445 V2AI 446 V3AI	DDR DC DDR DC	CL8' ' A(0) A(0)	E7 name address of v2 source address of v3 source
00000020 00000028				447 RELI 448 REAI 449 450 V10U	DDR DC DS	A(0) A(0) FD XL16	RESULT LENGTH result (expected) address gap V1 Output
	0000000 0000000			450 V100 451 452 453 *	DS	FD	gap be here (from VRI-a macro)
				454 * 455 * 456 *		wed by EXPECTED RES	
000010C4		00000000	0000219F	458 ZVE7 459	7TST CSECT DS	, OF	
00001004				433	JЗ	<b>U</b> r	
				461 **** 462 * 463 ****	Macros t	**************************************	**************************************
				407 11			
				<b>467</b> *	_	nerate indivi	dual test
				468 469 470 .*	MACRO VRI_A	&INST, &I2, &M	B &INST - VRI-a instruction under test
				470 . * 471 . * 472 . * 473			&i2 - i2 field (signed decimal) &m3 - element size
				474 475 &TNU 476	UM GBLA SETA		
				477 478 479	DS USING	•	base for test data and test routine
				480 T&TN 481 482	NUM DC DC DC	A(X&TNUM) H' &TNUM' X' 00'	address of test routine test number

ASMA Ver.	0. 7. 0 zvector- e7- 2	21- VREPI					03 Apr 2025 15: 40: 21 Page 15
LOC	OBJECT CODE	ADDR1	ADDR2	STMI			
							**********
				531 * 532 ******	E7 VR.	I-a tests ******	**********
				533	PRINT		
				534 * 535 * E745	VREPI	- Vector Rep	licate Immediate
				<b>536</b> *		-	
				537 * 538 *	VK1_A	followed by	I2 (signed decinal), MB
				539 * 540 *		16 byte ex	pected result (V1)
				541 * VREPI	- Vec	tor Replicate	Immediate
				542 * 543 *Byte			
00004055				<b>544</b>		VREPI, 0, 0	
000010C8 000010C8		000010C8		545+ 546+	DS USING	OFD *. R5	base for test data and test routine
000010C8	00001110			547+T1	DC	A(X1)	address of test routine
000010CC 000010CE	0001 00			548+ 549+	DC DC	H' 1' X' 00'	test number
000010CF 000010D0	00 0000			550+ 551+	DC DC	HL1' 0' HL2' 0'	m3 field i2 used
000010D2	E5D9C5D7 C9404040			<b>552</b> +	DC	CL8' VREPI'	instruction name
000010DC 000010E0	00001134 00001144			553+ 554+	DC DC	A(RE1+16) A(RE1+32)	address of v2 source address of v3 source
000010E4	0000010			<b>555</b> +	DC	A(16)	result length
000010E8 000010F0				556+REA1 557+	DC DS	A(RE1) FD	result address gap
000010F8 00001100				558+V101	DS	XL16	gap V1 output
00001100				559+	DS	FD	gap
00001110				560+* 561+X1	DS	<b>OF</b>	
	E760 8EA4 0806 E760 0000 0845		000010A4	562+ 563+	VL	V22, V1FUDGE	test instruction (dest is a source)
0000111C	E760 5030 080E		000010F8	<b>564</b> +	VST	V22, 0, 0 V22, V101	test instruction (dest is a source) save v1 output
00001122 00001124	07FB			565+ 566+RE1	BR DC	R11 OF	return xl16 expected result
00001124				<b>567</b> +	DROP	<b>R</b> 5	-
00001124 0000112C	00000000 00000000 0000000 00000000			568	DC	16HL1' 0'	result
				569 570	VDT A	VREPI, 1, 0	
00001138				<b>571</b> +	DS	OFD	
$00001138 \\ 00001138$	00001180	00001138		572+ 573+T2	USI NG DC	*, R5 A(X2)	base for test data and test routine address of test routine
0000113C	0002			<b>574</b> +	DC	H' 2'	test number
0000113E 0000113F	00 00			575+ 576+	DC DC	X' 00' HL1' 0'	m3 field
00001140 00001142	0001 E5D9C5D7 C9404040			577+ 578+	DC DC	HL2' 1' CL8' VREPI'	i2 used
0000114C	000011A4			<b>579</b> +	DC	A(RE2+16)	instruction name address of v2 source
00001150 00001154	000011B4 00000010			580+ 581+	DC DC	A(RE2+32) A(16)	address of v3 source result length
00001158	00001194			582+REA2	DC	A(RE2)	result address
00001160	0000000 00000000			583+	DS	FD	gap

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LOC	OBJECT CODE	ADDR1	ADDR2	STMI			
$00001168 \\ 00001170$	00000000 00000000 0000000 00000000			584+V102	DS	XL16	V1 output
00001178	0000000 00000000			585+ 586+*	DS	FD	gap
00001180	E700 0E44 0000		00001014	587+X2	DS	OF	
00001180 00001186 0000118C	E760 8EA4 0806 E760 0001 0845 E760 5030 080E		000010A4 00001168	588+ 589+ 590+	VL VREPI VST	V22, V1FUDGE V22, 1, 0 V22, V102	test instruction (dest is a source) save v1 output
00001192 00001194	07FB		00001100	591+ 592+ <b>RE</b> 2	BR DC	R11 0F	return xl 16 expected result
00001194	01010101 01010101			593+	DROP	R5	-
00001194 0000119C	01010101 01010101 01010101 01010101			594 595	DC	16HL1' 1'	resul t
000011A8				596 597+	VRI_A DS	VREPI, 2, 0 OFD	
000011A8		000011A8		<b>598</b> +	USING		base for test data and test routine
000011A8 000011AC	000011F0 0003			599+T3 600+	DC DC	A(X3) H' 3'	address of test routine test number
000011AE	00			<b>601</b> +	DC	X' 00'	
000011AF 000011B0	00 0002			602+ 603+	DC DC	HL1' 0' HL2' 2'	m3 field i2 used
000011B0 000011B2	E5D9C5D7 C9404040			604+	DC DC	CL8' VREPI'	instruction name
000011BC	00001214			605+	DC	A(RE3+16)	address of v2 source
000011C0 000011C4	00001224 00000010			606+ 607+	DC DC	A(RE3+32) A(16)	address of v3 source result length
000011C8	00001204			608+REA3	DC	A(RE3)	result address
000011D0 000011D8	0000000 0000000 0000000 0000000			609+ 610+V103	DS DS	FD XL16	gap V1 output
000011E0 000011E8	00000000 00000000			611+ 612+*	DS	FD	gap
000011F0	EZCO OELA OOOC		00001014	613+X3	DS	OF	
000011F0 000011F6	E760 8EA4 0806 E760 0002 0845		000010A4	614+ 615+	VL VREPI	V22, V1FUDGE V22, 2, 0	test instruction (dest is a source)
000011FC	E760 5030 080E		000011D8	616+	VST	V22, V103	save v1 output
00001202 00001204	07FB			617+ 618+RE3	BR DC	R11 OF	return xl16 expected result
00001204				619+	DROP	<b>R</b> 5	Allo expected result
00001204 0000120C	02020202 02020202 02020202 02020202			620	DC	16HL1'2'	result
				621 622	VRI A	VREPI, 99, 0	
00001218		00001015		623+	DS	OFD	
$00001218 \\ 00001218$	00001260	00001218		624+ 625+T4	USI NG DC	*, <b>R</b> 5 A(X4)	base for test data and test routine address of test routine
0000121C	0004			<b>626</b> +	DC	H' 4'	test number
0000121E	00			627+	DC	X' 00'	o €: al d
0000121F 00001220	00 0063			628+ 629+	DC DC	HL1' 0' HL2' 99'	m3 field i2 used
00001222	E5D9C5D7 C9404040			630+	DC	CL8' VREPI'	instruction name
0000122C 00001230	00001284 00001294			631+ 632+	DC DC	A(RE4+16) A(RE4+32)	address of v2 source address of v3 source
00001230	00001234			633+	DC	A(16)	result length
$00001238 \\ 00001240$	00001274 00000000 00000000			634+REA4 635+	DC DS	A(RE4) FD	result address gap

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LOC	OBJECT CODE	ADDR1	ADDR2	STMI			
$00001248 \\ 00001250$	00000000 00000000 0000000 00000000	)		636+V104	DS	XL16	V1 output
00001258	0000000 0000000	)		637+ 638+*	DS	FD	gap
00001260	EZCO OELA OOOC		00001014	639+X4	DS	OF	
00001260 00001266	E760 8EA4 0806 E760 0063 0845		000010A4	640+ 641+	VL VREPI	V22, V1FUDGE V22, 99, 0	test instruction (dest is a source)
0000126C	E760 5030 080E		00001248	642+	VST	V22, V104	save v1 output
00001272	07FB			643+	BR	R11	return
00001274 00001274				644+RE4 645+	DC DROP	0F R5	xl16 expected result
00001274	63636363 63636363	3		646	DC	16HL1' 99'	result
0000127C	63636363 63636363						
				647 648	VDT A	VREPI, 999, 0	
00001288				649+	DS DS	0FD	
00001288		00001288		<b>650</b> +	USING	*, <b>R</b> 5	base for test data and test routine
00001288	000012D0			651+T5	DC	A(X5)	address of test routine
0000128C 0000128E	0005 00			652+ 653+	DC DC	H' 5' X' 00'	test number
0000128F	00			<b>654</b> +	DC	HL1' 0'	m3 field
00001290	03E7			655+	DC	HL2' 999'	i 2 used
00001292 0000129C	E5D9C5D7 C9404040 000012F4	)		656+ 657+	DC DC	CL8' VREPI' A(RE5+16)	instruction name address of v2 source
0000123C	00001214			658+	DC	A(RE5+10) A(RE5+32)	address of v2 source
000012A4	0000010			<b>659</b> +	DC	A(16)	result length
000012A8 000012B0	000012E4 00000000 00000000	<b>.</b>		660+REA5 661+	DC DS	A(RE5) FD	result address
000012B0 000012B8	0000000 0000000			662+V105	DS DS	XL16	gap V1 output
000012C0	00000000 00000000	)					· · · · · · · · · · · · · · · · · · ·
000012C8	0000000 0000000	)		663+ 664+*	DS	FD	gap
000012D0	E700 OEAA 0000		00001014	665+X5	DS	OF	
000012D0 000012D6	E760 8EA4 0806 E760 03E7 0845		000010A4	666+ 667+	VL VREPI	V22, V1FUDGE V22, 999, 0	test instruction (dest is a source)
000012DC	E760 5030 080E		000012B8	668+	VST	V22, V105	save v1 output
000012E2	07FB			669+	BR	R11	return
000012E4 000012E4				670+RE5 671+	DC DROP	OF R5	xl16 expected result
000012E4	<b>E7E7E7E7 E7E7E7E</b> 3	7		672	DC	ко 16HL1' 999'	result
000012EC	E7E7E7E7 E7E7E7E7	7		679			
				673 674	VRT A	VREPI, - 1, 0	
000012F8				675+	DS	OFD	
000012F8	00001040	000012F8		676+	USING		base for test data and test routine
000012F8 000012FC	00001340 0006			677+T6 678+	DC DC	A(X6) H' 6'	address of test routine test number
000012FE	00			679+	DC	X' 00'	
000012FF	00			680+	DC	HL1' 0'	m3 field
00001300 00001302	FFFF E5D9C5D7 C9404040	)		681+ 682+	DC DC	HL2' - 1' CL8' VREPI'	i2 used instruction name
00001302 0000130C	00001364	,		683+	DC DC	A(RE6+16)	address of v2 source
00001310	00001374			<b>684</b> +	DC	A(RE6+32)	address of v3 source
00001314 00001318	00000010 00001354			685+ 686+REA6	DC DC	A(16) A(RE6)	result length result address
00001318	00001354	)		687+	DC DS	A(REO) FD	gap
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LOC	OBJECT CODE	ADDR1	ADDR2	STMI				
$00001328 \\ 00001330$	00000000 00000000 0000000 00000000	)		688+V106	DS	XL16	V1 output	
00001338	0000000 00000000	)		689+ 690+*	DS	FD	gap	
00001340	E700 OEA4 0000		00001014	691+X6	DS	OF		
00001340 00001346	E760 8EA4 0806 E760 FFFF 0845		000010A4	692+ 693+	VL VRFPI	V22, V1FUDGE V22, - 1, 0	test instruction (dest is a source)	
0000134C	E760 5030 080E		00001328	694+	VST	V22, V106	save v1 output	
00001352	07FB			695+	BR	R11	return	
00001354 00001354				696+RE6 697+	DC DROP	OF R5	xl16 expected result	
00001354	FFFFFFFF FFFFFFFF	7		698	DC	16HL1' - 1'	result	
	FFFFFFFF FFFFFFFF							
				699 700	VRT A	VREPI, - 22, 0		
00001368				<b>701</b> +	DS	OFD		
00001368	00001070	00001368		702+	USING		base for test data and test routine	
00001368 0000136C	000013B0 0007			703+T7 704+	DC DC	A(X7) H' 7'	address of test routine test number	
0000136E	0007			704+ 705+	DC	X' 00'	test number	
0000136F	00			706+	DC	HL1' 0'	m3 field	
$00001370 \\ 00001372$	FFEA E5D9C5D7 C9404040	1		707+ 708+	DC DC	HL2' - 22' CL8' VREPI'	i2 used instruction name	
00001372 0000137C	000013D4	,		708+ 709+	DC	A(RE7+16)	address of v2 source	
00001380	000013E4			710+	DC	A(RE7+32)	address of v3 source	
00001384	00000010			711+ 712+REA7	DC	A(16)	result length	
$00001388 \\ 00001390$	000013C4 00000000 00000000	)		712+ <b>REA</b> 7 713+	DC DS	A(RE7) FD	result address	
00001398	0000000 00000000	)		714+V107	DS	XL16	gap V1 output	
000013A0 000013A8	00000000 00000000 0000000 00000000			715.	DS	FD		
000013A6		,		715+ 716+*	ъ	ги	gap	
000013B0				717+X7	DS	OF		
000013B0 000013B6	E760 8EA4 0806 E760 FFEA 0845		000010A4	718+ 719+	VL VDEDI	V22, V1FUDGE V22, - 22, 0	test instruction (dest is a source)	
000013BC	E760 5030 080E		00001398	713+ 720+	VKETT	V22, V107	save v1 output	
000013C2	07FB			<b>721</b> +	BR	R11	return	
000013C4 000013C4				722+RE7 723+	DC DROP	OF R5	xl16 expected result	
000013C4 000013C4	EAEAEAEA EAEAEAEA	1		723+ 724	DKOP DC	кэ 16HL1' - 22'	result	
	EAEAEAEA EAEAEAEA							
				725 726	VRT A	VREPI, - 302, 0		
000013D8				<b>727</b> +	DS	OFD		
000013D8	00001400	000013D8		728+	USING		base for test data and test routine	
000013D8 000013DC	00001420 0008			729+T8 730+	DC DC	A(X8) H' 8'	address of test routine test number	
000013DE	00			731+	DC	X' 00'		
000013DF	00 EED0			732+	DC	<b>Ⅲ1'0'</b>	m3 field	
000013E0 000013E2	FED2 E5D9C5D7 C9404040	)		733+ 734+	DC DC	HL2' - 302' CL8' VREPI'	i2 used instruction name	
000013EC	00001444	•		735+	DC	A(RE8+16)	address of v2 source	
000013F0	00001454			736+	DC	A(RE8+32)	address of v3 source	
000013F4 000013F8	00000010 00001434			737+ 738+REA8	DC DC	A(16) A(RE8)	result length result address	
00001310	00000000 00000000	)		739+	DS	FD	gap	

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LOC	OBJECT	CODE	ADDR1	ADDR2	STMI			
001408 001410	00000000				740+V108	DS	XL16	V1 output
001418	0000000				741+ 742+*	DS	FD	gap
001420					743+X8	DS	0F	
001420	E760 8EA4	0806		000010A4	744+	VL	V22, V1FUDGE	
001426	E760 FED2				<b>745</b> +	VREPI	V22, -302, 0	test instruction (dest is a source)
00142C	E760 5030	080E		00001408	<b>746</b> +	VST	V22, V108	save v1 output
001432	07FB				<b>747</b> +	BR	R11	return
001434					748+RE8	DC	0F	xl16 expected result
01434	DODODODO	Dananana			749+	DROP	R5	1.
01434 0143C	D2D2D2D2 D2D2D2D2				750	DC	16HL1' - 302'	result
					751	,		
					752 *Hal fwo		VDEDI O 1	
001440					753 754+		VREPI, 0, 1	
001448 001448			00001448		754+ 755+	DS USING	OFD * DE	hase for test data and test routing
01448	00001490		00001448		756+T9	DC	A(X9)	base for test data and test routine address of test routine
01446 00144C	00001490				750+19 757+	DC	H' 9'	test number
0144E	0003				757+ 758+	DC	X' 00'	cest number
)0144E )0144F	01				759+	DC	HL1' 1'	m3 field
01450	0000				<b>760</b> +	DC	HL2' 0'	i 2 used
01452	E5D9C5D7	C9404040			761+	DC	CL8' VREPI'	instruction name
0145C	000014B4	00 10 10 10			762+	DC	A(RE9+16)	address of v2 source
01460	000014C4				763+	DC	A(RE9+32)	address of v3 source
01464	0000010				<b>764</b> +	DC	A(16)	result length
001468	000014A4				765+REA9	DC	A(RE9)	result address
001470	0000000				<b>766</b> +	DS	FD	gap
001478	0000000				767+V109	DS	XL16	gap V1 output
001480	0000000							
01488	0000000	0000000			768+	DS	FD	gap
01.400					769+*	D.C.	OF	
001490	EGOO OEAA	0000		00001014	770+X9	DS	OF	
001490	E760 8EA4			000010A4	771+	VL	V22, V1FUDGE	test instruction (dest is a second)
001496 00149C	E760 0000			00001470	772+		V22, 0, 1	test instruction (dest is a source)
)0149C )014A2	E760 5030 07FB	UOUE		00001478	773+ 774+	VST BR	V22, V109 R11	save v1 output return
014A2 014A4	UITD				774+ 775+RE9	DC	OF	xl 16 expected result
014A4 014A4					775+RE9 776+	DROP	R5	ATTO CAPCECEU TESUTE
	0000000	0000000			777	DC	8HL2' 0'	result
	00000000						-	2 3 3 4 2
					778			
					779		VREPI, 1, 1	
014B8					780+	DS	OFD	
014B8	00001===		000014B8		781+	USING		base for test data and test routine
014B8	00001500				782+T10	DC	A(X10)	address of test routine
014BC	000A				783+	DC	H' 10'	test number
014BE	00				784+	DC	X' 00'	o. C: -1 J
014BF	01				785+	DC	Ш1' 1' ш 2' 1'	m3 field
	0001	C0404040			786+ 787+	DC DC	HL2' 1'	i 2 used
014C0		しづ404040			/O/+	DC	CL8' VREPI'	instruction name
0014C0 0014C2	E5D9C5D7				799 -	DC	Λ( <b>D</b> F1Λ, 1Ω)	addrage of v9 source
0014C0 0014C2 0014CC	00001524				788+ 789+	DC DC	A(RE10+16) A(RE10+32)	address of v2 source
0014C0 0014C2					788+ 789+ 790+	DC DC DC	A(RE10+16) A(RE10+32) A(16)	address of v2 source address of v3 source result length

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000014E0 000014E8 000014F0	00000000 00000000 00000000 00000000 000000			792+ 793+V1010	DS DS	FD XL16	gap V1 output
000014F8	0000000 0000000			794+ 795+*	DS	FD	gap
00001500 00001500 00001506 0000150C 00001512	E760 8EA4 0806 E760 0001 1845 E760 5030 080E 07FB		000010A4 000014E8	796+X10 797+ 798+ 799+ 800+	DS VL VREPI VST BR	OF V22, V1FUDGE V22, 1, 1 V22, V1010 R11	test instruction (dest is a source) save v1 output return
00001514 00001514 00001514	00010001 00010001 00010001 00010001			801+RE10 802+ 803	DC DROP DC	OF R5 8HL2' 1'	xl16 expected result result
00001528				804 805 806+	DS	VREPI, 2, 1 OFD	
00001528 00001528 0000152C	00001570 000B	00001528		807+ 808+T11 809+	USING DC DC	A(X11) H' 11'	base for test data and test routine address of test routine test number
0000152E 0000152F 00001530	00 01 0002			810+ 811+ 812+	DC DC DC	X' 00' HL1' 1' HL2' 2'	m3 field i2 used
00001532 0000153C 00001540	E5D9C5D7 C9404040 00001594 000015A4			813+ 814+ 815+	DC DC DC	CL8' VREPI' A(RE11+16) A(RE11+32)	instruction name address of v2 source address of v3 source
00001544 00001548 00001550	00000010 00001584 00000000 00000000			816+ 817+REA11 818+	DC DC DS	A(16) A(RE11) FD	result length result address gap V1 output
00001558 00001560 00001568	00000000 00000000 00000000 00000000 000000			819+V1011 820+	DS DS	XL16 FD	V1 output gap
00001570 00001570	E760 8EA4 0806		000010A4	821+* 822+X11 823+	DS VL	OF V22, V1FUDGE	
00001576 0000157C 00001582	E760 0002 1845 E760 5030 080E 07FB		00001558	824+ 825+ 826+	VST BR	V22, 2, 1 V22, V1011 R11	test instruction (dest is a source) save v1 output return
	00020002 00020002			827+RE11 828+ 829	DC DROP DC	OF R5 8HL2' 2'	xl16 expected result result
	00020002 00020002			830 831		VREPI, 99, 1	
00001598 00001598 00001598 0000159C	000015E0	00001598		832+ 833+ 834+T12 835+	DS USING DC	A(X12)	base for test data and test routine address of test routine
0000159E 0000159F	000C 00 01			835+ 836+ 837+ 838+	DC DC DC	H' 12' X' 00' HL1' 1' HL2' 99'	test number mß field
000015A0 000015A2 000015AC	0063 E5D9C5D7 C9404040 00001604			839+ 840+	DC DC DC	CL8' VREPI' A(RE12+16)	i2 used instruction name address of v2 source
000015B0 000015B4 000015B8	00001614 00000010 000015F4			841+ 842+ 843+REA12	DC DC DC	A(RE12+32) A(16) A(RE12)	address of v3 source result length result address

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LOC	OBJECT CODE	ADDR1	ADDR2	STM			
000015C0 000015C8 000015D0	00000000 00000000 00000000 00000000 000000			844+ 845+V1012	DS DS	FD XL16	gap V1 output
000015D8	0000000 0000000			846+ 847+*	DS	FD	gap
000015E0 000015E0 000015E6 000015EC	E760 8EA4 0806 E760 0063 1845 E760 5030 080E 07FB		000010A4 000015C8	848+X12 849+ 850+ 851+ 852+	DS VL VREPI VST BR	OF V22, V1FUDGE V22, 99, 1 V22, V1012 R11	test instruction (dest is a source) save v1 output return
000015F4 000015F4 000015F4 000015FC	00630063 00630063 00630063 00630063			853+RE12 854+ 855	DC DROP DC	OF R5 8HL2' 99'	xl16 expected result result
00001608				856 857 858+	DS	VREPI, 9999, 1 OFD	
00001608 00001608 0000160C	00001650 000D	00001608		859+ 860+T13 861+	USING DC DC	A(X13) H' 13'	base for test data and test routine address of test routine test number
0000160E 0000160F 00001610	00 01 270F			862+ 863+ 864+	DC DC DC	X' 00' HL1' 1' HL2' 9999'	m3 field i2 used
00001612 0000161C 00001620	E5D9C5D7 C9404040 00001674 00001684			865+ 866+ 867+	DC DC DC	CL8' VREPI' A(RE13+16) A(RE13+32)	instruction name address of v2 source address of v3 source
00001624 00001628 00001630	00000010 00001664 00000000 00000000			868+ 869+REA13 870+	DC DC DS	A(16) A(RE13) FD	result length result address gap V1 output
00001638 00001640 00001648	00000000 00000000 00000000 00000000			871+V1013 872+	DS DS	XL16 FD	VI output gap
00001650 00001650	E760 8EA4 0806		000010A4	873+* 874+X13 875+	DS VL	OF V22, V1FUDGE	
00001656 0000165C 00001662	E760 270F 1845 E760 5030 080E 07FB		00001638	876+ 877+ 878+	VST BR	V22, 9999, 1 V22, V1013 R11	test instruction (dest is a source) save v1 output return
	270F270F 270F270F 270F270F 270F270F			879+RE13 880+ 881	DC DROP DC	OF R5 8HL2' 9999'	xl16 expected result result
	27012701 27012701			882 883		VREPI, - 1, 1	
00001678 00001678 00001678	000016C0	00001678		884+ 885+ 886+T14	DS USING DC	A(X14)	base for test data and test routine address of test routine
0000167C 0000167E 0000167F	000E 00 01			887+ 888+ 889+	DC DC DC	H' 14' X' 00' HL1' 1'	test number m3 field
00001680 00001682 0000168C	FFFF E5D9C5D7 C9404040 000016E4			890+ 891+ 892+	DC DC DC	HL2' - 1' CL8' VREPI' A(RE14+16)	i2 used instruction name address of v2 source
00001690 00001694 00001698	000016F4 00000010 000016D4			893+ 894+ 895+REA14	DC DC DC	A(RE14+32) A(16) A(RE14)	address of v3 source result length result address

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LOC	OBJECT CODE	ADDR1	ADDR2	STMI			
000016A0 000016A8	00000000 00000000 00000000 00000000			896+ 897+V1014	DS DS	FD XL16	gap V1 output
000016B0 000016B8	00000000 00000000 00000000 00000000			898+ 899+*	DS	FD	gap
000016C0 000016C0 000016C6 000016CC 000016D2	E760 8EA4 0806 E760 FFFF 1845 E760 5030 080E 07FB		000010A4 000016A8	900+X14 901+ 902+ 903+ 904+	VST BR	OF V22, V1FUDGE V22, -1, 1 V22, V1014 R11	test instruction (dest is a source) save v1 output return
000016D4 000016D4 000016D4 000016DC	FFFFFFFF FFFFFFFFFFFFFFFFFFFFFFFFFFFFF			905+RE14 906+ 907	DC DROP DC	OF R5 8HL2' - 1'	xl16 expected result
000016E8		00001050		908 909 910+	DS	VREPI, -2, 1 OFD	
000016E8 000016E8 000016EC	00001730 000F	000016E8		911+ 912+T15 913+	USING DC DC	A(X15) H' 15'	base for test data and test routine address of test routine test number
000016EE 000016EF 000016F0	00 01 FFFE			914+ 915+ 916+	DC DC DC	X' 00' HL1' 1' HL2' - 2'	m3 field i2 used
000016F2 000016FC 00001700	E5D9C5D7 C9404040 00001754 00001764			917+ 918+ 919+	DC DC DC	CL8' VREPI' A(RE15+16) A(RE15+32)	instruction name address of v2 source address of v3 source
00001704 00001708 00001710	00000010 00001744 00000000 00000000			920+ 921+REA15 922+	DC DC DS	A(16) A(RE15) FD	result length result address gap V1 output
00001718 00001720 00001728	00000000 00000000 00000000 00000000			923+V1015 924+	DS DS	XL16 FD	vi output gap
00001730 00001730	E760 8EA4 0806		000010A4	925+* 926+X15 927+	DS VL	OF V22, V1FUDGE	
00001742	E760 FFFE 1845 E760 5030 080E 07FB		00001718	928+ 929+ 930+	VST BR	V22, - 2, 1 V22, V1015 R11	test instruction (dest is a source) save v1 output return
	FFFEFFFE FFFEFFFE			931+RE15 932+ 933	DC DROP DC	OF R5 8HL2' - 2'	xl16 expected result result
	FFFEFFFE FFFEFFFE			934 935		VREPI, - 22, 1	
00001758 00001758 00001758	000017A0	00001758		936+ 937+ 938+T16	DS USING DC	A(X16)	base for test data and test routine address of test routine
0000175F	0010 00 01			939+ 940+ 941+	DC DC DC	H' 16' X' 00' HL1' 1'	test number m3 field
0000176C	FFEA E5D9C5D7 C9404040 000017C4			942+ 943+ 944+	DC DC DC	HL2' - 22' CL8' VREPI' A(RE16+16)	i2 used instruction name address of v2 source
	000017D4 00000010 000017B4			945+ 946+ 947+REA16	DC DC DC	A(RE16+32) A(16) A(RE16)	address of v3 source result length result address

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00001780 00001788 00001790	00000000 00000000 00000000 00000000 000000			948+ 949+V1016	DS DS	FD XL16	gap V1 output
00001798	0000000 0000000			950+ 951+*	DS	FD	gap
000017A0 000017A0 000017A6 000017AC 000017B2 000017B4	E760 8EA4 0806 E760 FFEA 1845 E760 5030 080E 07FB		000010A4 00001788	952+X16 953+ 954+ 955+ 956+ 957+RE16	DS VL VREPI VST BR DC DROP	OF V22, V1FUDGE V22, - 22, 1 V22, V1016 R11 OF R5	test instruction (dest is a source) save v1 output return xl16 expected result
000017B4 000017B4 000017BC	FFEAFFEA FFEAFFEA FFEAFFEA FFEAFFEA			958+ 959	DC	8HL2' - 22'	result
000017C8 000017C8 000017C8 000017CC	00001810 0011	000017C8		960 961 962+ 963+ 964+T17 965+	VRI_A DS USING DC DC	VREPI, - 302, 1 OFD *, R5 A(X17) H' 17'	base for test data and test routine address of test routine test number
000017CE 000017CF 000017D0 000017D2 000017DC	00 01 FED2 E5D9C5D7 C9404040 00001834			966+ 967+ 968+ 969+ 970+	DC DC DC DC DC	X' 00' HL1' 1' HL2' - 302' CL8' VREPI' A(RE17+16)	m3 field i2 used instruction name address of v2 source
000017E0 000017E4 000017E8 000017F0	00001844 00000010 00001824 00000000 00000000			971+ 972+ 973+REA17 974+	DC DC DC DS	A(RE17+32) A(16) A(RE17) FD	address of v2 source  result length  result address  gap  V1 output
000017F8 00001800 00001808	00000000 00000000 00000000 00000000 000000			975+V1017 976+ 977+*	DS DS	XL16 FD	gap
00001810 00001810 00001816 0000181C 00001822	E760 8EA4 0806 E760 FED2 1845 E760 5030 080E 07FB		000010A4 000017F8	978+X17 979+ 980+ 981+ 982+	DS VL VREPI VST BR	OF V22, V1FUDGE V22, -302, 1 V22, V1017 R11	test instruction (dest is a source) save v1 output return
00001824 00001824 00001824 0000182C	FED2FED2 FED2FED2 FED2FED2 FED2FED2			983+RE17 984+ 985	DC DROP DC	OF R5 8HL2' - 302'	xl16 expected result result
00001838 00001838 00001838 0000183C 0000183E 0000183F	00001880 0012 00 01	00001838		987 988+ 989+ 990+T18 991+ 992+ 993+	DS USING DC DC DC DC	A(X18) H' 18' X' 00' HL1' 1'	base for test data and test routine address of test routine test number
00001840 00001842 0000184C 00001850 00001854 00001858	FD30 E5D9C5D7 C9404040 000018A4 0000018B4 00000010 00001894			994+ 995+ 996+ 997+ 998+ 999+REA18	DC DC DC DC DC DC	HL2' - 720' CL8' VREPI' A(RE18+16) A(RE18+32) A(16) A(RE18)	i2 used instruction name address of v2 source address of v3 source result length result address

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LOC	OBJECT CODE	ADDR1	ADDR2	STMI			
00001860 00001868 00001870	00000000 00000000 00000000 00000000 000000			1000+ 1001+V1018	DS DS	FD XL16	gap V1 output
00001878	0000000 0000000			1002+ 1003+*	DS	FD	gap
00001880 00001880 00001886 0000188C 00001892 00001894	E760 8EA4 0806 E760 FD30 1845 E760 5030 080E 07FB		000010A4 00001868	1004+X18 1005+ 1006+ 1007+ 1008+ 1009+RE18	DS VL VREPI VST BR DC	OF V22, V1FUDGE V22, - 720, 1 V22, V1018 R11 OF	test instruction (dest is a source) save v1 output return xl16 expected result
00001894 00001894 0000189C	FD30FD30 FD30FD30 FD30FD30 FD30FD30			1010+ 1011	DROP DC	R5 8HL2' - 720'	result
000018A8 000018A8 000018A8 000018AC	000018F0 0013	000018A8		1012 1013 1014+ 1015+ 1016+T19 1017+	VRI_A DS USING DC DC	VREPI, - 31456, 1 OFD *, R5 A(X19) H' 19'	base for test data and test routine address of test routine test number
000018AE 000018AF 000018B0 000018B2	00 01 8520 E5D9C5D7 C9404040			1018+ 1019+ 1020+ 1021+	DC DC DC DC	X' 00' HL1' 1' HL2' - 31456' CL8' VREPI'	m3 field i2 used instruction name
000018BC 000018C0 000018C4 000018C8 000018D0	00001914 00001924 00000010 00001904 00000000 00000000			1022+ 1023+ 1024+ 1025+REA19 1026+	DC DC DC DC DC	A(RE19+16) A(RE19+32) A(16) A(RE19) FD	address of v2 source address of v3 source result length result address gap V1 output
000018D8 000018E0 000018E8	00000000 00000000 0000000 00000000 000000			1027+V1019 1028+	DS DS	XL16 FD	V1 output gap
000018F0 000018F0 000018F6 000018FC 00001902	E760 8EA4 0806 E760 8520 1845 E760 5030 080E 07FB		000010A4 000018D8	1029+* 1030+X19 1031+ 1032+ 1033+ 1034+	VST BR	0F V22, V1FUDGE V22, - 31456, 1 V22, V1019 R11	test instruction (dest is a source) save v1 output return
	85208520 85208520 85208520 85208520			1035+RE19 1036+ 1037	DC DROP DC	OF R5 8HL2' - 31456'	xl16 expected result
				1038 1039 *Word 1040	VRT A	VREPI, 0, 2	
00001918 00001918 00001918 0000191C 0000191E	00001960 0014 00	00001918		1040 1041+ 1042+ 1043+T20 1044+ 1045+	DS USING DC DC DC	OFD	base for test data and test routine address of test routine test number
0000191F 00001920 00001922 0000192C	02 0000 E5D9C5D7 C9404040 00001984			1046+ 1047+ 1048+ 1049+	DC DC DC DC	HL1' 2' HL2' 0' CL8' VREPI ' A(RE20+16)	m3 field i2 used instruction name address of v2 source
00001930 00001934	00001994 00000010			1050+ 1051+	DC DC	A(RE20+32) A(16)	address of v3 source result length

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LOC	OBJECT CODE	ADDR1	ADDR2	STMI			
00001938	00001974			1052+REA20	DC	A(RE20)	result address
00001940	0000000 00000000			1053+	DS	FD	gap
00001948	0000000 00000000			1054+V1020	DS	XL16	V1 output
00001950	00000000 00000000						
00001958	00000000 00000000			1055+	DS	FD	gap
00001000				1056+*	Th C	<b></b>	
00001960	TT00 0744 0000		00001011	1057+X20	DS	OF	
00001960	E760 8EA4 0806		000010A4	1058+	VL	V22, V1FUDGE	
00001966	E760 0000 2845		00004040	1059+		V22, 0, 2	test instruction (dest is a source)
0000196C	E760 5030 080E		00001948	1060+	VST	V22, V1020	save v1 output
00001972	07FB			1061+	BR	R11	return
00001974				1062+RE20	DC	OF DE	xl16 expected result
00001974	0000000 0000000			1063+	DROP	R5	magul t
00001974	00000000 00000000			1064	DC	4FL4' 0'	result
0000197C	0000000 00000000			1065			
				1066	T/DT A	VREPI, 1, 2	
00001988				1067+	DS DS	OFD	
00001988		00001988		1067+ 1068+	USI NG		base for test data and test routine
00001988	000019D0	00001366		1069+T21	DC	A(X21)	address of test routine
0000198C	0001300			1070+	DC	H' 21'	test number
0000138E	00			1071+	DC	X' 00'	cese number
0000198F	02			1072+	DC	HL1'2'	m3 field
00001990	0001			1073+	DC	HL2' 1'	i 2 used
00001992	E5D9C5D7 C9404040			1074+	DC	CL8' VREPI'	instruction name
0000199C	000019F4			1075+	DC	A(RE21+16)	address of v2 source
000019A0	00001A04			1076+	DC	A(RE21+32)	address of v3 source
000019A4	00000010			1077+	DC	A(16)	result length
000019A8	000019E4			1078+REA21	DC	A(RE21)	result address
000019B0	0000000 00000000			1079+	DS	FD	
000019B8	0000000 00000000			1080+V1021	DS	XL16	gap V1 output
000019C0	0000000 00000000						
000019C8	0000000 00000000			1081+	DS	FD	gap
				1082+*			
000019D0				1083+X21	DS	<b>OF</b>	
000019D0	E760 8EA4 0806		000010A4	1084+	VL	V22, V1FUDGE	
000019D6	E760 0001 2845			1085+		V22, 1, 2	test instruction (dest is a source)
000019DC	E760 5030 080E		000019B8	1086+	VST	V22, V1021	save v1 output
000019E2	07FB			1087+	BR	R11	return
000019E4				1088+RE21	DC	OF DE	xl16 expected result
000019E4	00000001 0000001			1089+	DROP	R5	1,
000019E4	00000001 00000001			1090	DC	4FL4' 1'	result
000019EC	00000001 00000001			1001			
				1091	VDT 4	VDEDI O O	
00001050				1092		VREPI, 2, 2	
000019F8		00001050		1093+ 1094+	DS	0FD * D5	hase for test data and test mouting
000019F8 000019F8	00001A40	000019F8		1094+ 1095+T22	USI NG		base for test data and test routine address of test routine
000019F8 000019FC	00001A40 0016			1095+122 1096+	DC DC	A(X22) H' 22'	test number
000019FC 000019FE	0010			1090+ 1097+	DC DC	π 22 X' 00'	Cest Humber
000019FE 000019FF	02			1097+	DC	HL1' 2'	m3 field
00001311 00001A00	0002			1099+	DC DC	HL2' 2'	i2 used
00001A00	E5D9C5D7 C9404040			1100+	DC DC	CL8' VREPI'	instruction name
00001A02	00001A64			1101+	DC	A(RE22+16)	address of v2 source
00001A0C	00001A74			1101+	DC	A(RE22+32)	address of v2 source
00001A10	00001174			1102+	DC	A(16)	result length
COOUTHIT	0000010			1100	DO	11(10)	1 Court of Congon

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LOC	OBJECT CODE	ADDR1	ADDR2	STMI				
00001A18 00001A20	00001A54 00000000 00000000			1104+REA22 1105+	DC DS	A(RE22) FD	result address gap	
00001A28 00001A30	00000000 00000000 0000000 00000000			1106+V1022	DS	XL16	V1 output	
00001A38	00000000 00000000			1107+ 1108+*	DS	FD	gap	
00001A40	TT00 0714 0000		00001011	1109+X22	DS	OF		
00001A40 00001A46	E760 8EA4 0806 E760 0002 2845		000010A4	1110+ 1111+	VL VREPI	V22, V1FUDGE V22, 2, 2	test instruction (dest is a source)	
00001A4C 00001A52	E760 5030 080E 07FB		00001A28	1112+ 1113+	VST BR	V22, V1022 R11	save v1 output return	
00001A54 00001A54				1114+RE22 1115+	DC DROP	OF R5	xl16 expected result	
	00000002 00000002 00000002 00000002			1116	DC	4FL4' 2'	result	
				1117 1118	VRT A	VREPI, 99, 2		
00001A68		00001100		1119+	DS	OFD		
00001A68 00001A68	00001AB0	00001A68		1120+ 1121+T23	USI NG DC	*, K5 A(X23)	base for test data and test routine address of test routine	
00001A6C 00001A6E	0017 00			1122+ 1123+	DC DC	H' 23' X' 00'	test number	
00001A6F	02			1124+	DC	HL1' 2'	m3 field	
00001A70 00001A72	0063 E5D9C5D7 C9404040			1125+ 1126+	DC DC	HL2' 99' CL8' VREPI'	i2 used instruction name	
00001A7C 00001A80	00001AD4 00001AE4			1127+ 1128+	DC DC	A(RE23+16) A(RE23+32)	address of v2 source address of v3 source	
00001A30 00001A84 00001A88	00001AL4 00000010 00001AC4			1129+ 1130+REA23	DC DC DC	A(16) A(RE23)	result length result address	
00001A90	0000000 00000000			1131+	DS	FD	gap V1 output	
00001A98 00001AA0	00000000 00000000 0000000 00000000			1132+V1023	DS	XL16	VI output	
	00000000 00000000			1133+ 1134+*	DS	FD	gap	
00001AB0 00001AB0	E760 8EA4 0806		000010A4	1135+X23 1136+	DS VL	OF V22, V1FUDGE		
00001AB6	E760 0063 2845 E760 5030 080E		00001A98	1137+ 1138+		V22, 99, 2 V22, V1023	test instruction (dest is a source) save v1 output	
00001AC2 00001AC4	07FB			1139+ 1140+RE23	BR DC	R11 0F	return xl16 expected result	
00001AC4				1141+	DROP	<b>R5</b>	-	
	00000063 00000063 00000063 00000063			1142	DC	4FL4' 99'	result	
				1143 1144	VRI A	VREPI, 9999, 2		
00001AD8 00001AD8		00001AD8		1145+ 1146+	DS USING	OFD	base for test data and test routine	
00001AD8	00001B20	OOOTADO		1147+T24	DC	A(X24)	address of test routine	
00001ADC 00001ADE	0018 00			1148+ 1149+	DC DC	H' 24' X' 00'	test number	
00001ADF 00001AE0	02 270F			1150+ 1151+	DC DC	HL1' 2' HL2' 9999'	m3 field i2 used	
00001AE2	E5D9C5D7 C9404040			1152+	DC	CL8' VREPI'	instruction name	
00001AEC 00001AF0	00001B44 00001B54			1153+ 1154+	DC DC	A(RE24+16) A(RE24+32)	address of v2 source address of v3 source	
	00000010			1155+	DC	A(16)	result length	

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LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
00001AF8 00001B00	00001B34 00000000 00000000			1156+REA24 1157+	DC DS	A(RE24) FD	result address gap	
00001B08 00001B10	00000000 00000000 0000000 00000000			1158+V1024	DS	XL16	V1 output	
00001B18	00000000 00000000			1159+ 1160+*	DS	FD	gap	
00001B20 00001B20	E760 8EA4 0806		000010A4	1161+X24 1162+	DS VL	OF V22, V1FUDGE		
00001B26 00001B2C 00001B32	E760 270F 2845 E760 5030 080E 07FB		000010A4 00001B08	1163+ 1164+ 1165+		V22, V1F0BGE V22, 9999, 2 V22, V1024 R11	test instruction (dest is a source) save v1 output return	
00001B34 00001B34				1166+RE24 1167+	DC DROP	OF R5	xl16 expected result	
00001B34 00001B3C	0000270F 0000270F 0000270F 0000270F			1168	DC	4FL4' 9999'	resul t	
00001B48		00001040		1169 1170 1171+	DS	VREPI, -1, 2 OFD		
00001B48 00001B48	00001B90	00001B48		1172+ 1173+T25	USI NG DC	A(X25)	base for test data and test routine address of test routine	
00001B4C 00001B4E 00001B4F	0019 00 02			1174+ 1175+ 1176+	DC DC DC	H' 25' X' 00' HL1' 2'	test number m3 field	
00001B50 00001B52	FFFF E5D9C5D7 C9404040			1177+ 1178+	DC DC	HL2' - 1' CL8' VREPI'	i2 used instruction name	
00001B5C 00001B60 00001B64	00001BB4 00001BC4 00000010			1179+ 1180+ 1181+	DC DC DC	A(RE25+16) A(RE25+32) A(16)	address of v2 source address of v3 source result length	
00001B68 00001B70	00001BA4 00000000 00000000			1182+REA25 1183+	DC DS	A(RE25) FD	result address	
00001B78 00001B80	00000000 00000000 0000000 00000000			1184+V1025	DS	XL16	gap V1 output	
00001B88	0000000 00000000			1185+ 1186+*	DS	FD	gap	
00001B90 00001B90 00001B96	E760 8EA4 0806 E760 FFFF 2845		000010A4	1187+X25 1188+ 1189+		OF V22, V1FUDGE V22, -1, 2	test instruction (dest is a source)	
00001B9C 00001BA2 00001BA4	E760 5030 080E 07FB		00001B78	1190+ 1191+ 1192+RE25	VST BR DC	V22, V1025 R11 OF	save v1 output return xl16 expected result	
00001BA4 00001BA4	FFFFFFFF FFFFFFF			1193+ 1194	DROP DC	R5 4FL4' - 1'	result	
00001BAC	FFFFFFF FFFFFFF			1195	_ v	<del>_</del>		
00001BB8 00001BB8		00001BB8		1196 1197+ 1198+	VRI_A DS USING	VREPI, -2, 2 OFD * R5	base for test data and test routine	
00001BB8 00001BBC 00001BBE	00001C00 001A 00	00001000		1199+T26 1200+ 1201+	DC DC DC	A(X26) H' 26' X' 00'	address of test routine test number	
00001BBF 00001BC0 00001BC2	02 FFFE E5D9C5D7 C9404040			1202+ 1203+ 1204+	DC DC DC	HL1' 2' HL2' - 2' CL8' VREPI'	m3 field i2 used instruction name	
00001BCC 00001BD0	00001C24 00001C34			1205+ 1206+	DC DC	A(RE26+16) A(RE26+32)	address of v2 source address of v3 source	
00001BD4	00000010			1207+	DC	A(16)	result length	

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LOC	OBJECT CODE	ADDR1	ADDR2	STMI				
00001BD8 00001BE0	00001C14 00000000 00000000			1208+REA26 1209+	DC DS	A(RE26) FD	result address gap	
00001BE8 00001BF0	00000000 00000000 0000000 00000000			1210+V1026	DS	XL16	V1 output	
00001BF8	00000000 00000000			1211+	DS	FD	gap	
00001C00				1212+* 1213+X26	DS	0F		
00001C00	E760 8EA4 0806		000010A4	1214+	VL	V22, V1FUDGE		
00001C06 00001C0C	E760 FFFE 2845 E760 5030 080E		00001BE8	1215+ 1216+	<b>VST</b>	V22, - 2, 2 V22, V1026	test instruction (dest is a source) save v1 output	
00001C12	07FB			1217+ 1218+RE26	BR DC	R11 OF	return	
00001C14 00001C14				1219+	DROP	<b>R5</b>	xl16 expected result	
00001C14	FFFFFFFE FFFFFFE			1220	DC	4FL4' - 2'	result	
00001C1C	FFFFFFFE FFFFFFE			1221				
00001600				1222		VREPI, - 720, 2		
00001C28 00001C28		00001C28		1223+ 1224+	DS USING	OFD *. R5	base for test data and test routine	
00001C28	00001C70			1225+T27	DC	A(X27)	address of test routine	
00001C2C 00001C2E	001B 00			1226+ 1227+	DC DC	H' 27' X' 00'	test number	
00001C2F	02			1228+	DC	HL1' 2'	m3 field	
00001C30 00001C32	FD30 E5D9C5D7 C9404040			1229+ 1230+	DC DC	HL2' - 720' CL8' VREPI'	i2 used	
00001C3Z	00001C94			1230+ 1231+	DC DC	A(RE27+16)	instruction name address of v2 source	
00001C40	00001CA4			1232+	DC	A(RE27+32)	address of v3 source	
00001C44 00001C48	00000010 00001C84			1233+ 1234+REA27	DC DC	A(16) A(RE27)	result length result address	
00001C50	0000000 00000000			1235+	DS	FD	gap V1 output	
00001C58 00001C60	00000000 00000000 0000000 00000000			1236+V1027	DS	XL16	VI output	
00001C68	00000000 00000000			1237+ 1238+*	DS	FD	gap	
00001C70				1239+X27	DS	<b>0F</b>		
00001C70	E760 8EA4 0806 E760 FD30 2845		000010A4	1240+	VL	V22, V1FUDGE	test instruction (dest is a source)	
00001C76 00001C7C	E760 FD30 2845 E760 5030 080E		00001C58	1241+ 1242+	VKEPI VST	V22, - 720, 2 V22, V1027	test instruction (dest is a source) save v1 output	
00001C82	07FB			1243+	BR	R11	return	
00001C84 00001C84				1244+RE27 1245+	DC DROP	OF R5	xl16 expected result	
00001C84	FFFFFD30 FFFFFD30			1246	DC	4FL4' - 720'	result	
00001C8C	FFFFFD30 FFFFFD30			1247				
00004600				1248		VREPI, - 31456, 2		
00001C98 00001C98		00001C98		1249+ 1250+	DS USING	OFD *. R5	base for test data and test routine	
00001C98	00001CE0	30001000		1251+T28	DC	A(X28)	address of test routine	
00001C9C 00001C9E	001C 00			1252+ 1253+	DC DC	H' 28' X' 00'	test number	
00001C9F	02			1254+	DC	HL1' 2'	m3 field	
00001CA0	8520 E5D0C5D7 C0404040			1255+	DC DC	HL2' - 31456'	i2 used	
00001CA2 00001CAC	E5D9C5D7 C9404040 00001D04			1256+ 1257+	DC DC	CL8' VREPI' A(RE28+16)	instruction name address of v2 source	
00001CB0	00001D14			1258+	DC	A(RE28+32)	address of v3 source	
00001CB4	0000010			1259+	DC	A(16)	result length	

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LOC	OBJECT CODE	ADDR1	ADDR2	STM			
00001CB8 00001CC0	00001CF4 00000000 00000000			1260+REA28 1261+	DC DS	A(RE28) FD	result address gap
00001CC8 00001CD0	$\begin{array}{cccc} 00000000 & 00000000 \\ 00000000 & 00000000$			1262+V1028	DS	XL16	gap V1 output
00001CD8	00000000 00000000			1263+ 1264+*	DS	FD	gap
00001CE0 00001CE0 00001CE6	E760 8EA4 0806 E760 8520 2845		000010A4	1265+X28 1266+ 1267+	DS VL VREPI	0F V22, V1FUDGE V22, -31456, 2	test instruction (dest is a source)
00001CEC 00001CF2	E760 5030 080E 07FB		00001CC8	1268+ 1269+	VST BR	V22, V1028 R11	save v1 output return
00001CF4 00001CF4				1270+RE28 1271+	DC DROP	0F R5	xl16 expected result
00001CF4 00001CFC	FFFF8520 FFFF8520 FFFF8520			1272	DC	4FL4' - 31456'	result
				1273 1274 *Doubl ew			
00001D08				1275 1276+	VRI_A DS	VREPI, 0, 3 OFD	
00001D08 00001D08	00001D50	00001D08		1277+ 1278+T29	USI NG DC	*, R5 A(X29)	base for test data and test routine address of test routine
00001D0C 00001D0C	001D 001D 00			1279+ 1279+ 1280+	DC DC	H' 29' X' 00'	test number
00001D0F 00001D10	03 0000			1281+ 1282+	DC DC	HL1'3' HL2'0'	m3 field i2 used
00001D12	E5D9C5D7 C9404040			1283+	DC	CL8' VREPI'	instruction name
00001D1C 00001D20 00001D24	00001D74 00001D84 00000010			1284+ 1285+ 1286+	DC DC DC	A(RE29+16) A(RE29+32) A(16)	address of v2 source address of v3 source result length
00001D28 00001D30	00001D64 00000000 00000000			1287+REA29 1288+	DC DS	A(RE29) FD	result address gap
00001D38 00001D40	00000000 00000000 0000000 00000000			1289+V1029	DS	XL16	V1 output
00001D48	00000000 00000000			1290+ 1291+*	DS	FD	gap
00001D50 00001D50 00001D56	E760 8EA4 0806 E760 0000 3845		000010A4	1292+X29 1293+ 1294+	DS VL VRFPI	OF V22, V1FUDGE V22, 0, 3	test instruction (dest is a source)
00001D5C 00001D62 00001D64	E760 5030 080E 07FB		00001D38	1295+ 1296+ 1297+RE29	VST BR DC	V22, V1029 R11 OF	save v1 output return xl 16 expected result
00001D64 00001D64 00001D6C	00000000 00000000 00000000 00000000			1298+ 1299	DROP DC	R5 2DL8' 0'	result
00001D78				1300 1301 1302+	VRI_A DS	VREPI, 1, 3 OFD	
00001D78 00001D78 00001D7C	00001DC0 001E	00001D78		1303+ 1304+T30 1305+	USI NG DC DC		base for test data and test routine address of test routine test number
00001D7E 00001D7F 00001D80	00 03 0001			1306+ 1307+ 1308+	DC DC DC	X' 00' HL1' 3' HL2' 1'	m3 field i2 used
00001D82 00001D8C 00001D90	E5D9C5D7 C9404040 00001DE4 00001DF4			1309+ 1310+ 1311+	DC DC DC	CL8' VREPI' A(RE30+16) A(RE30+32)	instruction name address of v2 source address of v3 source

ASMA Ver.	0. 7. 0 zvector- e7- 2	21-VREPI					03 Apr 2025 15: 40: 21 Page 30
LOC	OBJECT CODE	ADDR1	ADDR2	STM			
00001D94 00001D98	00000010 00001DD4			1312+ 1313+REA30	DC DC	A(16) A(RE30)	result length result address
00001DA0 00001DA8 00001DB0	00000000 00000000 00000000 00000000 000000			1314+ 1315+V1030	DS DS	FD XL16	gap V1 output
00001DB8 00001DC0	00000000 00000000			1316+ 1317+* 1318+X30	DS DS	FD OF	gap
00001DC0 00001DC6 00001DCC	E760 8EA4 0806 E760 0001 3845 E760 5030 080E		000010A4 00001DA8	1319+ 1320+ 1321+	VL VREPI VST	V22, V1FUDGE V22, 1, 3 V22, V1030	test instruction (dest is a source) save v1 output
00001DD2 00001DD4 00001DD4	07FB			1322+ 1323+RE30 1324+	BR DC DROP	R11 OF R5	return xl16 expected result
00001DD4 00001DDC	00000000 00000001 0000000 00000001			1325 1326	DC	2DL8' 1'	resul t
00001DE8 00001DE8		00001DE8		1327 1328+ 1329+	VRI_A DS USING	VREPI, 2, 3 OFD * P5	base for test data and test routine
00001DE8 00001DEC 00001DEC	00001E30 001F 00	OOOOTDE8		1329+ 1330+T31 1331+ 1332+	DC DC DC	A(X31) H' 31' X' 00'	address of test routine test number
00001DEF 00001DF0 00001DF2	03 0002 E5D9C5D7 C9404040			1333+ 1334+ 1335+	DC DC DC	HL1'3' HL2'2' CL8'VREPI'	m3 field i2 used instruction name
00001DFC 00001E00 00001E04	00001E54 00001E64 00000010			1336+ 1337+ 1338+	DC DC DC	A(RE31+16) A(RE31+32) A(16)	address of v2 source address of v3 source result length
00001E08 00001E10 00001E18	00001E44 00000000 00000000 00000000 00000000			1339+REA31 1340+ 1341+V1031	DC DS DS	A(RE31) FD XL16	result address gap V1 output
00001E20 00001E28	00000000 00000000			1342+ 1343+*	DS	FD	gap
00001E30 00001E30 00001E36	E760 8EA4 0806 E760 0002 3845		000010A4	1344+X31 1345+ 1346+	DS VL VREPI	OF V22, V1FUDGE V22, 2, 3	test instruction (dest is a source)
00001E3C 00001E42 00001E44	E760 5030 080E 07FB		00001E18	1347+ 1348+ 1349+RE31	VST BR DC	V22, V1031 R11 OF	save v1 output return xl 16 expected resul t
00001E44 00001E44 00001E4C				1350+ 1351	DROP DC		result
00001E58				1352 1353 1354+	VRI_A DS	VREPI, 99, 3 OFD	
00001E58 00001E58 00001E5C	00001EA0 0020	00001E58		1355+ 1356+T32 1357+	USI NG DC DC	*, R5 A(X32) H' 32'	base for test data and test routine address of test routine test number
00001E5E 00001E5F 00001E60	00 03 0063			1358+ 1359+ 1360+	DC DC DC	X' 00' HL1' 3' HL2' 99'	m3 field i2 used
00001E62 00001E6C 00001E70	E5D9C5D7 C9404040 00001EC4 00001ED4			1361+ 1362+ 1363+	DC DC DC	CL8' VREPI ' A(RE32+16) A(RE32+32)	instruction name address of v2 source address of v3 source

ASMA Ver.	0.7.0 zvector-e7-2	1-VREPI					03 Apr 2025 15: 40: 21 Page 31
LOC	OBJECT CODE	ADDR1	ADDR2	STM			
00001E74 00001E78 00001E80	00000010 00001EB4 00000000 00000000			1364+ 1365+REA32 1366+	DC DC DS	A(16) A(RE32) FD	result length result address gap
00001E88 00001E90	00000000 00000000 0000000 00000000			1367+V1032	DS	XL16	V1 output
00001E98 00001EA0	00000000 00000000			1368+ 1369+* 1370+X32	DS DS	FD OF	gap
00001EA0 00001EA6 00001EAC	E760 8EA4 0806 E760 0063 3845 E760 5030 080E		000010A4 00001E88	1371+ 1372+ 1373+	VL VREPI VST	V22, V1FUDGE V22, 99, 3 V22, V1032	test instruction (dest is a source) save v1 output
00001EB2 00001EB4 00001EB4	07FB			1374+ 1375+RE32 1376+	BR DC DROP	R11 OF R5	return xl16 expected result
00001EB4 00001EBC	00000000 00000063 00000000 00000063			1377 1378	DC	2DL8' 99'	result
00001EC8 00001EC8		00001EC8		1379 1380+ 1381+	VRI_A DS USING	VREPI, 9999, 3 OFD *. R5	base for test data and test routine
00001EC8 00001ECC 00001ECE	00001F10 0021 00	OUUTEO		1382+T33 1383+ 1384+	DC DC DC	A(X33) H' 33' X' 00'	address of test routine test number
00001ECF 00001ED0 00001ED2	03 270F E5D9C5D7 C9404040			1385+ 1386+ 1387+	DC DC DC	HL1'3' HL2'9999' CL8'VREPI'	m3 field i2 used instruction name
00001EDC 00001EE0 00001EE4	00001F34 00001F44 00000010			1388+ 1389+ 1390+	DC DC DC	A(RE33+16) A(RE33+32) A(16)	address of v2 source address of v3 source result length
00001EE8 00001EF0 00001EF8	00001F24 00000000 00000000 0000000 00000000			1391+REA33 1392+ 1393+V1033	DC DS DS	A(RE33) FD XL16	result address gap V1 output
	00000000 00000000 00000000 00000000			1394+ 1395+*	DS	FD	gap
00001F10 00001F10 00001F16	E760 8EA4 0806 E760 270F 3845		000010A4	1396+X33 1397+ 1398+	DS VL VREPI	OF V22, V1FUDGE V22, 9999, 3	test instruction (dest is a source)
00001F1C 00001F22 00001F24	E760 5030 080E 07FB		00001EF8	1399+ 1400+ 1401+RE33	VST BR DC	V22, V1033 R11 OF	save v1 output return xl16 expected result
00001F24 00001F24 00001F2C	00000000 0000270F 00000000 0000270F			1402+ 1403	DROP DC	R5 2DL8' 9999'	result
00001F38				1404 1405 1406+	DS	VREPI, -1, 3 OFD	
00001F38 00001F38 00001F3C		00001F38		1407+ 1408+T34 1409+	USING DC DC	A(X34) H' 34'	base for test data and test routine address of test routine test number
00001F3E 00001F3F 00001F40	03 FFFF			1410+ 1411+ 1412+	DC DC DC	X' 00' HL1' 3' HL2' - 1'	m3 field i2 used
00001F42 00001F4C 00001F50				1413+ 1414+ 1415+	DC DC DC	CL8' VREPI' A(RE34+16) A(RE34+32)	instruction name address of v2 source address of v3 source

ASMA Ver.	0. 7. 0 zvector-e7-2	1-VREPI					03 Apr 2025 15: 40: 21 Page 32
LOC	OBJECT CODE	ADDR1	ADDR2	STMI			
00001F58	00000010 00001F94 00000000 00000000			1416+ 1417+REA34 1418+	DC DC DS	A(16) A(RE34) FD	result length result address gap
00001F68 00001F70	00000000 00000000 0000000 00000000			1419+V1034	DS	XL16	V1 output
00001F80	00000000 00000000			1420+ 1421+* 1422+X34	DS DS	FD OF	gap
00001F86	E760 8EA4 0806 E760 FFFF 3845 E760 5030 080E		000010A4 00001F68	1423+ 1424+ 1425+	VL VREPI VST	V22, V1FUDGE V22, - 1, 3 V22, V1034	test instruction (dest is a source) save v1 output
00001F92 00001F94 00001F94	07FB			1426+ 1427+RE34 1428+	BR DC DROP	R11 OF R5	return xl16 expected result
00001F94	FFFFFFFF FFFFFFFFFFFFFFFFFFFFFFFFFFFFF			1429 1430	DC	2DL8' - 1'	resul t
00001FA8 00001FA8		00001FA8		1431 1432+ 1433+	VRI_A DS USING	VREPI, -2, 3 OFD * R5	base for test data and test routine
00001FA8 00001FAC	00001FF0 0023 00	OOOTTAG		1434+T35 1435+ 1436+	DC DC DC	A(X35) H' 35' X' 00'	address of test routine test number
00001FB0	03 FFFE E5D9C5D7 C9404040			1437+ 1438+ 1439+	DC DC DC	HL1'3' HL2'-2' CL8'VREPI'	m3 field i2 used instruction name
00001FC0	00002014 00002024 00000010			1440+ 1441+ 1442+	DC DC DC	A(RE35+16) A(RE35+32) A(16)	address of v2 source address of v3 source result length
00001FD0	00002004 00000000 00000000 0000000 00000000			1443+REA35 1444+ 1445+V1035	DC DS DS	A(RE35) FD XL16	result address gap V1 output
	00000000 00000000			1446+ 1447+*	DS	FD	gap
	E760 8EA4 0806 E760 FFFE 3845		000010A4	1448+X35 1449+ 1450+	DS VL VREPI	0F V22, V1FUDGE V22, - 2, 3	test instruction (dest is a source)
00001FFC 00002002 00002004	E760 5030 080E 07FB		00001FD8	1451+ 1452+ 1453+RE35	VST BR DC	V22, V1035 R11 OF	save v1 output return xl16 expected result
	FFFFFFFF FFFFFFE FFFFFFFF FFFFFFE			1454+ 1455	DROP DC	R5 2DL8' - 2'	resul t
00002018				1456 1457 1458+	DS	VREPI, - 720, 3 OFD	
0000201C	00002060 0024	00002018		1459+ 1460+T36 1461+	USING DC DC	A(X36) H' 36'	base for test data and test routine address of test routine test number
0000201F 00002020	00 03 FD30			1462+ 1463+ 1464+	DC DC DC	X' 00' HL1' 3' HL2' - 720'	m3 field i2 used
0000202C	E5D9C5D7 C9404040 00002084 00002094			1465+ 1466+ 1467+	DC DC DC	CL8' VREPI' A(RE36+16) A(RE36+32)	instruction name address of v2 source address of v3 source

F' 0'

F' 0'

END OF TABLE

DC

DC

1511

1512

000020F4 00000000

00000000

000020F8

ASMA Ver.	0. 7. 0 zvector- e7-	21- VREPI				03 Apr 2025 15: 40: 21 Page	35
LOC	OBJECT CODE	ADDR1	ADDR2	STMF			
200	020201 0022			1564 ****** 1565 *	**************************************	************	
				1566 *****		***********	
		00000000	0000001	1568 RO	EQU O		
		$00000001 \\ 00000002$	00000001 00000001	1569 R1 1570 R2	EQU 1 EQU 2		
		00000003 00000004	00000001 00000001	1571 R3 1572 R4	EQU 3 EQU 4		
		00000005 00000006	00000001 00000001	1573 R5 1574 R6	EĞÜ 5 EĞU 6		
		00000007 00000008	00000001 00000001	1575 R7 1576 R8	EĞÜ 7 EĞU 8		
		00000009 0000000A	00000001 00000001	1577 R9 1578 R10	EĞÜ 9 EĞU 10		
		0000000B 0000000C	00000001 00000001	1579 R11 1580 R12	EQU 11		
		0000000D 0000000E	00000001 00000001	1581 R13 1582 R14	EQU 12 EQU 13 EQU 14		
		0000000F	00000001	1583 R15	EQU 15		
				1585 *****	*********	***********	
				1586 * 1587 *****	Register equates	**********	
		00000000 0000001	00000001 00000001	1589 V0 1590 V1	EQU 0 EQU 1		
		00000002 00000003	00000001 00000001	1591 V2 1592 V3	EQU 2 EQU 3		
		00000004 00000005	00000001 00000001	1593 V4 1594 V5	EĞÜ 4 EĞU 5		
		00000006 00000007	00000001 00000001	1595 V6 1596 V7	EĞÜ 6 EĞÜ 7		
		00000008 00000009	00000001 00000001	1597 V8 1598 V9	EQU 8 EQU 9		
		00000003 0000000A 0000000B	00000001 00000001	1599 V10 1600 V11	EĞÜ 10 EĞÜ 11		
		0000000D 0000000D	00000001 00000001	1601 V12 1602 V13	EQU 12 EQU 13		
		000000E 0000000F	00000001 00000001	1602 V13 1603 V14 1604 V15	EQU 14 EQU 15		
		0000001 00000010 00000011	00000001 00000001	1605 V16 1606 V17	EQU 16 EQU 17		
		00000011 00000012 00000013	00000001 00000001	1607 V18 1608 V19	EQU 17 EQU 18 EQU 19		
		00000013 00000014 00000015	00000001 00000001 00000001	1608 V19 1609 V20 1610 V21	EQU 19 EQU 20 EQU 21		
		00000013	0000001	TOTO VAI	TAO VI		

T 0.0	OD TECH CORE	ADDD4	ABBBS	CITIN ET				03 Apr 2025	8	36
LOC	OBJECT CODE	ADDR1	ADDR2	STMI						
		00000016 00000017	00000001	1611 V22 1612 V23	EQU FOU	22 23				
		0000018	00000001	1613 V24	EQU	24				
		00000019 000001A	00000001 00000001	1614 V25 1615 V26	EQU EQU	25 26				
		0000001B	00000001	1616 V27	EQU	27				
		0000001C 0000001D	00000001	1617 V28 1618 V29	EQU EQU	29				
		0000001E 0000001F	00000001 00000001	1619 V30 1620 V31	EQU	22 23 24 25 26 27 28 29 30 31				
		0000011	0000001	1621		01				
				1622	END					

SYMB0L	TYPE	VALUE	LENGTH	DEFN	REFERE	NCFS											
EGI N	Ι	00000200	2	151	117	147	148	149									
ΓLRO	F	000004C4	4	365	161	162	163	164									
ECNUM	C	00001082	16	422	260	262	268	270	288	290							
0M4	H	0000038C	2	286	277	281											
7TEST	4	00000000	72	436	210	~~-											
7TESTS	F	000020FC	4	1517	203												
DIT	X	00002016	18	417	261	269	289										
NDTEST			10			203	203										
	Ų	00000318	1	246	208	0.40											
0J	I	000004A8	4	355	196	<b>249</b>											
OJPSW	D	00000498	8	353	355												
AI LCONT	U	00000308	1	236													
AI LED	F	00001000	4	396	238	247											
AILMSG	U	00000304	1	230	220												
AILPSW	D	000004B0	8	357	359												
AILTEST	Ī	000004C0	4	359	250												
B0001	F	00000280	8	180	184	185	187										
2	X	0000008	2	442	267	100	107										
z 2neg																	
	H	00000386	2	283	275												
2ZERO	H	0000037C	2	279	274												
MAGE	1	00000000	8608	0													
	U	00000400	1	380	381	<b>382</b>	383										
64	U	00010000	1	382													
3	U	0000007	1	441	287												
В	U	00100000	1	383													
SG	Ī	000003E0	4	315	195	298											
SGCMD	Ĉ	0000042E	9	345	328	329											
SGMSG	č	00000437	95	346	322	343	320										
SGMVC	T	00000437	6	343	326	343	320										
	1			343													
SGOK	Ţ	000003F6	2	324	321	005											
SGRET	<u>I</u>	00000416	4	339	332	335											
SGSAVE	F	0000041C	4	342	318	339											
EXTE7	U	000002D4	1	205	223	241											
PNAME	C	000000A	8	444	265												
AGE	U	00001000	1	381													
RT3	č	0000106C	18	420	<b>261</b>	262	<b>263</b>	269	270	271	289	290	291				
RTI 2	č	00001000	5	408	271	202	200	200	210	~11	200	200	201				
						297											
RTLINE	C	00001008	16	402	412	297											
RTLNG	U	0000004E	1	412	296												
RTMB	C	00001053	2	410	291												
RTNAME	C	00001033	8	405	<b>265</b>												
RTNUM	C	00001018	3	403	<b>263</b>												
RTSGN	C	00001044	1	407	276	280	284										
0	Ü	00000000	1	1568	111	161	164	184	186	187	188	193	212	213	237	238	295
		300000	•	1000	296	299	315	318	320	322	324	339	~1~	~10	~0,	200	200
1	U	0000001	1	1569	194	218	219	247	248	297	329	343					
1 10		00000001 0000000A	1	1578				<b>~</b> ⁴ /	~40	₩J I	JAJ	JHJ					
	U		1		149	158	159	F01	017	0.40	CCC	005	701	747	774	000	000
11	U	000000B	1	1579	215	216	565	591	617	643	669	695	721	747	774	800	826
					852	878	904	930	956	982	1008	1034	1061	1087	1113	1139	1165
						1217	1243	1269	1296	1322	1348	1374	1400	1426	1452	1478	1504
12	U	000000C	1	1580	203	206	222	240									
13	U	000000D	1	1581													
14	Ü	000000E	1	1582													
15	Ŭ	000000E	1	1583	231	256	302	303									
2	Ü	00000001	1	1570	195	259	260	267	268	273	287	288	295	298	299	316	318
<b>~</b>	U	0000000	1	13/0	324	325	326	328	334	339	340	200	233	200	ผิปป	310	310

CITA		r- e7- 21- VREP		DEFE	DEFER	ENCEC							03 Apr	2020			igc	38
SYMB	OL TYPE	VALUE	LENGTH	DEFN	REFER	ENCES												
24	U	00000004	1		000	007	010	057	001	T 40	507		500	500	010	004	0.45	
25	U	0000005	1	1573	206 650	207 671	210 676	257 697	301 702	546 723	567 728	572 749	593 755	598 776	619 781	624 802	645 807	
					828	833	854	859	880	885	906	911	932	937	958	963	984	
					989	1010	1015	1036	1042	1063	1068	1089	1094	1115	1120	1141	1146	
					1167	1172	1193	1198	1219	1224	1245	1250	1271	1277	1298	1303	1324	Ļ
					1329	1350	1355	1376	1381	1402	1407	1428	1433	1454	1459	1480	1485	•
26	U	00000006	1	1574	1506													
R7	Ü	00000007	1	1575														
88	Ū	8000000	1	1576	147	151	152	153	155									
29	U	00000009	1	1577	148	155	156	158										
RE1	F	$00001124 \\ 00001514$	4	566	553	554 789	556 701											
RE10 RE11	F F	00001514	4	801 827	788 814	815	791 817											
RE12	F	00001554	4	853	840	841	843											
RE13	F	00001664	4	879	866	867	869											
RE14	F	000016D4	4	905	892	893	895											
RE15	F F	00001744 000017B4	4	931	918 944	919 945	921											
RE16 RE17	r F	00001764	4	957 983	970	943	947 973											
RE18	F	00001824	4	1009	996	997	999											
RE19	F	00001904	4	1035	1022	1023	1025											
RE2	F	00001194	4	592	579	580	582											
RE20 RE21	F F	00001974 000019E4	4	1062 1088	1049 1075	1050 1076	1052 1078											
RE22	F	000019E4 00001A54	4	1114	1101	1102	1104											
RE23	F	00001AC4	$\dot{4}$	1140	1127	1128	1130											
RE24	F	00001B34	4	1166	1153	1154	1156											
RE25	F	00001BA4	4	1192	1179	1180	1182											
RE26 RE27	F F	00001C14 00001C84	4	1218 1244	1205 1231	1206 1232	1208 1234											
RE28	F	00001C84 00001CF4	4		1257	1258	1260											
RE29	F	00001011 00001D64	$\dot{4}$	1297	1284	1285	1287											
RE3	F	00001204	4	618	605	606	608											
RE30	F	00001DD4	4		1310	1311	1313											
RE31 RE32	F T	00001E44 00001EB4	4		1336 1362	1337	1339											
RE32	F	00001EB4 00001F24	4	1401	1388	1363 1389	1365 1391											
RE34	F	00001124 00001F94	$\overset{1}{4}$	1427	1414	1415	1417											
RE35	<u>F</u>	00002004	4	1453	1440	1441	1443											
RE36	F	00002074	4		1466	1467	1469											
RE37 RE4	F E	000020E4 00001274	4	1505 644	1492 631	1493 632	1495 634											
RE5	F	00001274 000012E4	4	670	657	658	660											
RE6	F	00001354	$\dot{4}$	696	683	684	686											
RE7	<u>F</u>	000013C4	4	722	709	710	712											
RE8	F	00001434	4	748	735	736	738											
RE9 REA1	F A	000014A4 000010E8	4	775 556	762	763	765											
REA10	A	000010E8 000014D8	4	791														
REA11	Ä	00001120	$\dot{4}$	817														
REA12	A	000015B8	4	843														
REA13	A	$00001628 \\ 00001698$	4	869 895														
REA14	Α																	

CVMDAT	TUDE	VAT TIT	I EMCTH	DEFEN	REFEREN	
SYMBOL	TYPE	VALUE	LENGTH	DEFN	KEFEKEN	
EA16	A	00001778	4	947		
EA17	Ā	000017E8	4	973		
EA18	Ā	00001858	4	999		
EA19	Ä	000018C8	4	1025		
EA2	Ä	00001158	4	582		
EA20	A	00001138	$\overline{4}$	1052		
EA21	Ä	000019A8	$\dot{\overline{4}}$	1078		
EA22	Ä	000013A8	4	1104		
EA23	A	00001A18	4	1130		
EA24	Ä	00001A68	4	1156		
EA25	Ä	00001AF8	4	1182		
EA26				1102		
	A	00001BD8	4	1208		
EA27	A	00001C48	4	1234		
EA28	A	00001CB8	4	1260		
EA29	A	00001D28	4	1287		
EA3	A	00001108	4	608		
EA30	A	00001D98	4	1313		
EA31	A	00001E08	4	1339		
EA32	A	00001E78	4	1365		
EA33	A	00001EE8	4	1391		
EA34	A	00001F58	4	1417		
EA35	A	00001FC8	4	1443		
EA36	A	00002038	4	1469		
EA37	A	000020A8	4	1495		
EA4	A	00001238	4	634		
EA5	A	000012A8	4	660		
EA6	A	00001318	4	686		
EA7	A	00001388	4	712		
EA8	A	000013F8	4	738		
EA9	A	00001468	4	765		
EADDR	A	00000020	4	448	218	
EG2LOW	Ü	000000DD	1	386		
EG2PATT	Ŭ	AABBCCDD	1	385		
ELEN	Ä	0000001C	4	447		
PTDWSAV	D	000003D0	8	308	295	
PTERROR	Ĭ	00000326	4	256	231	
PTSAVE	F	00000320 000003C8	4	305	256 S	
PTSVR5	F	000003C8	4	306	257	
KL0001	U	000003CC 0000004E	4	177	193	
KT0001 KT0001	C	000004E 0000022A	20	174	193 177	
	U				1//	
VOLDPSW	U	00000140	0	113	1590	
1	A	000010C8	4	547	1520	
10	A	000014B8	4	782	1529	
11	A	00001528	4	808	1530	
12	A	00001598	4	834	1531	
13	A	00001608	4	860	1532	
14	A	00001678	4	886	1533	
15	A	000016E8	4	912	1534	
16	A	00001758	4	938	1535	
17	A	000017C8	4	964	1536	
18	A	00001838	4	990	1537	
19	A	000018A8	4	1016	1538	
2	A	00001138	4	573	1521	
20	Ā	00001918	4	1043	1539	
21	Ä	00001988	$\overline{4}$	1069	1540	
22	Ä	000019F8	$\hat{4}$		1541	

	CVMDAT	zvector TVDF	- e7- 21- VREP	LENGTH	DEEM	DEEED	ENCES							оо лрт	2020	15: 40: 2		<b>5</b> ~	4
24	SIMBUL	IIFE		LENGIH			LNCES												
25 A 00001B48 4 1173 1544  46 A 00001B88 4 1228 1546  58 A 00001C88 4 1228 1546  58 A 00001L088 4 1228 1546  58 A 00001L088 4 1228 1546  59 A 00001B8 4 1228 1546  50 A 00001B8 4 1330 1550  51 A 00001B8 4 1330 1550  51 A 00001B8 4 1330 1550  52 A 00001B8 4 1330 1550  53 A 00001B8 4 1408 1553  54 A 00001B8 4 1408 1553  55 A 00001B8 4 1408 1556  57 A 00001B8 4 1677 1525  58 A 00001B8 4 766 1524  58 A 00001B8 4 776 1528  58 A 00001B8 4 170 1526  58 A 00001B8 4 776 1528  59 B 22 2 259  30 B 1 0000000 1 1 1589  50 U 0000000 1 1 1589  50 U 0000000 1 1 1589  51 U 0000000 1 1 1602  51 U 0000000 1 1 1604  52 U 0000000 1 1 1604  53 U 0000000 1 1 1604  54 U 0000000 1 1 1604  55 U 0000000 1 1 1604  56 U 0000000 1 1 1604  57 U 0000000 1 1 1604  58 U 0000000 1 1 1604  59 U 0000000 1 1 1604  50 U 0000000 1 1 1604  51 U 0000000 1 1 1604  51 U 0000000 1 1 1604  52 U 0000000 1 1 1604  53 U 0000000 1 1 1604  54 U 0000000 1 1 1604  55 U 0000000 1 1 1604  56 U 0000000 1 1 1604  57 U 0000000 1 1 1604  58 U 0 000000 1 1 1604  59 U 000000 1 1 1604  50 U 000000 1 1 1604  50 U 000000 1 1 1604  50 U 00000 1 1 1604  50 U 0000 1 1 1604  50 U 0	23																		
26 A 00001BB 4 1199 1545  77 A 0 00001C28 4 1225 1546  88 A 0 0001C28 4 1221 1547  89 A 00001D8 4 1231 1548  80 A 00001D8 4 1231 1549  80 A 00001D8 4 1304 1549  81 A 00001D8 4 1304 1549  81 A 00001D8 4 1305 1551  81 A 00001BB 4 1305 1551  81 A 00001BB 4 1305 1551  81 A 00001BB 4 1408 1555  81 A 00001BB 4 1408 1555  81 A 00001BB 4 1408 1555  81 A 00001BB 4 1408 1556  81 A 00001BB 4 1708 1568  81 B 0 0000000 1 1 1000  81 B 0 00000000 1 1 1000  81 B 0 00000000 1 1 10000  81 B 0 00000000 1 1 10000  81 B																			
77 A 00001C88 4 1225 1546 82 A 00001C88 4 1278 1547 83 A 00001D88 4 1278 1548 84 A 00001D88 4 1278 1548 85 A 00001D8 4 1330 1550 81 A 00001E8 4 1340 1553 81 A 00001E8 4 1340 1553 81 A 00001E8 4 1440 1553 81 A 00001E8 4 1651 1524 81 A 00001E8 4 1671 1525 81 A 00001E8 4 172 1527 81 A 0000000 1 1500 81 A 0000000 1 1600 81 A 00000000 1 1600 81 A 0000000 1 1600 81 A 00000000 1 1600 81 A 00000000 1 1600 81 A 0000		A A		_															
28		Α Δ		4															
99 A 00001D08 4 1278 1548 3 A 00001D08 4 1278 1548 3 A 00001D78 4 1304 1549 4 1304 1549 5 1548 5 A 00001E58 4 1382 1552 3 A 00001E58 4 1382 1552 3 A 00001E78 4 1408 1553 4 A 00001E78 4 1400 1555 5 A 00001E78 4 1460 1555 6 A 00001E78 4 1486 1556 6 A 00001E78 4 1596 6 A 00001E78 4 1596 6 A 00001E78 4 1596 6 A 00001E78 4 1677 1525 7 A 0000158 4 729 1527 7 A 0000168 4 730 1526 8 A 00001E78 4 1519 6 A 00000000 1 1590 6 A 00000000 1 1590 6 A 00000000 1 1590 6 A 00000000 1 1500 6 A 000000000 1 1500 6 A 00000000 1 1500 6 A 000000000000000000000000000000000		A		4															
10	29	Ā																	
11		A		4		1522													
122		A		_															
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77		Ä		_															
A 00001218	37	$\bar{\mathbf{A}}$	00002088		1486	1556													
A 00001288 4 677 1525 A 00001388 4 703 1526 A 00001388 4 703 1526 A 00001448 4 756 1528 STING F 00001004 4 397 213 UM H 00000000 4 437 215 ABLE F 000020FC 4 1519 U 00000000 1 1589 U 00000000 1 1589 U 00000000 1 1589 U 00000000 1 1600 2 U 0000000 1 1600 3 U 0000000 1 1600 2 U 0000000 1 1600 2 U 0000000 1 1600 3 U 0000000 1 1600 5 U 0000000 1 1600 8 U 0000000 1 1600 9 U 0000000 1 1600 1 1600 1 1600 1 1 1600 1 1 10000 1 1 1000 1 1 1000 1 1 1000 1 1 1000 1 1 1000 1 1 1000 1 1 10000 1 1 1000 1 1 1000 1 1 1000 1 1 1000 1 1 1000 1 1 1000 1 1 10000 1 1 1000 1 1 1000 1 1 10000 1 1 10000 1 1 10000 1 1 10000 1 1 10000 1 1 10000 1 1 10000 1 1 10000 1 1 10000 1 1 10000 1 1 1000		A	00001218	4	625	1523													
A 00001388 4 703 1526  A 00001318 4 703 1526  A 0000148 4 756 1528  STING F 00001004 4 397 213  UM H 00000000 4 2 438 212 259  ABLE F 000020FC 4 1519  U 00000000 1 1 1589  U 00000000 1 1 1599  1 U 00000000 1 1 1599  1 U 00000000 1 1 1602  2 U 00000000 1 1 1602  3 U 00000000 1 1 1603  5 U 00000000 1 1 1603  5 U 00000000 1 1 1605  7 U 00000001 1 1 1605  6 U 0000001 1 1 1606  8 U 0000001 1 1 1606  8 U 0000001 1 1 1606  8 U 0000001 1 1 1606  9 U 0000001 1 1 1606  1 0000001 1 1 1606  1 0000001 1 1 1606  1 0000001 1 1 1606  6 U 0000001 1 1 1606  8 U 0000001 1 1 1606  8 U 0000001 1 1 1606  9 U 0000001 1 1 1606  1 0000001 1 1 1606  1 0000001 1 1 1606  1 0000001 1 1 1606  1 0000001 1 1 1606  1 0000001 1 1 1606  1 0000001 1 1 1606  1 0000001 1 1 1606  1 0000001 1 1 1606  2 U 0000001 1 1 1606  3 U 0000001 1 1 1606  6 U 0000001 1 1 1606  6 U 0000001 1 1 1606  8 U 0000001 1 1 1606  8 U 0000001 1 1 1606  9 U 0000001 1 1 1606  1 0000001 1 1 1606  1 0000001 1 1 1606  1 0000001 1 1 1606  2 U 0000001 1 1 1606  3 U 0000001 1 1 1606  4 U 0000001 1 1 1606  5 U 0000001 1 1 1606  6 U 0000001 1 1 1606  6 U 0000001 1 1 1606  6 U 0000001 1 1 1606  8 U 0000001 1 1 1606  8 U 0000001 1 1 1606  9 U 0000001 1 1 1606  1 0000001 1 1 1606  1 00000001 1 1 1606  1 00000000 1 1 1606  1 000000000 1 1 1606  1 00000000 1 1 1606  1 00000000 1 1 1606  1 0000000000		A																	
A 0000148		A																	
STING F 00001044 4 397 213  UM H 0000000 4 437 215  UB A 00000000 1 1589  UB 00000000 1 1589  U 00000000 1 1600  2 U 0000000 1 1600  2 U 0000000 1 1600  2 U 0000000 1 1600  3 U 0000000 1 1600  5 U 0000000 1 1602  4 U 0000000 1 1602  4 U 0000000 1 1606  5 U 0000000 1 1606  6 U 000000 1 1606  7 U 000000 1 1606  8 U 000000 1 1608  FUDGE X 0000104 16 429  9 U 0000001 1 1608  FUDGE X 0000104 16 429  9 U 0000001 1 1608  FUDGE X 0000108 16 658 564  FUDGE X 0000108 16 849 562  10 0000013 1 1608  FUDGE X 0000168 16 871 879 1005 1031 1058 1084 1110 1136 1162 1188 1214  01 X 00001558 16 819 825  012 X 00001558 16 871 879  011 X 00001558 16 871 879  013 X 0000168 16 871 871  014 X 0000168 16 877 903  015 X 0000178 16 923 929  016 X 0000178 16 923 929  017 X 0000178 16 923 929  018 X 0000178 16 923 929  019 X 0000188 16 1027 1033  02 X 0000188 16 1027 1033  02 X 0000188 16 1054 1060		A																	
STING F 00001004 4 397 213 UM H 000000000 4 437 215 ABLE F 000020FC 4 1519 U 00000000 1 1589 U 00000000 1 1599 0 U 00000000 1 1599 1 U 00000000 1 1600 2 U 00000000 1 1600 2 U 00000000 1 1600 2 U 00000000 1 1600 3 U 00000000 1 1600 3 U 00000000 1 1602 4 U 00000000 1 1602 4 U 00000000 1 1602 5 U 00000000 1 1603 5 U 00000000 1 1604 6 U 00000000 1 1605 7 U 00000001 1 1605 8 U 0000001 1 1607 9 U 0000001 1 1607 9 U 0000001 1 1607 9 U 0000001 1 1607 1 100		A		4															
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NB A 0000000 4 4 437 215  ABLE F 000020FC 4 1519  U 00000000 1 1589  U 00000001 1 1590  O U 00000000 1 1600  2 U 00000000 1 1600  3 U 0000000 1 1601  3 U 0000000 1 1603  5 U 0000000 1 1603  5 U 0000000 1 1605  7 U 0000001 1 1605  7 U 0000001 1 1606  8 U 0000001 1 1606  8 U 0000001 1 1607  9 U 0000001 1 1607  9 U 0000001 1 1607  9 U 0000001 1 1607  10 000001 1 1608  FUDGE X 0000108 16 558 564  01 0 00000188 16 897 993  01 X 0000158 16 845 851  01 X 0000168 16 845 851  01 X 0000168 16 897 993  01 X 0000168 16 871 877  014 X 0000168 16 897 993  015 X 0000168 16 845 851  016 X 0000178 16 897 993  017 X 0000178 16 897 993  018 X 0000168 16 871 877  014 X 0000168 16 871 877  015 X 0000178 16 993 999  016 X 0000178 16 993 999  017 X 0000178 16 993 999  018 X 0000168 16 871 877  019 X 0000178 16 993 993  010 X 0000178 16 993 999  010 X 0000178 16 993 993  011 X 0000168 16 871 877  014 X 0000178 16 993 993  015 X 0000178 16 993 995  017 X 0000178 16 993 995  018 X 0000178 16 993 995  019 X 0000178 16 993 995  010 X 0000178 16 993 995  011 X 0000178 16 993 995  012 X 0000178 16 993 995  013 X 0000178 16 993 995  014 X 0000178 16 993 995  015 X 0000178 16 993 995  017 X 0000178 16 993 995  018 X 0000188 16 1001 1007  019 X 0000188 16 1004 1007  021 X 0000198 16 1054 1060							259												
ABLE F 000020FC 4 1519 U 00000000 1 1589 U 00000001 1 1599 U 00000000 1 1 1599 U 00000000 1 1 1600 2 U 00000000 1 1 1601 3 U 00000000 1 1 1602 4 U 0000000 1 1 1602 4 U 0000000 1 1 1603 5 U 0000000 1 1 1604 6 U 0000001 1 1 1605 7 U 0000001 1 1 1605 8 U 0000001 1 1 1606 8 U 0000001 1 1 1607 9 U 0000001 1 1 1607 9 U 0000001 1 1 1607 9 U 000001 1 1 1608 16 849 562 588 614 640 666 692 718 744 771 797 823 849 875 1240 1266 1293 1319 1345 1371 1397 1423 1449 1475 1501 01 X 0000168 16 558 564 01 X 0000168 16 819 825 012 X 0000158 16 845 851 013 X 0000168 16 871 877 014 X 0000168 16 887 897 015 X 0000178 16 949 955 016 X 0000178 16 949 955 017 X 0000178 16 949 955 018 X 0000178 16 949 955 019 X 0000178 16 949 955 010 X 0000178 16 949 955 017 X 0000178 16 975 981 018 X 0000178 16 975 981 019 X 00001188 16 1027 1033 02 X 00001188 16 1054 1060		Ä			437		200												
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1		U		1															
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4 U 0000000E 1 1603 5 U 000000F 1 1604 6 U 00000010 1 1605 7 U 00000011 1 1606 8 U 00000012 1 1607 9 U 00000013 1 1608 FUDGE X 00001084 16 429 562 588 614 640 666 692 718 744 771 797 823 849 875 901 927 953 979 1005 1031 1058 1084 1110 1136 1162 1188 1214 1240 1266 1293 1319 1345 1371 1397 1423 1449 1475 1501 01 X 0000158 16 558 564 010 X 0000158 16 819 825 012 X 0000158 16 845 851 013 X 0000168 16 845 851 014 X 0000168 16 897 903 015 X 0000178 16 923 929 014 X 0000178 16 923 929 015 X 0000178 16 923 929 016 X 0000178 16 923 929 017 X 0000178 16 923 929 018 X 0000178 16 975 981 018 X 0000178 16 975 981 019 X 0000168 16 1001 1007 019 X 0000188 16 1001 1007 019 X 0000198 16 1080 1086		U		I 1															
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6 U 00000010 1 1605 7 U 00000011 1 1606 8 U 00000012 1 1607 9 U 00000013 1 1608 FUDGE X 000010A4 16 429 562 588 614 640 666 692 718 744 771 797 823 849 875 901 927 953 979 1005 1031 1058 1084 1110 1136 1162 1188 1214 1240 1266 1293 1319 1345 1371 1397 1423 1449 1475 1501 01 X 00001488 16 793 799 011 X 00001588 16 819 825 012 X 00001508 16 845 851 013 X 00001638 16 871 877 014 X 00001638 16 897 903 015 X 00001718 16 923 929 016 X 00001718 16 923 929 017 X 00001788 16 975 981 018 X 00001788 16 975 981 019 X 0000168 16 1027 1033 02 X 0000168 16 584 590 020 X 00001988 16 1054 1060 021 X 00001988 16 1086 1086			OOOOOOE	1															
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8 U 00000012 1 1607 9 U 00000013 1 1608 FUDGE		Ŭ		1															
FUDGE X 000010A4 16 429 562 588 614 640 666 692 718 744 771 797 823 849 875 901 927 953 979 1005 1031 1058 1084 1110 1136 1162 1188 1214 1240 1266 1293 1319 1345 1371 1397 1423 1449 1475 1501 1240 1266 1293 1319 1345 1371 1397 1423 1449 1475 1501 1240 1266 1293 1319 1345 1371 1397 1423 1449 1475 1501 1240 1240 1240 1240 1240 1240 1240 12		U		1															
901 927 953 979 1005 1031 1058 1084 1110 1136 1162 1188 1214 1240 1266 1293 1319 1345 1371 1397 1423 1449 1475 1501  1				1															
1240 1266 1293 1319 1345 1371 1397 1423 1449 1475 1501 01	FUDGE	X	000010A4	16	429														
01       X       000010F8       16       558       564         010       X       000014E8       16       793       799         011       X       00001558       16       819       825         012       X       00001508       16       845       851         013       X       00001638       16       871       877         014       X       00001688       16       897       903         015       X       00001788       16       949       955         017       X       000017F8       16       975       981         018       X       00001868       16       1001       1007         019       X       00001808       16       1027       1033         02       X       00001168       16       584       590         020       X       00001948       16       1054       1060         021       X       00001988       16       1080       1086																	1188	1214	
010       X       000014E8       16       793       799         011       X       00001558       16       819       825         012       X       00001638       16       845       851         013       X       00001638       16       877       877         014       X       000016A8       16       897       903         015       X       00001718       16       923       929         016       X       00001788       16       949       955         017       X       00001788       16       975       981         018       X       00001868       16       1001       1007         019       X       00001808       16       1027       1033         02       X       00001948       16       1054       1060         020       X       00001988       16       1080       1086	Ω1	Y	000010F8	16	558		1200	1293	1319	1343	13/1	1397	1423	1449	14/3	1301			
011       X       00001558       16       819       825         012       X       000015C8       16       845       851         013       X       00001638       16       871       877         014       X       000016A8       16       897       903         015       X       00001718       16       923       929         016       X       00001788       16       949       955         017       X       000017F8       16       975       981         018       X       00001868       16       1001       1007         019       X       00001808       16       1027       1033         02       X       00001948       16       584       590         020       X       00001988       16       1080       1086																			
012       X       000015C8       16       845       851         013       X       00001638       16       871       877         014       X       000016A8       16       897       903         015       X       00001718       16       923       929         016       X       00001788       16       949       955         017       X       00001788       16       975       981         018       X       00001868       16       1007       1007         019       X       00001808       16       1027       1033         02       X       00001948       16       584       590         020       X       00001948       16       1054       1060         021       X       000019B8       16       1080       1086																			
014       X       000016A8       16       897       903         015       X       00001718       16       923       929         016       X       00001788       16       949       955         017       X       000017F8       16       975       981         018       X       00001868       16       1001       1007         019       X       000018D8       16       1027       1033         02       X       00001168       16       584       590         020       X       00001948       16       1054       1060         021       X       00001988       16       1080       1086	012		000015C8	16	845														
015       X       00001718       16       923       929         016       X       00001788       16       949       955         017       X       000017F8       16       975       981         018       X       00001868       16       1001       1007         019       X       000018D8       16       1027       1033         02       X       00001168       16       584       590         020       X       00001948       16       1054       1060         021       X       000019B8       16       1080       1086	013																		
016       X       00001788       16       949       955         017       X       000017F8       16       975       981         018       X       00001868       16       1001       1007         019       X       000018D8       16       1027       1033         02       X       00001168       16       584       590         020       X       00001948       16       1054       1060         021       X       000019B8       16       1080       1086	014																		
017       X       000017F8       16       975       981         018       X       00001868       16       1001       1007         019       X       000018D8       16       1027       1033         02       X       00001168       16       584       590         020       X       00001948       16       1054       1060         021       X       000019B8       16       1080       1086	U15																		
018       X       00001868       16       1001       1007         019       X       000018D8       16       1027       1033         02       X       00001168       16       584       590         020       X       00001948       16       1054       1060         021       X       000019B8       16       1080       1086	010 017																		
019       X       000018D8       16       1027       1033         02       X       00001168       16       584       590         020       X       00001948       16       1054       1060         021       X       000019B8       16       1080       1086	017																		
02     X     00001168     16     584     590       020     X     00001948     16     1054     1060       021     X     000019B8     16     1080     1086	019																		
1020 X 00001948 16 1054 1060 1021 X 000019B8 16 1080 1086																			
1021 X 000019B8 16 1080 1086	1020																		
	1021		000019B8		1080	1086													

SYMBOL	ТҮРЕ	VALUE	LENGTH	DEFN	REFER	ENCES												
						LITOLIS												
1023	X	00001A98	16	1132	1138													
1024	X	00001B08	16	1158	1164													
1025	X	00001B78	16	1184	1190													
1026 1027	X X	00001BE8 00001C58	16 16	1210 1236	1216 1242													
1028	X	00001C38	16	1262	1268													
1029	X	00001CCS 00001D38	16	1289	1295													
103	X	00001D00	16	610	616													
1030	X	00001DA8	16	1315	1321													
1031	X	00001E18	16	1341	1347													
1032	X	00001E88	16	1367	1373													
1033	X	00001EF8	16	1393	1399													
1034	X	00001F68	16	1419	1425													
1035	X	00001FD8	16	1445	1451													
1036	X	00002048	16	1471 1497	1477													
1037 104	X X	000020B8 00001248	16 16	636	1503 642													
105	X	00001248 000012B8	16	662	668													
106	X	00001228	16	688	69 <b>4</b>													
107	X	00001398	16	714	720													
108	X	00001408	16	740	746													
109	X	00001478	16	767	773													
10UTPUT	X	00000030	16	<b>450</b>	219													
2	U	00000002	1	1591														
20	U	00000014	1	1609														
21	Ü	00000015	1	1610	700	700	<b>504</b>	700	700	700	04.4	045	040	0.40	0.44	0.40	000	
22	U	0000016	1	1611	562 667 773	563 668 797	564 692 798	588 693 799	589 694 823	590 718 824	614 719 825	615 720 849	616 744 850	640 745 851	641 746 875	642 771 876	666 772 877	<b>)</b>
					901 1006	902 1007	903 1031	927 1032	928 1033	929 1058	953 1059	954 1060	955 1084	979 1085	980 1086	981 1110	1005 1111	5
					1112	1136	1137	1138	1162	1163	1164	1188	1189	1190	1214	1215	1216	
					1240	1241	1242	1266	1267		1293				1320			
										1397		1399	1423	1424	1425	1449	1450	)
· · · · · · · · · · · · · · · · · · ·		0000001#	_	1010	1451	1475	1476	1477	1501	1502	1503							
23	U	00000017	1	1612														
24	U	00000018	1	1613														
<b>25</b> <b>26</b>	II	00000019 0000001A	1	1614 1615														
27	II	0000001A 0000001B	1	1616														
28	Ŭ	0000001B	1	1617														
29	Ŭ	0000001D	1	1618														
2ADDR	A	0000014	4	445														
3	U	0000003	1	1592														
30	U	000001E	1	1619														
31	Ų	0000001F	1	1620														
3ADDR	A	00000018	4	446														
4	U	00000004	1	1593														
5 6	U	00000005	1 1	1594														
6 7	U	00000006 0000007	1	1595 1596														
8	II	00000007	1	1596 1597														
9	II	00000009	1	1598														
0001	Ŭ	0000003 000002A8	1	183	171	184												
1	F	00001110	4	561	547	101												
		00001500		796	782													

		REFEREN		21 - VREPI										оз Арі	2023	15: 40: 21	rage	43
CHECK TABLE	63 511	170 1518	570	506	699	CAO	674	700	796	759	770	905	091	057	002	000	025	061
I_A	469	544 987 1431	570 1013 1457	596 1040 1483	622 1066	648 1092	674 1118	700 1144	726 1170	753 1196	779 1222	805 1248	831 1275	857 1301	883 1327	909 1353	935 1379	961 1405

