L'ASSEMBLEUR – for et while

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Traduire un for

```
int i,s;
main()
{
s=0;
for(i=1;i<10;i++)s=s+i;
}</pre>
```

L1: .word i .word s .comm i,4,4 .comm s,4,4 mov r0,#0 Idr r1,L1+4 str r0,[r1] mov r0,#1 Idr r1,L1 str r0,[r1] for: Idr r0,L1

Idr r1,[r0]

cmp r1,#10

Traduction

```
bge fin
Idr r0,L1+4
Idr r1,[r0]
Idr r2,L1
Idr r3,[r2]
add r1,r1,r3
str r1,[r0]
Idr r0,L1
Idr r1,[r0]
add r1,r1,#1
str r1,[r0]
bra for
fin:
```

Exercice : traduire en assembleur ARM

```
int a,b,s,i;
main()
{a=1;b=2;s=0;}
for(i=1;i<=12;i++)
 a=a+b;
  b=b+1;
  s=s+a;
```

Solution

| L1: | mov r0,#0 | str r1,[r0] | str r1,[r0] |
|-------------|-------------------|--------------|-------------|
| .word a | ldr r1,L1+8 | ldr r0,L1+4 | bra for |
| .word b | str r0,[r1] | ldr r1,[r0] | fin: |
| .word s | mov r0,#1 | add r1,r1,#1 | |
| .word i | Idr r1,L1+12 | str r1,[r0] | |
| .comm a,4,4 | 50 10,[11] | Idr r0,L1+8 | |
| .COMM c 4 4 | for: Idr r0,L1+12 | fldr r1,[r0] | |
| .comm s,4,4 | ' b | ldr r2,L1 | |
| .comm i,4,4 | • | ldr r3,[r2] | |
| mov r0,#1 | | add r1,r1,r3 | |
| Idr r1,L1 | · | str r1,[r0] | |
| str r0,[r1] | · | Idr r0,L1+12 | |
| mov r0,#2 | Idr r2,L1+4 | ldr r1,[r0] | |
| Idr r1,L1+4 | ldr r3,[r2] | add r1,r1,#1 | 5 |
| str r0,[r1] | add r1,r1,r3 | , , | |

Exercice: Traduire en assembleur ARM

```
int a,b;
main()
a=1;b=2;
while(a<100)
  b=a+b;
  a=a+b;
  a=a+10;
```

Solution

L1: .word a .word b .comm a,4,4 .comm b,4,4 mov r0,#1 Idr r1,L1 str r0,[r1] mov r0,#2 Idr r1,L1+4 str r0,[r1]

while: Idr r0,L1 Idr r1,[r0] cmp r1,#100 bge fin Idr r0,L1 Idr r1,[r0] Idr r0,L1+4 Idr r2,[r0] add r1,r1,r2 str r1,[r0] Idr r0,L1 Idr r1,[r0]

Idr r2,L1+4 Idr r3,[r2] add r1,r1,r3 str r1,[r0] bra while fin : Idr r0,L1 Idr r1,[r0] add r1,r1,#10 str r1,[r0]

EXERCICE

```
Int a,b,c,d;
main()
{a=10;b=1;c=2;d=3;}
while(a+b>c+d)
  {c=c+1};
  d=d+c;
a=a+c-d;
```

| L1. |
|-------------|
| .word a |
| .word b |
| .word c |
| .word d |
| .comm a,4,4 |
| .comm b,4,4 |
| .comm c,4,4 |
| .comm d,4,4 |
| mov r0,#10 |
| ldr r1,L1 |
| str r0,[r1] |
| mov r0,#1 |
| Idr r1,L1+4 |
| str r0,[r1] |
| |
| |

| mov r0,#2 |
|----------------|
| ldr r1,L1+8 |
| str r0,[r1] |
| mov r0,#3 |
| ldr r1,L1+12 |
| str r0,[r1] |
| while:ldr r0,L |
| ldr r1, [r0] |
| ldr r0,L1+4 |
| ldr r2,[r0] |
| add r1,r1,r2 |
| ldr r0,L+8 |
| ldr r2, [r0] |
| ldr r0,L1+12 |
| ldr r3,[r0] |

add r2,r2,r3 cmp r1,r2 ble fin Idr r0,L1+8 Idr r1,[r0] add r1,r1,#1 str r1,[r0] _1 ldr r0,L1+12 Idr r1,[r0] Idr r2,L1+8 Idr r3,[r2] add r1,r1,r3 str r1,[r0] bra while

Fin: Idr r0,L1 Idr r1,[r0] Idr r2,L1+8 Idr r3,[r2] add r1,r1,r3 Idr r2,L1+12 Idr r3,[r2] sub r1,r1,r3 str r1,[r0]