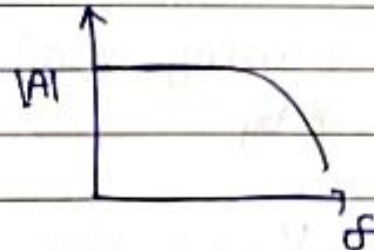
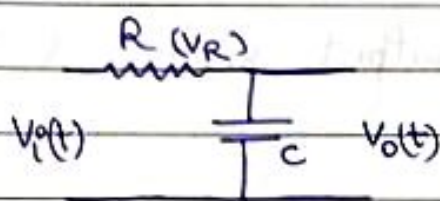


AC

//_

Low Pass RC Ckt :-



$$X_c = \frac{1}{\omega C}$$

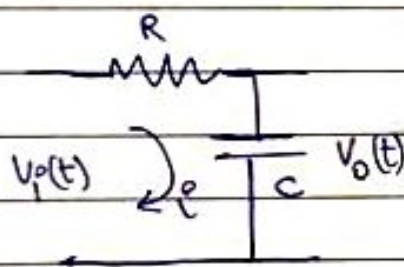
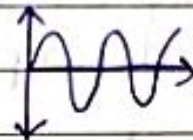
low freq - open ckt

$$V_o(t) = V_i(t)$$

high freq - short ckt

$$V_o(t) \neq V_i(t) = 0$$

① Sinoidal I/p :-



$$V_i(t) = V_m \sin \omega_m t$$

$$V_i(s) = (R + \frac{1}{sC}) i(s)$$

$$i(s) = \frac{V_i(s)}{R + \frac{1}{sC}} = \frac{V_i(s)}{R(1 + \frac{1}{sRC})}$$

$$V_o(s) = \frac{1}{sC} i(s) \text{ , So, Transfer Function is, } \frac{V_o(s)}{V_i(s)}$$

$$\Rightarrow \frac{1}{sC} \times \frac{1}{R(1 + \frac{1}{sRC})} = \frac{1}{1 + sRC} = \frac{1}{1 + j2\pi fRC}$$

Again transfer func. can be given in complex func in polar form as,

$$\frac{V_o(s)}{V_i(s)} = A \angle \theta^\circ$$

where A is the gain and θ is output leads input angle

$$⑥ A = \frac{1}{\sqrt{1 + (\frac{f}{f_c})^2}}$$

f_c = upper cutoff freq

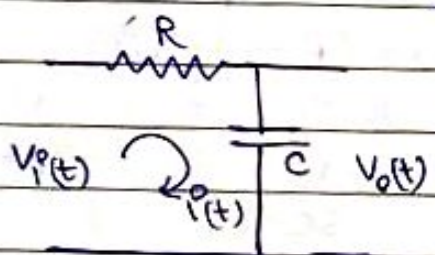
$$\textcircled{6} \quad \theta = \tan^{-1}\left(\frac{\phi_1}{\phi_2}\right) \quad \left\{ \phi_2 = \frac{1}{2\pi RC} \text{ upper cutoff freq} \right\}$$

Hence using eq. (5) & (6) output signal $V_o(t)$ is given by

$$\begin{aligned} V_o(s) &= V_i(s) A \angle \theta^\circ \\ &= A V_m \sin(\omega t + \theta) - (8) \end{aligned}$$

As the phase angle θ is negative hence output $V_o(t)$ lags behind input signal $V_i(t)$ by θ° .

② Step Input :-



$V_p(t) = V_R + V_C$
Consider capacitor charge & discharge exponentially. Applying KVL,

$$\begin{aligned} V_i(t) &= i(t) R + \frac{1}{C} \int i(t) dt \\ V &= i(t) R + \frac{1}{C} \int i(t) dt \end{aligned}$$

Diff w.r.t t

$$\frac{dV}{dt} = 0$$

$$\Rightarrow 0 = R \frac{di(t)}{dt} + \frac{1}{C} i(t)$$

$$\frac{di(t)}{dt} + \frac{1}{RC} i(t) = 0$$

Put $\frac{di}{dt} = 0 \rightarrow (D + \frac{1}{RC})i = 0, D = -\frac{1}{RC}$

$i(t) = c_1 e^{-t/RC}$

① Put $t = 0$

$i = \frac{V}{R}$

② Put $t = 0$

$c_1 = \frac{V}{R}$

$i(t) = \frac{V}{R} e^{-t/RC}$

$V_R = i(t) R$

$V_R(t) = \frac{V}{R} e^{-t/RC} R = V e^{-t/RC}$

$V_R = V e^{-t/RC}$

$V_C = V - V e^{-t/RC} = V(1 - e^{-t/RC})$

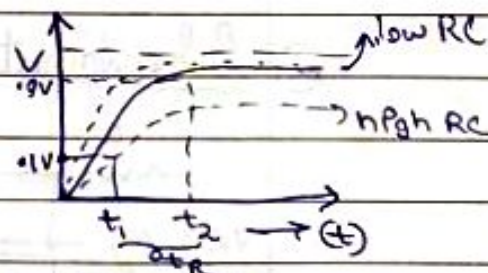
$V_C = V_0(t) = V(1 - e^{-t/RC})$

At $t = 0,$

$V_0(t) = 0$

$t = \infty$

$V_0(t) = V$



Rise time:- Time taken by capacitor voltage to increase from 10% - 90% of its final voltage V_0 is called rise time.

$V_0(t) = V(1 - e^{-t/RC})$

$\therefore t = t_1 = \dots 1V = V(1 - e^{-t/RC})$
 $e^{-t/RC} = .9V$

$-\frac{t}{RC} = \log(.9)$

$$t_1 = 0.105 RC$$

$$0.9V = V(1 - e^{-t/RC})$$

$$e^{-t_2/RC} = 0.1$$

$$-\frac{t_2}{RC} = \log 0.1$$

$$t_2 = 2.303 RC$$

Now Rise time, $t_r = t_2 - t_1$

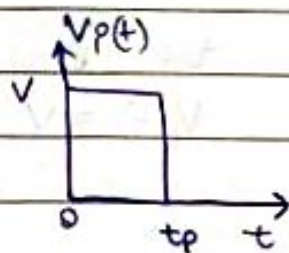
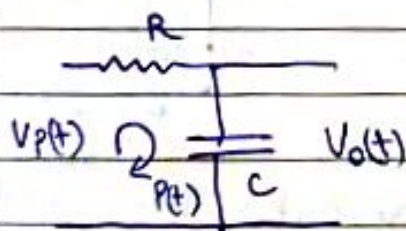
$$t_r = 2.2 RC$$

Let f_a be upper 3db frequency $\left(\frac{1}{2\pi RC}\right)$, so,

$$t_r = 2.2 RC$$

$$t_r = \frac{2.2}{2\pi f_a} = \boxed{t_r = 0.35 \frac{1}{f_a}}$$

② Pulse Input:-



$$V_P(t) = V_R + V_C$$

At $t=0$

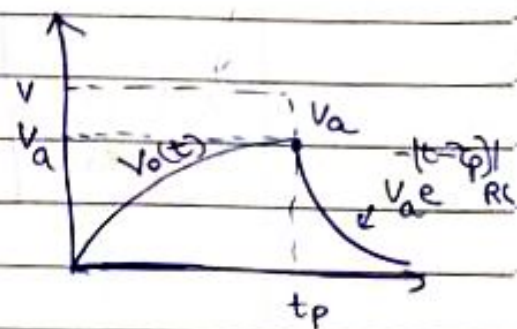
$$V_P(t) = V \quad \text{as} \quad \begin{aligned} V_C &= 0 \\ V_R &= V \end{aligned}$$

//_

At $0 < t < t_p$ $V_i = V$, $V_o = V(1 - e^{-t/RC})$

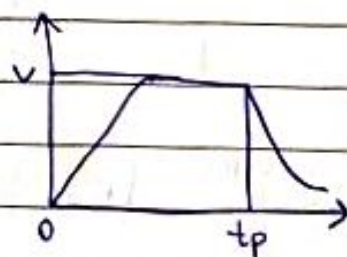
At $t = t_p$ $V_a = V(1 - e^{-t_p/RC})$

At $t > t_p$ $V_o(t) = V_a (e^{-(t-t_p)/RC})$

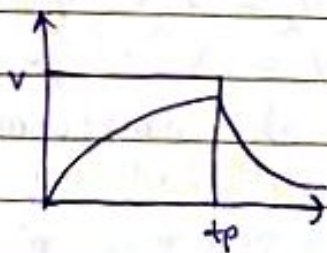


$t_p \rightarrow$ time period of pulse
 $RC \rightarrow$ time constant of ckt

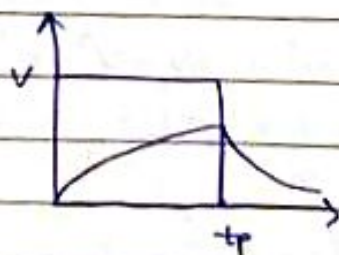
When $t_p \gg RC$



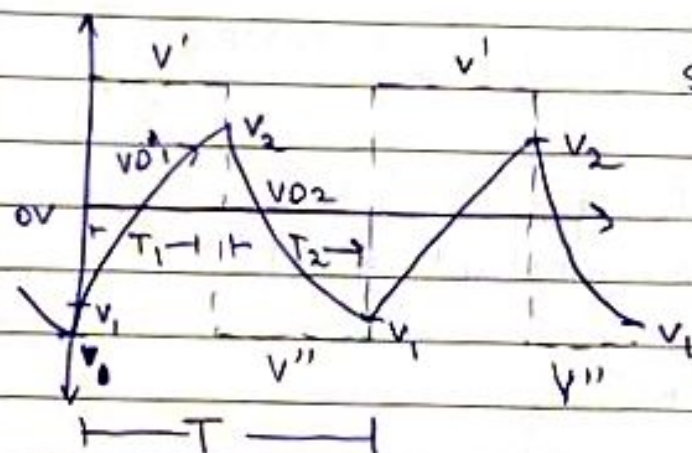
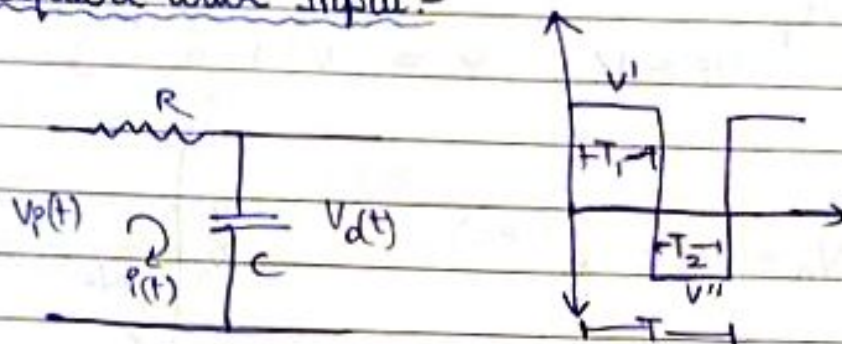
When $t_p = RC$



When $t_p \ll RC$



⑨ Square Wave Input:-



$$V_o(t) = V_f + (V_i - V_f) e^{-t/RC} \quad \boxed{\text{FIF}}$$

In accordance to Pt,

$$V_2 = V' + (V_1 - V') e^{-t_1/RC}$$

$$V_1 = V'' + (V_2 - V'') e^{-t_2/RC}$$

considering symmetrical waveform as input we have

$$V' = -V'' = \frac{V}{2}$$

$$T_1 = T_2 = \frac{T}{2}$$

$$V_2 = -V_1$$

$$\begin{aligned} V_2 &= V' + (V_1 - V') e^{-t_1/RC} \\ &= \frac{V}{2} + (-V_2 - \frac{V}{2}) e^{-t_1/2RC} \end{aligned}$$

$$V_2(1 + e^{-\frac{t}{2RC}}) = \frac{V}{2}(1 - e^{-\frac{t}{2RC}})$$

$$V_z = \frac{V}{2} \frac{(1 - e^{-t/2RC})}{(1 + e^{-t/2RC})} \left(\frac{e^{t/4RC}}{e^{t/4RC}} \right)$$

$$V_z = \frac{V}{2} \left(\frac{e^{t/4RC} - e^{-t/4RC}}{e^{t/4RC} + e^{-t/4RC}} \right)$$

$$V_z = \frac{V}{2} \left(\frac{e^x - e^{-x}}{e^x + e^{-x}} \right)$$

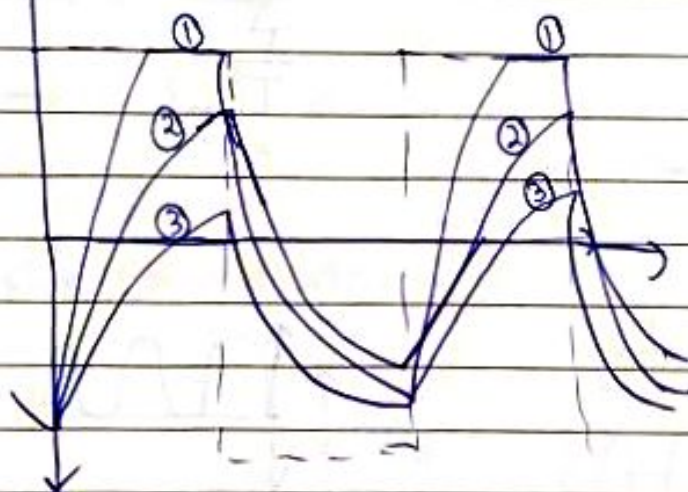
$$V_z = \frac{V \tanh x}{2}$$

$$x = \frac{t}{4RC}$$

When $RC \ll T$ ①

$RC = T$ ②

$RC \gg T$ ③



Q. An ideal 1 microsecond pulse is fed to an amplifier. Calculate & plot the output waveform,

(i) the upper 3dB fs 10MHz,

(ii) upper freq fs 1MHz

~~fs~~ $f_a =$

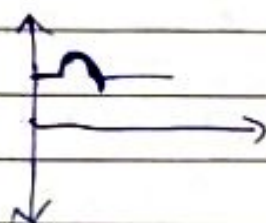
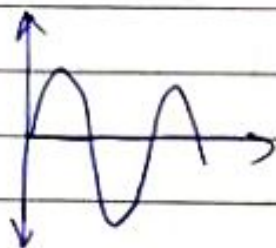
$$t_p = 1 \mu\text{s} = 10^{-6} \text{ secs}$$

$$f_a = \frac{1}{2\pi RC}$$

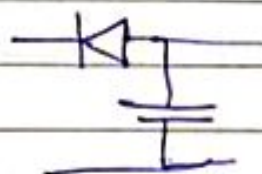
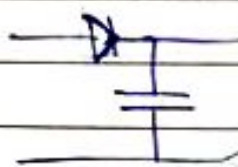
$$RC = \frac{1}{2\pi \times 10 \times 10^6} \Rightarrow \frac{1}{2\pi \times 10^7} \Rightarrow \frac{1}{6.28 \times 10^7} \Rightarrow \frac{1}{6.28} \times 10^{-8} \Rightarrow 0.159 \times 10^{-8} \Rightarrow 1.59 \times 10^{-9}$$

Clipper & Clamping :-

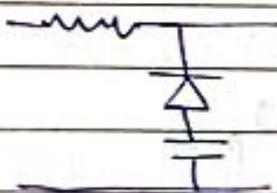
Clipper → Series
→ Shunt clipper



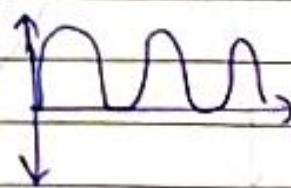
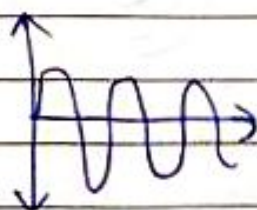
Series clipper →



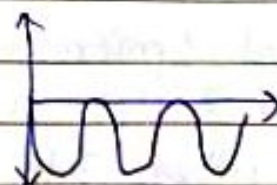
Shunt clipper →



Clamper :-



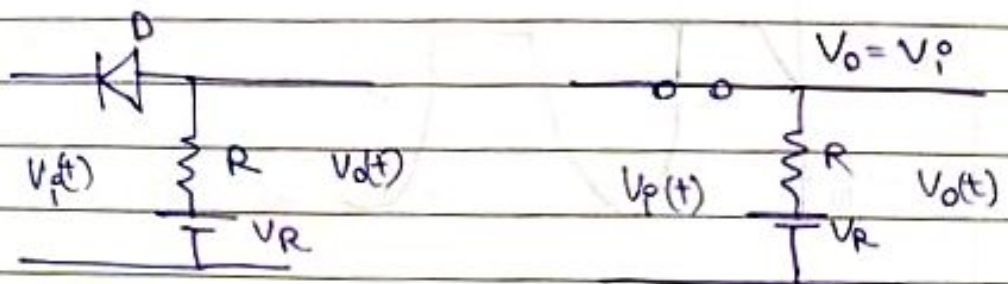
+ve clamp



-ve clamp

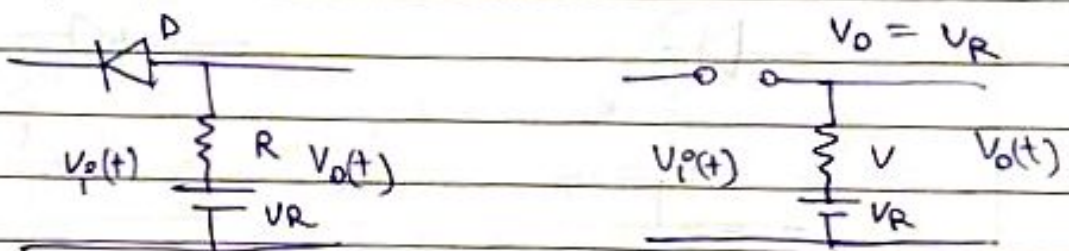
Series clipper :-

① Clipping above reference voltage :-

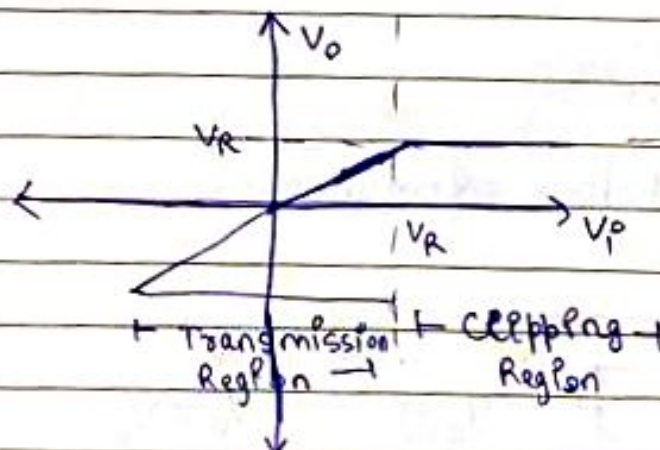


$$V_R > V_i$$

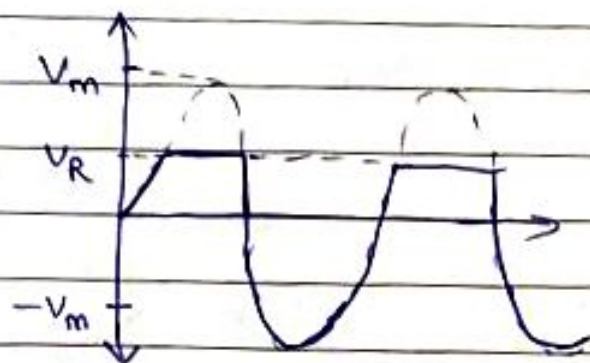
$$V_R < V_i$$



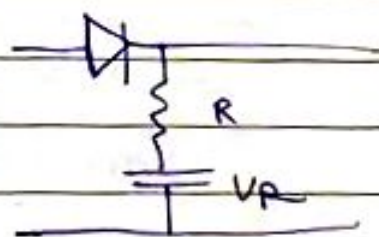
Transfer Characteristics :-



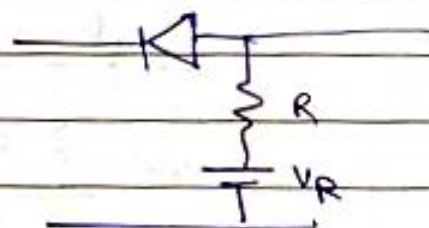
With sinusoidal i/p :-



Clipping below reference :-



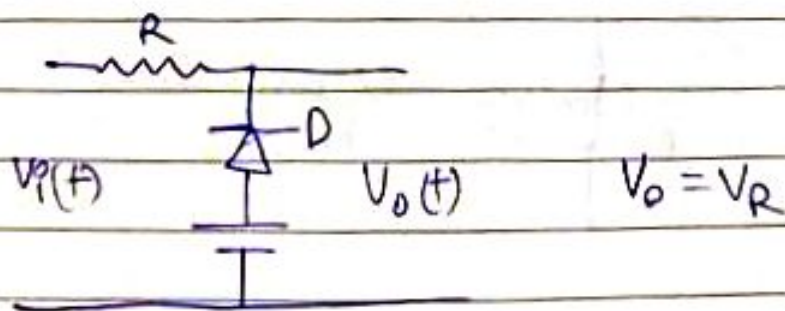
$$V_R > V_I$$



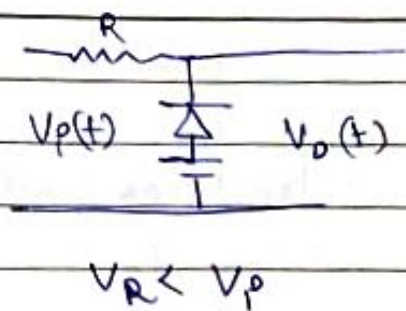
$$V_R < V_I$$

Shunt Clipper :-

Clipping below reference :-



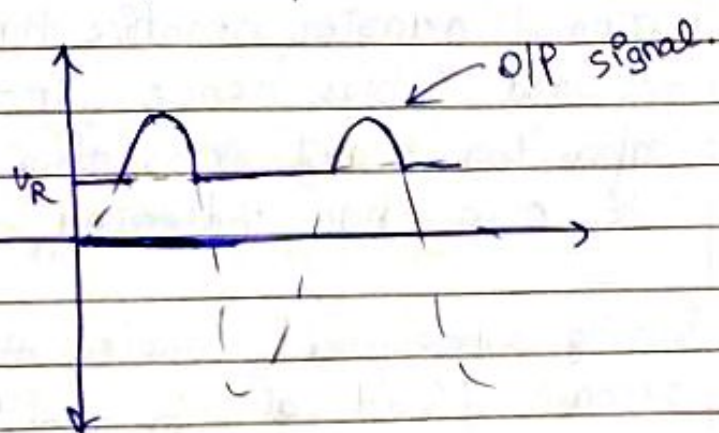
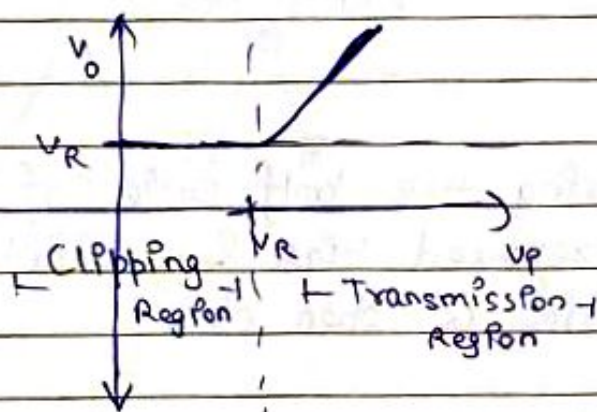
$$V_R > V_I$$



$$V_o = V_p$$

Transfer Characteristics :-

$$\begin{aligned} V_o &= V_R & V_p < V_R \\ V_o &= V_p & V_p > V_R \end{aligned}$$

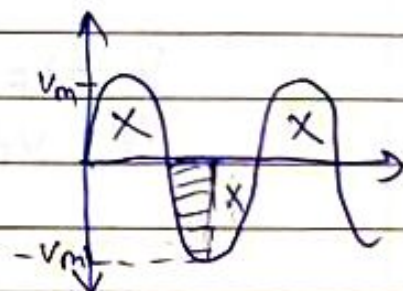


Clamper :-

Positive Clamper - This ckt introduces positive DC value to P/P signal



First open ckt diode
Second short ckt



During +ve half cycle of P/P signal the diode is reversed bias & capacitor will not charge as diode is open ckt

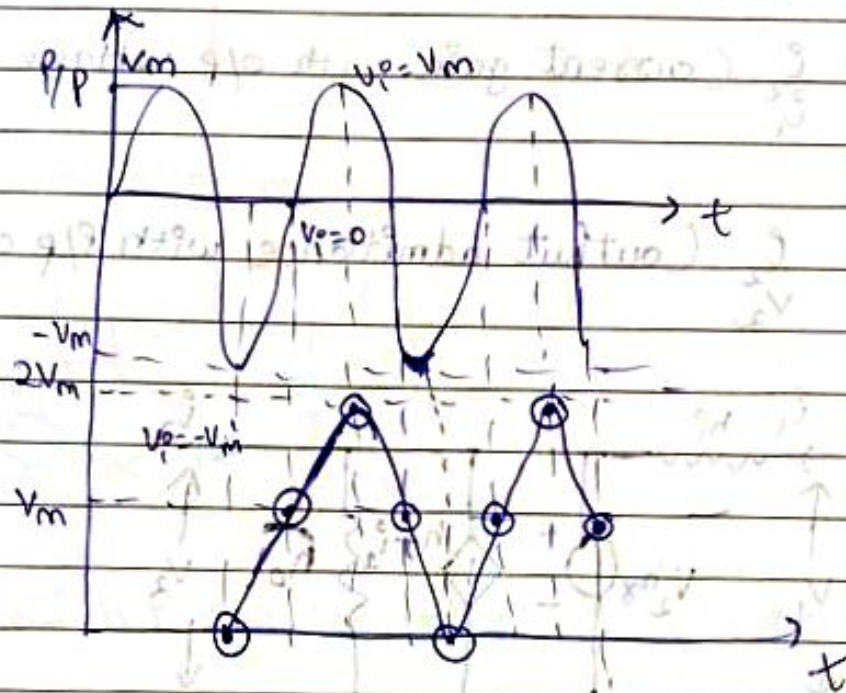
During 1st quarter negative half cycle the diode is forward bias hence short ckt ~~and~~, then capacitor start charging upto $-V_m$ where -ve sign has indicated on capacitor.

During subsequent cycle the capacitor voltage remain fixed at V_m with same polarity.

Under steady state condition,

$$V_o = V_i + V_m$$

V_i	V_o
0	V_m
$-V_m$	0
V_m	$2V_m$



Hybrid Parameter :-

$$i_c = i_c - I_c = \Delta i_c$$

i_c → Instantaneous value of varying components
 $i_c - I_c$ → Inst. total value
 I_c → Quiescent value

$I_c \rightarrow$ fixed value b/w two terminal

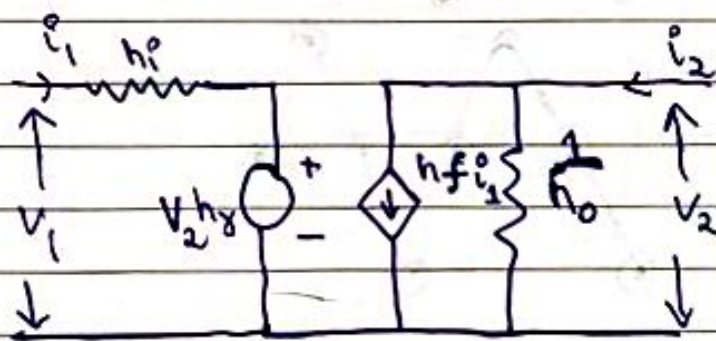
i/p current & o/p voltage - Independent term
 i/p voltage & o/p current - dependent term

(h_i) $h_{11} = \frac{V_1}{I_1}$ (input impedance with o/p voltage short ckt)

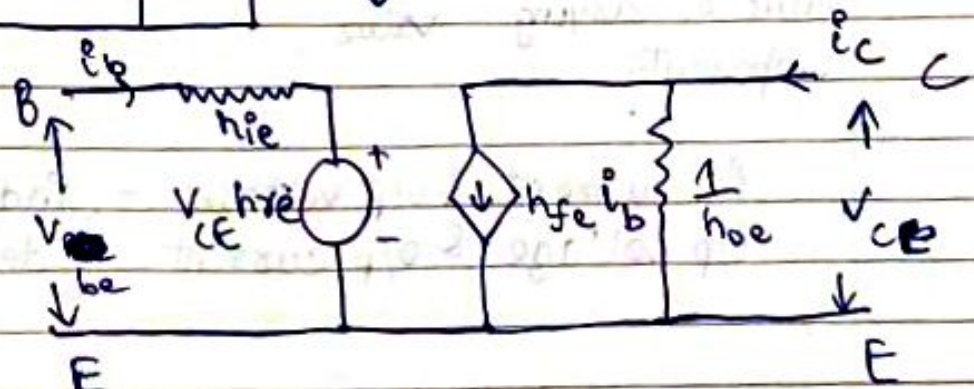
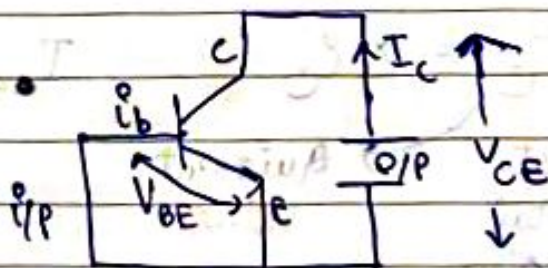
(h_r) $h_{12} = \frac{V_1}{V_2}$ (reverse voltage gain with i/p current open)

(h_f) $h_{21} = \frac{I_2}{I_1}$ (current gain with o/p voltage short ckt)

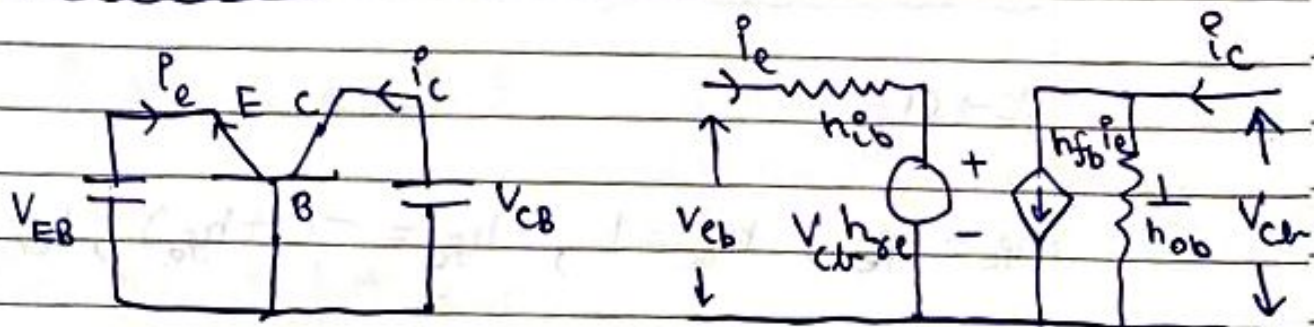
(h_o) $h_{22} = \frac{I_2}{V_2}$ (output admittance with i/p current open)



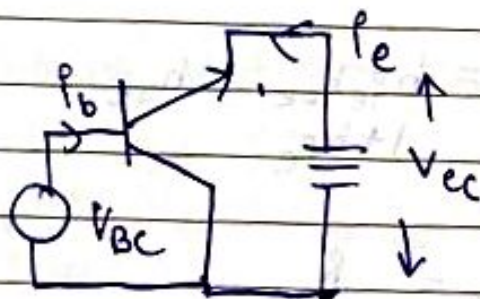
Common Emitter :-



Common Base :-



Common Collector :-



Typical h Parameter value at $I_E = 1.3 \text{ mA}$

Parameter	CE	CB	CC
h_i	1100Ω	1100Ω	2.6Ω
h_r	25×10^{-4}	1	2.9×10^{-4}
h_f	50	-51	-98
h_o	$24 \mu \text{ A/V}$	$25 \mu \text{ A/V}$	$49 \mu \text{ A/V}$
$1/h_o$	$40 \text{ k}\Omega$	$40 \text{ k}\Omega$	$2.04 \text{ M}\Omega$

Conversion Formula:-

CC \rightarrow CE

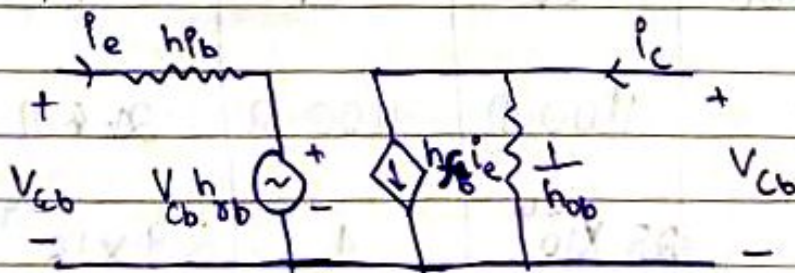
$$h_{fe} = h_{fe}', \quad h_{oc} = 1, \quad h_{fc} = -(1 + h_{fe}'), \quad h_{bc} = h_{oe}$$

CB \rightarrow CE, Same CE \rightarrow CB; just replace e with b

$$h_{ib} = \frac{h_{ie}}{1 + h_{fe}}, \quad h_{ob} = \frac{h_{ie} h_{oe}}{1 + h_{fe}} + h_{oe}$$

$$h_{fb} = -\frac{h_{fe}}{1 + h_{fe}}, \quad R_{ob} = \frac{R_{oe}}{1 + h_{fe}}$$

h_{oe} in terms of CB:-



$$h_{oe} = \left. \frac{V_{be}}{V_{ce}} \right|_{i_b=0}$$

$$= \frac{V_{bc} + V_{ce}}{V_{ce}} = \left(1 + \frac{V_{bc}}{V_{ce}} \right) \bigg|_{i_b=0}$$

Find h_{oe} in terms of CE

___/___/___

If $i_b = 0$, then $i_e = -i_c$

i_c in h_{ob} is equal to $i_c = h_{ob} V_{bc}$
 $= (1 + h_{fb}) i_e$

Apply KVL to path e-b-c

$$h_{fe} i_e + V_{cb} h_{ob} + V_{bc} + V_{ce} = 0$$

Combining last 2 eqⁿ we have,

$$\frac{h_{fe} h_{ob}}{1 + h_{fb}} V_{bc} - h_{ob} V_{bc} + V_{bc} + V_{ce} = 0$$

$$\frac{V_{bc}}{V_{ce}} = \frac{-(1 + h_{fb})}{h_{fe} h_{ob} + (1 - h_{ob})(1 + h_{fb})}$$

$$\text{Hence, } h_{oe} = 1 + \frac{V_{bc}}{V_{ce}} \Rightarrow \frac{V_{bc}}{V_{ce}} = h_{oe} - 1$$

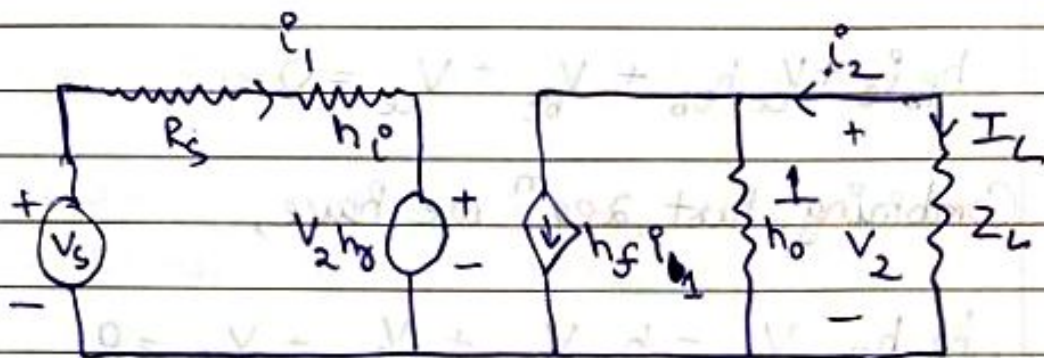
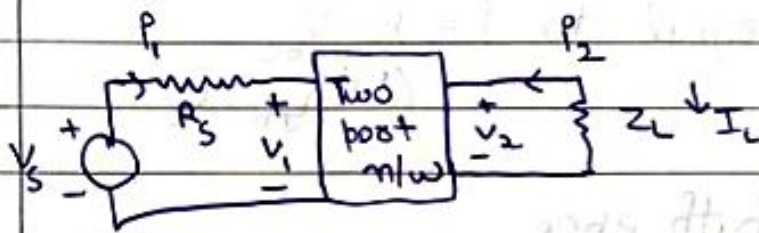
$$h_{oe} = 1 + \frac{(1 + h_{fb})}{h_{fe} h_{ob} + (1 - h_{ob})(1 + h_{fb})}$$

$$h_{oe} = \frac{h_{fe} h_{ob} - h_{ob}(1 + h_{fb})}{h_{fe} h_{ob} + (1 - h_{ob})(1 + h_{fb})} \rightarrow \text{Exact expression}$$

As, $h_{ob} \ll 1$ & $h_{fe} h_{ob} \ll (1 + h_{fb})$

$$\text{So, } h_{oe} \approx \frac{h_{fe} h_{ob}}{1 + h_{fb}} - h_{ob} \rightarrow \text{Simple approx formula}$$

Analysis of Transistor n/w using h-parameter :-



Current gain, $A_I = \frac{-I_2}{I_1} = -(h_f I_1 + h_o V_2)$

$$V_2 = \frac{I_2 Z_L}{-I_2 Z_L}$$

$$A_I = -(h_f I_1 + h_o I_2 Z_L)$$

$$\Rightarrow \frac{I_2 (-h_f I_1 + h_o Z_L)}{I_1}$$

$$\Rightarrow \frac{I_2}{I_1} = \frac{-h_f}{1 + h_o Z_L}$$

Input Impedance :-

$$Z_i = \frac{V_1}{I_1} = \frac{h_i I_1 + h_r V_2}{I_1}$$

$$Z_i = h_i + h_r \frac{V_2}{I_1}$$

$$= h_i + h_r \frac{A_i I_1 Z_L}{I_1}$$

$$\frac{Z_i}{Z_L} = \frac{h_i + A_i h_r Z_L}{Z_L}$$

$$\left\{ \frac{1}{Z_L} = Y_L \right\}$$

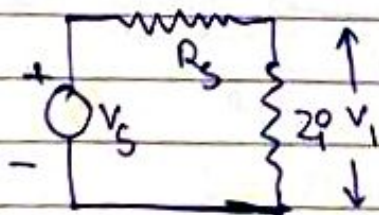
$$Z_i = h_i + \frac{h_r h_f}{Y_L + h_o}$$

voltage gain :-

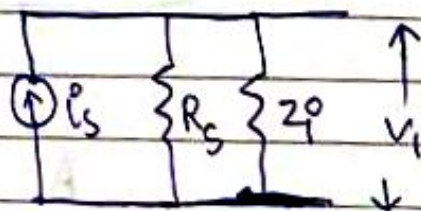
$$A_v = \frac{V_2}{V_1} = \frac{A_i I_1 Z_L}{V_1} \quad \& \quad V_2 = A_i I_1 Z_L$$

$$A_v = \frac{A_i Z_L}{Z_i}$$

Overall gain :-



Thevenin's
Eq.



Norton's
Eq.

$$AV_s = \frac{V_2}{V_s} = \frac{V_2}{V_1} \frac{V_1}{V_s} \Rightarrow A_v \frac{V_1}{V_s}$$

$$V_1 = \frac{V_s Z_o}{R_s + Z_o} \quad \text{So, } AV_s = A_v \frac{V_s Z_o}{(R_s + Z_o) V_s}$$

$$AV_s = \frac{A_v Z_o}{R_s + Z_o}$$

$$\overline{AV_s} = A_o \frac{Z_o}{R_s + Z_o}$$

$$A_{p_s} = -\frac{e_2}{e_s} = -\frac{e_2}{e_1} \frac{e_1}{e_s} = +A_o \frac{e_1}{e_s}$$

$$A_{p_s} = A_o \frac{e_1}{e_s} \left\{ e_1 = e_s \frac{R_s}{R_s + Z_o} \right\}$$

$$A_{p_s} = A_o \frac{R_s}{R_s + Z_o}$$

$$R_s = \infty$$

$$A_{p_s} = A_o$$

Hence A_{p_s} is current gain for ideal current source.

$$A_{V_S} = A_{I_S} \left(\frac{Z_L}{R_S} \right)$$

~~$$\frac{A_{V_S}}{A_{I_S}} = \frac{Z_L}{R_S}$$~~

Output Admittance :-

$$Z_0 = \frac{1}{Y_0}$$

$$Y_0 = \frac{I_2}{V_2} Y_0$$

$$Y_0 = h_f \frac{I_1}{V_2} + h_o$$

Put $V_S = 0$, So,

$$R_S I_1 + h_i I_1 + h_r V_2 = 0$$

$$\frac{I_1}{V_2} = \frac{-h_r}{h_i + R_S}$$

$$Y_0 = h_o - \frac{h_f h_r}{h_i + R_S}$$

Small signal analysis of a transistor :-

$$A_i = \frac{-h_f}{1 + h_o Z_L}$$

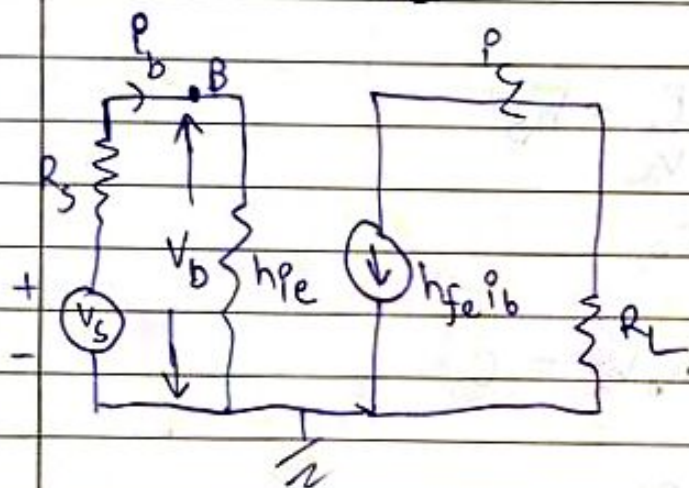
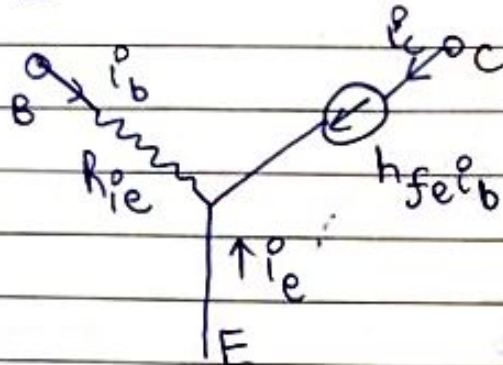
$$Z_0 = h_i + h_r A_v Z_L$$

$$A_v = \frac{A_i Z_L}{Z_0}$$

$$Y_0 = h_o - \frac{h_f h_r}{h_i + R_S}$$

Simplified Hybrid Model :-

$h_{oe} R_L < 1 \rightarrow$ Condition



Approx model

$$A_I = -h_{fe}$$

$$R_i^o = h_{ie} + h_{re} A_i^o R_L$$

$$= h_{ie} \left(1 + \frac{h_{re} A_i^o R_L}{h_{ie}} \right)$$

$$= h_{ie} \left(1 + \frac{h_{re} h_{fe} A_i^o h_{oe} R_L}{h_{ie} h_{oe} h_{fe}} \right)$$

$$A_s = \frac{h_{fe} h_{re}}{h_{ie} h_{oe}} \approx -5$$

$$R_i = R_{ie}$$

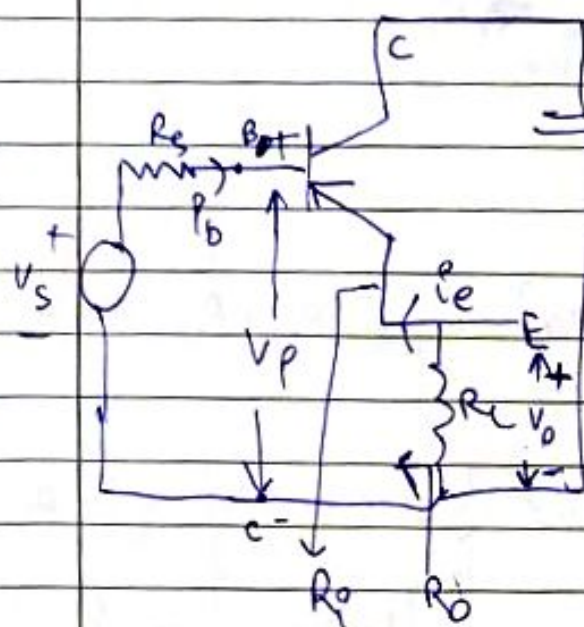
$$A_v = A_p \frac{R_L}{R_o} = -h_{fe} \frac{R_L}{h_{ie}}$$

$$R_L = \infty \text{ if } V_s = 0V$$

Summary for approx eqⁿ $h_{oe}(R_o + R_L) < 1$

	CE	CE with R_i	CC	CB
A_v	$-h_{fe}$	$-h_{fe}$	$1+h_{fe}$	$-h_{fb}$
R_o	h_{ie}	$h_{ie} + (1+h_{fe})R_e$	$h_{ie} + (1+h_{fe})R_e$	h_{ib}
A_v	$\frac{-h_{fe} R_L}{R_o}$	$-\frac{R_L}{R_o}$	$1 - \frac{h_{ie}}{R_o}$	$\frac{h_{fe} R_L}{h_{ie}}$
R_o	∞	∞	$\frac{R_s + h_{ie}}{1+h_{fe}}$	∞
R_o' $(\frac{1}{R_o})_{or}$	R_L	R_L	$R_o R_L$	R_L

Emitter Follower:-



$$A_v \approx 1$$

R_i = impedance input

R_o = impedance output

Change in base voltage appears as an equal change across the load. Hence emitter follower follows the signal.

Both A_i & A_v are real & positive

R_i is very high

R_o is very low

It is used as a buffer stage for resistance transformation from high to low resistance over a wide range of freq having voltage gain unity.

It also the power level of signal & no ~~phase~~ ^{phase} shift b/w i/p & o/p

$$A_i = -\frac{I_e}{I_b} = -\frac{h_{fe}}{1+h_{fe}R_E}$$

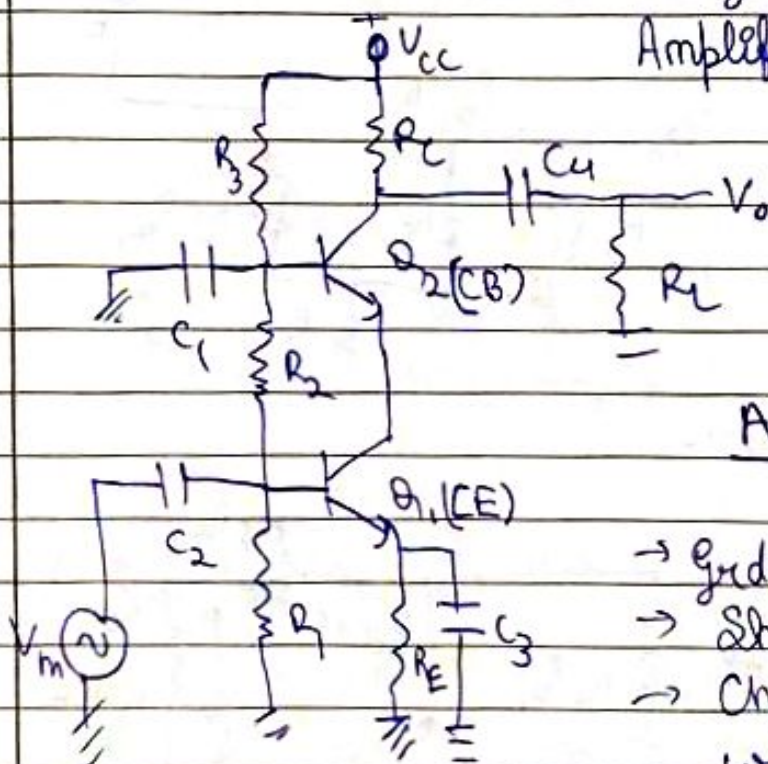
$$R_o = h_{ie} + h_{re} A_v R_L = h_{ie} + A_v R_L$$

$$A_v = \frac{A_v R_L}{R_o} = \frac{R_o - h_{ie}}{R_L} = 1 - \frac{h_{ie}}{R_L}$$

$$Y_o = h_{oc} + \frac{1 + h_{fe}}{h_{ie} + R_L}$$

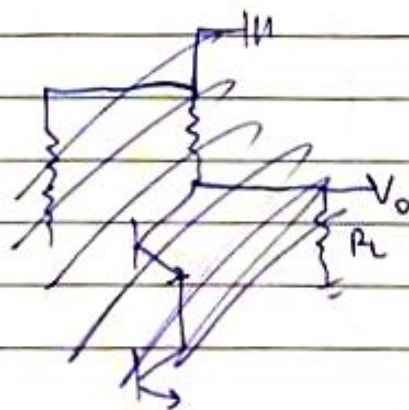
Cascade Amplifier:-

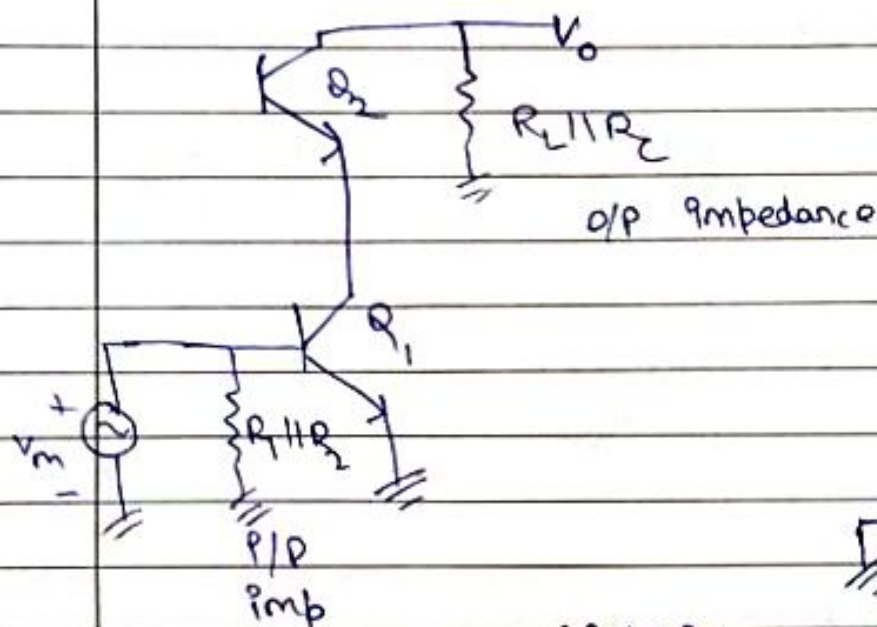
Multistage
Amplifier



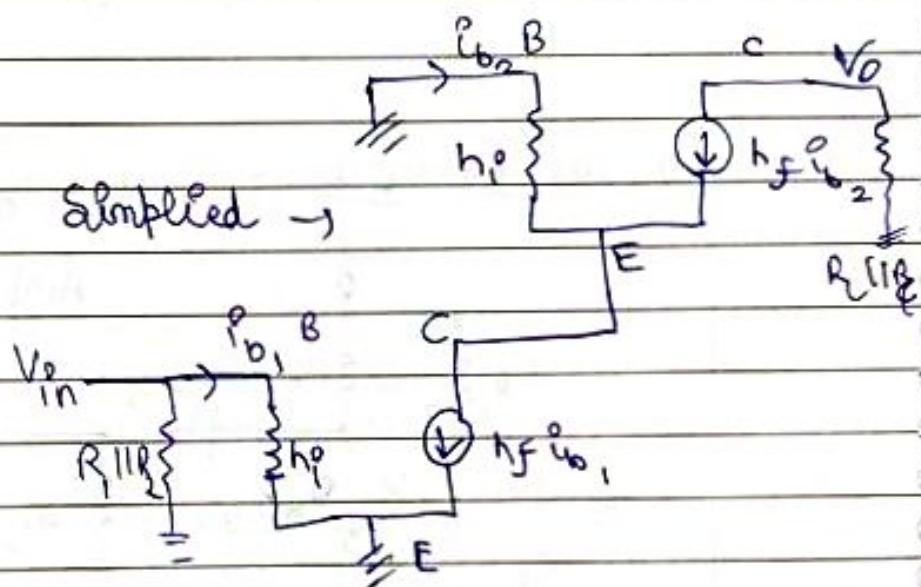
AC Analysis :-

- Gnd the DC compound
- Short ckt the capacitor
- Change the transistor with approx diagram

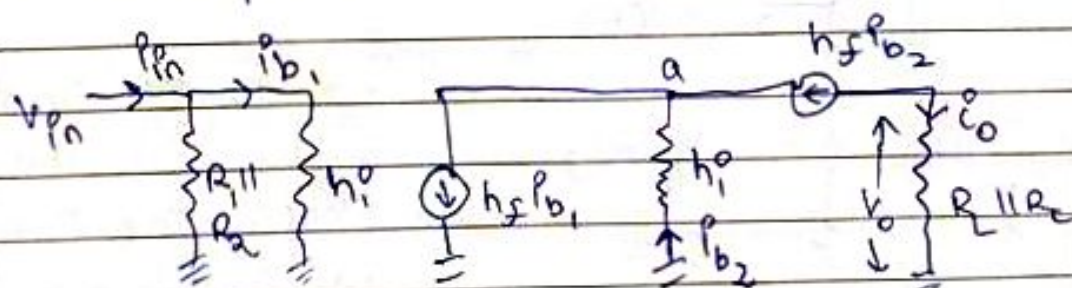




Simplified \rightarrow



In same plane :-



$$R_p = R_1 \parallel R_2 \parallel h_{i1}$$

$$R_o = R_2 \parallel R_L$$

//_

$$i_o = -h_f i_{b2}$$

Apply nodal at a ,

$$h_f i_{b1} = h_f i_{b2} + i_{b2}$$

$$h_f i_{b1} = i_{b2} (1 + h_f)$$

$$i_{b2} = \frac{h_f i_{b1}}{(1 + h_f)}$$

$$V_o = i_o (R_L \parallel R_c)$$

$$i_{b1} = \frac{V_{in}}{h_{i1}}, \quad i_{b2} = \frac{h_f V_{in}}{h_{i1} (1 + h_f)} = \cancel{h_f i_{b1} (R_L \parallel R_c)}$$

$$i_o = -h_f \left(\frac{h_f V_{in}}{h_{i1} (1 + h_f)} \right) = \frac{-h_f^2 V_{in}}{h_{i1} (1 + h_f)}$$

$$V_o = \frac{-h_f^2 V_{in}}{h_{i1} (1 + h_f)} (R_L \parallel R_c)$$

$$\frac{V_o}{V_{in}} = A_v = \frac{-h_f^2 (R_L \parallel R_c)}{h_{i1} (1 + h_f)}$$

$$i_{b2} = \frac{h_f i_{in} (R_L \parallel R_c)}{h_{i1} (1 + h_f)}$$

$$i_o = -h_f h_f i_{in} (R_L \parallel R_c) = \frac{P_o}{P_{in}} = \overset{A_v^2}{\frac{-h_f^2 (R_L \parallel R_c)}{h_{i1} (1 + h_f)}}$$

So final current gain,

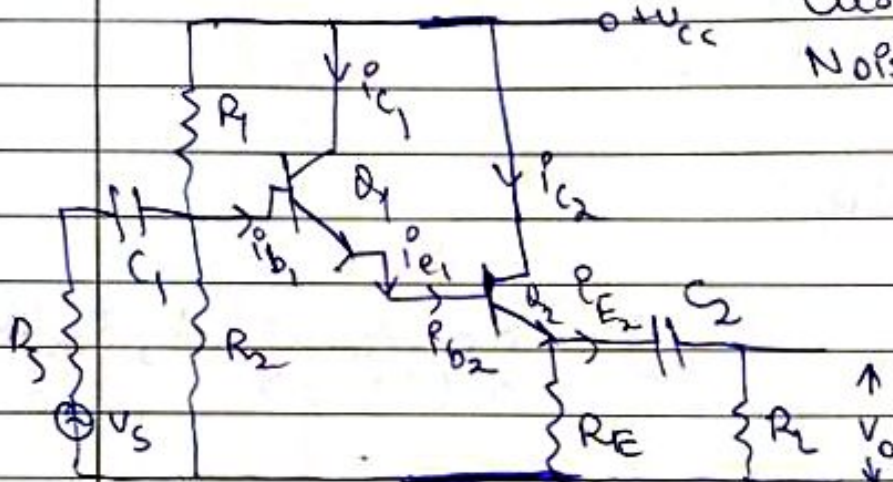
$$A_i^o = \frac{-h_f^2 (R_1 \parallel R_2)}{(1+h_f)(R_1 \parallel R_2 + h_i^o)}$$

Darlington pair:-

β Multiplier

Current Gain Max

Noisy, Heat dissipation is more

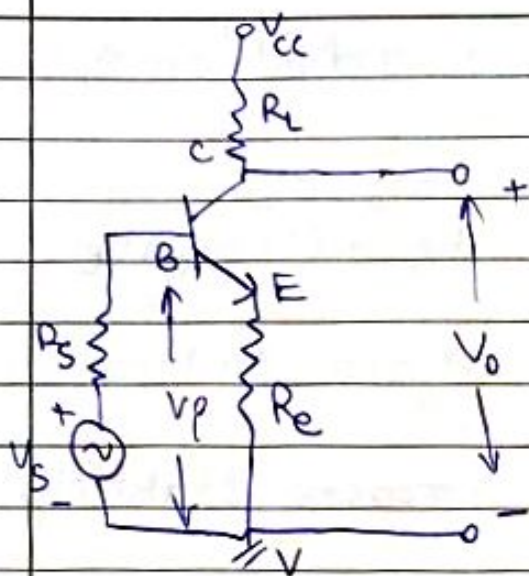


As we know $A_i^o = \frac{P_{e2}}{P_{b1}}$ for Q_1

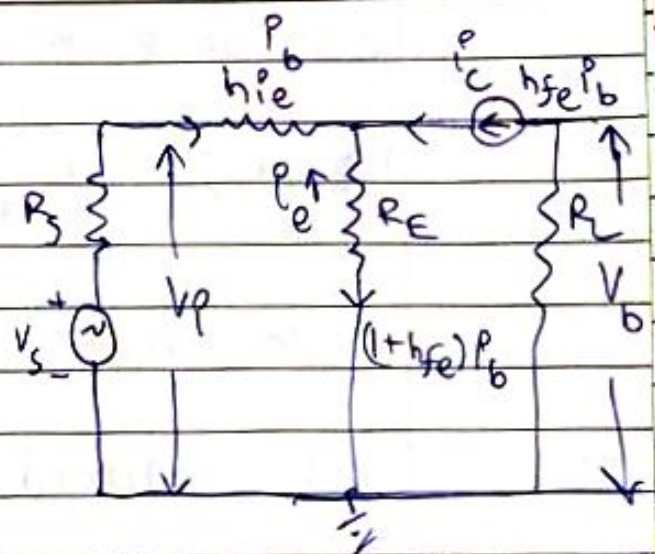
$$\frac{P_e}{P_{e1}} = \frac{\beta P_{b1}}{P_{b2}} \text{ So, } A_i^o = \frac{\beta_2 P_{b1}}{P_{b2}} = \beta_2 \frac{P_{e1}}{P_{b1}}$$

$$A_i^o = \beta_2 \beta_1 \frac{P_{e1}}{P_{b1}} = \beta_2 \beta_1$$

Common emitter with emitter resistance :-



CE amp^r with emitter resist.



Approx small signal eq. ckt

$$A_i^o = \frac{-I_o^o}{I_b^o} = -\frac{h_{fe} I_b^o}{I_b^o} \Rightarrow -h_{fe}$$

~~Current~~ Current gain equal the short ckt value & is unaffected by addition of R_E

$$R_i = \frac{V_i^o}{I_b^o} = I_b^o h_{ie} + \frac{(1+h_{fe}) I_b^o R_E}{I_b^o}$$

$$= I_b^o (h_{ie} + 1 + h_{fe} R_E)$$

$$\rightarrow R_i = h_{ie} + 1 + h_{fe} R_E$$

$$\rightarrow R_i = R_E + h_{fe} R_E + h_{ie}$$

From Pt can be concluded input

//_

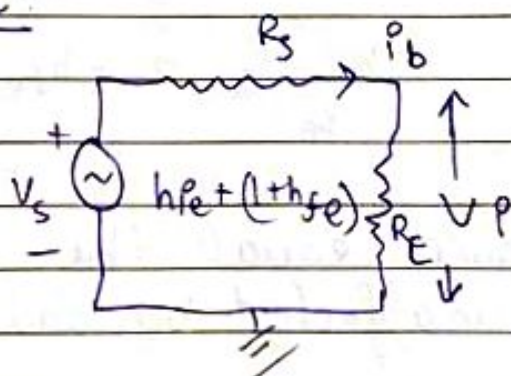
resistance P_s mostly dependant on R_E .

$$A_v = \frac{A_p R_L}{R_p} = \frac{-h_{fe} R_L}{h_{ie} + (1+h_{fe})R_E}$$

or $A_v \cong -\frac{R_L}{R_E}$. Hence voltage gain
got reduced by increases stability also.

Looking into Base & Emitter of Transistor:-

Base:-



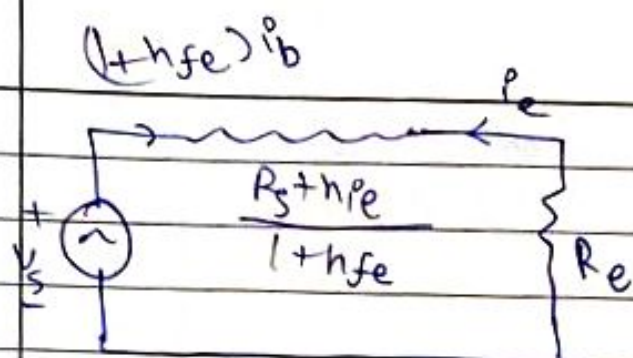
$$I_b = \frac{V_s}{R_s + h_{ie} + (1+h_{fe})R_E}$$

Emitter to Gnd voltage P_s ,

$$V_e = (1+h_{fe}) I_b R_E$$

$$\rightarrow \frac{V_s R_E}{(R_s + h_{ie}) / (1+h_{fe}) + R_E}$$

Looking
from
emitter



$$i_e = -\frac{V_e}{R_e}$$

$$i_b = \frac{-i_e}{1+h_{fe}}$$