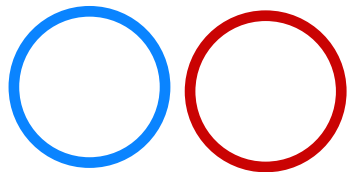
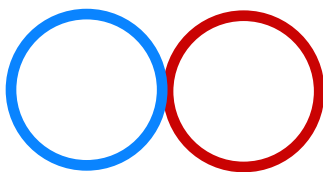


a dc b



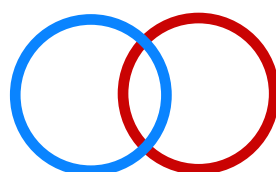
disconnected

a ec b



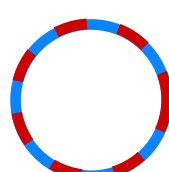
externally
connected

a po b



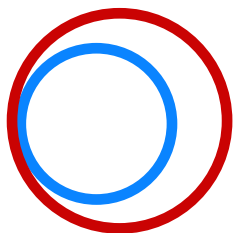
partially
overlapping

a eq b



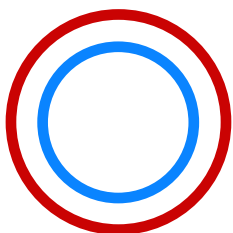
equal

a tpp b



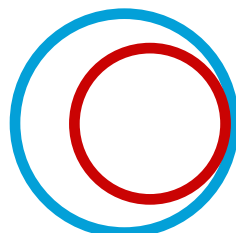
tangential
proper part

a nttp b



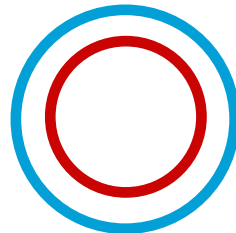
non-tangential
proper part

a tppi b



tangential proper
part inverse

a ntppi b



non-tangential
proper part inverse